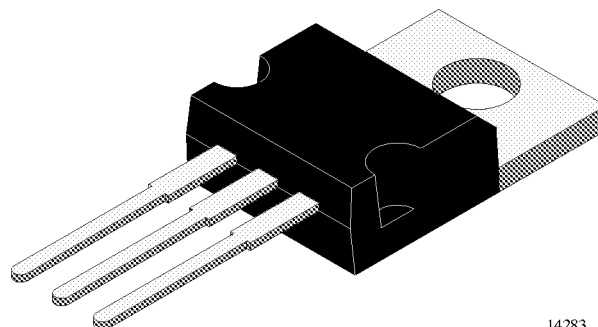


Silicon NPN High Voltage Switching Transistor

Features

- Simple-sWitch-Off Transistor (SWOT)
- HIGH SPEED technology
- Planar passivation
- 100 kHz switching rate
- Very low switching losses
- Very low dynamic saturation
- Very low operating temperature
- Optimized RBSOA
- High reverse voltage



14283

Applications

Electronic lamp ballast circuits

Absolute Maximum Ratings

$T_{case} = 25^{\circ}C$, unless otherwise specified

Parameter	Test Conditions	Symbol	Value	Unit
Collector-emitter voltage		V_{CEO}	600	V
		V_{CES}	1200	V
Emitter-base voltage		V_{EBO}	9	V
Collector current		I_C	3	A
Collector peak current		I_{CM}	5	A
Base current		I_B	2	A
Base peak current		I_{BM}	4	A
Total power dissipation	$T_{case} \leq 25^{\circ}C$	P_{tot}	50	W
Junction temperature		T_j	150	$^{\circ}C$
Storage temperature range		T_{stg}	-65 to +150	$^{\circ}C$

Maximum Thermal Resistance

$T_{case} = 25^{\circ}C$, unless otherwise specified

Parameter	Test Conditions	Symbol	Value	Unit
Junction case		R_{thJC}	2.5	K/W

Electrical Characteristics

$T_{\text{case}} = 25^{\circ}\text{C}$, unless otherwise specified

Parameter	Test Conditions	Symbol	Min	Typ	Max	Unit
Collector cut-off current	$V_{\text{CE}} = 1200 \text{ V}$	I_{CES}			100	μA
	$V_{\text{CE}} = 1200 \text{ V}; T_{\text{case}} = 150^{\circ}\text{C}$	I_{CES}			1	mA
Collector-emitter breakdown voltage (figure 1)	$I_{\text{C}} = 300 \text{ mA}; L = 125 \text{ mH}; I_{\text{measure}} = 100 \text{ mA}$	$V_{(\text{BR})\text{CEO}}$	550			V
Emitter cut-off current	$V_{\text{EB}} = 9 \text{ V}$	$I_{(\text{EBO})}$			10	μA
Collector-emitter saturation voltage	$I_{\text{C}} = 0.5 \text{ A}; I_{\text{B}} = 0.13 \text{ A}$	V_{CEsat}			0.25	V
	$I_{\text{C}} = 1.5 \text{ A}; I_{\text{B}} = 0.5 \text{ A}$	V_{CEsat}			0.5	V
Base-emitter saturation voltage	$I_{\text{C}} = 0.5 \text{ A}; I_{\text{B}} = 0.13 \text{ A}$	V_{BEsat}			1	V
	$I_{\text{C}} = 1.5 \text{ A}; I_{\text{B}} = 0.5 \text{ A}$	V_{BEsat}			1.1	V
DC forward current transfer ratio	$V_{\text{CE}} = 2 \text{ V}; I_{\text{C}} = 10 \text{ mA}$	h_{FE}	15			
	$V_{\text{CE}} = 2 \text{ V}; I_{\text{C}} = 0.5 \text{ A}$	h_{FE}	12			
	$V_{\text{CE}} = 2 \text{ V}; I_{\text{C}} = 1.5 \text{ A}$	h_{FE}	6			
	$V_{\text{CE}} = 5 \text{ V}; I_{\text{C}} = 3 \text{ A}$	h_{FE}	4			
Collector-emitter working voltage	$I_{\text{C}} = 3 \text{ A}; I_{\text{B1}} = 1 \text{ A}; -I_{\text{B2}} = 0.3 \text{ A}$	V_{CEW}	650			V
Dynamic saturation voltage	$I_{\text{C}} = 1.5 \text{ A}; I_{\text{B}} = 0.3 \text{ A}; t = 1 \mu\text{s}$	V_{CEsatdyn}			15	V
	$I_{\text{C}} = 1.5 \text{ A}; I_{\text{B}} = 0.3 \text{ A}; t = 3 \mu\text{s}$	V_{CEsatdyn}			7.5	V

$T_{\text{case}} = 25^{\circ}\text{C}$, unless otherwise specified

Figure 1. Test circuit for $V_{(BR)CE0}$

94 8852

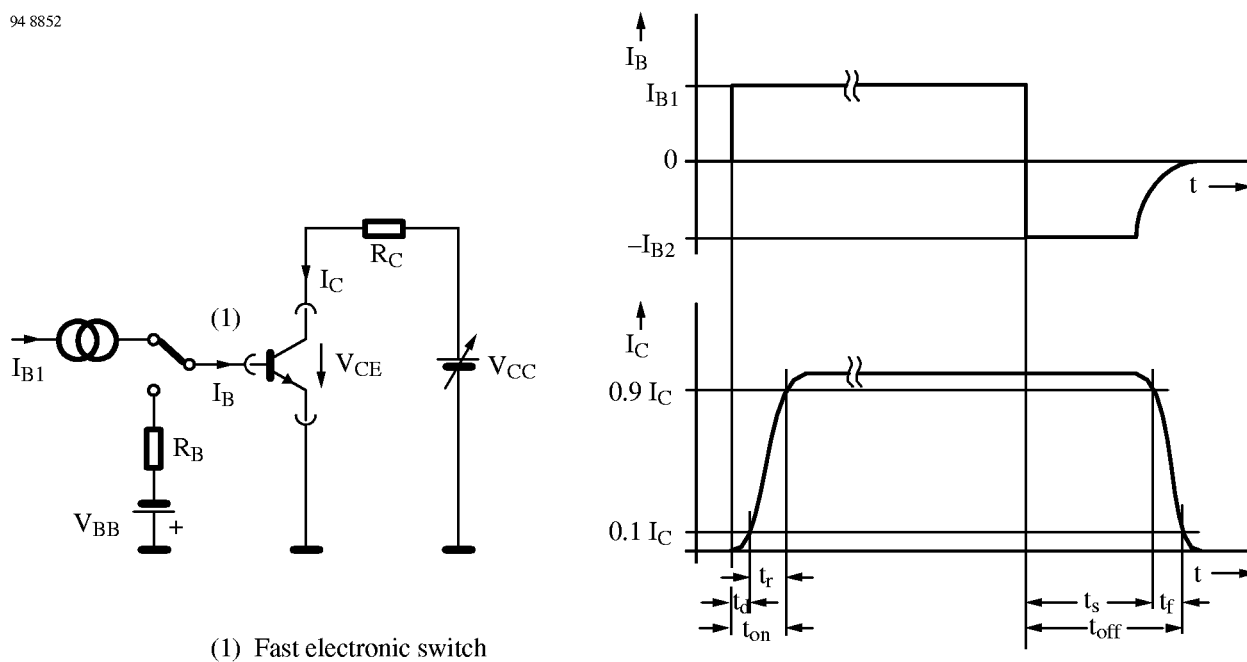


Figure 2. Test circuit for switching characteristics – resistive load

94 8853

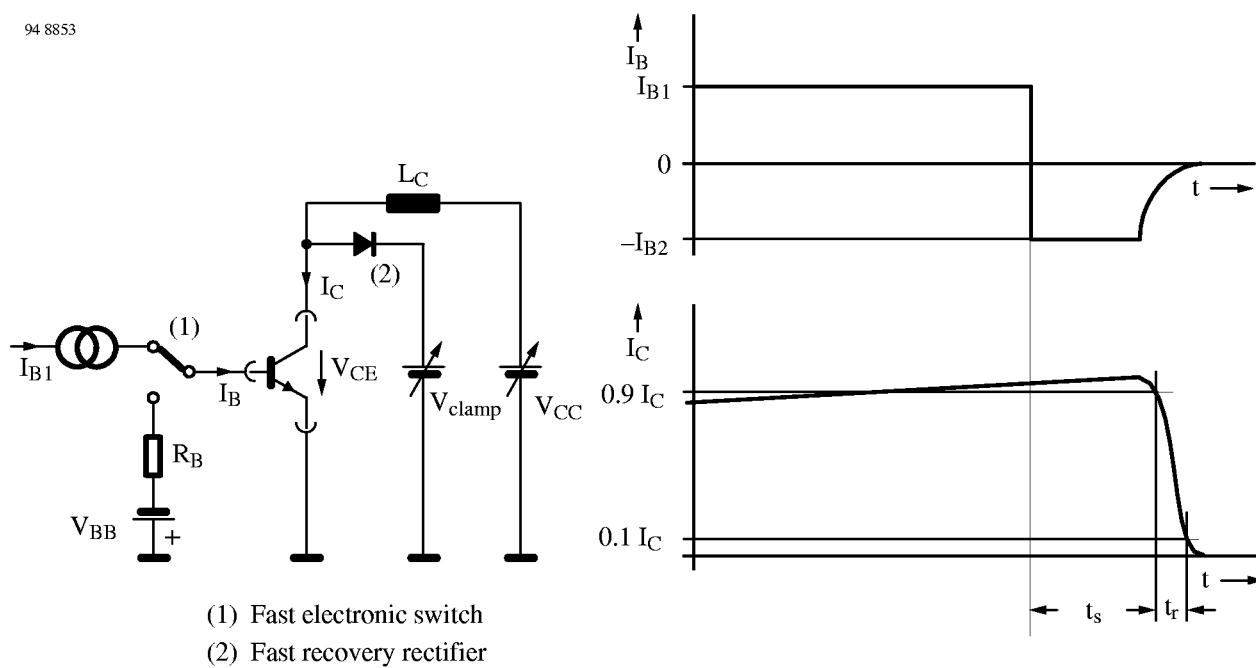
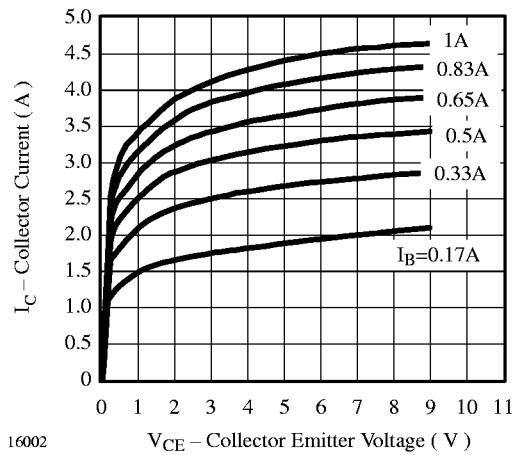
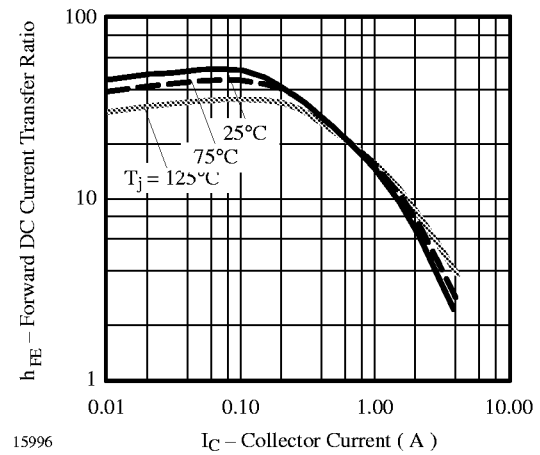
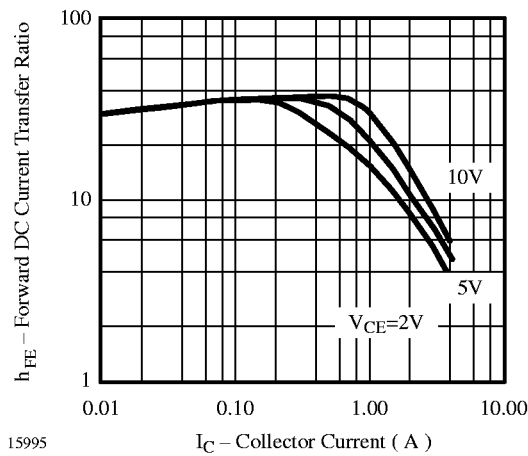
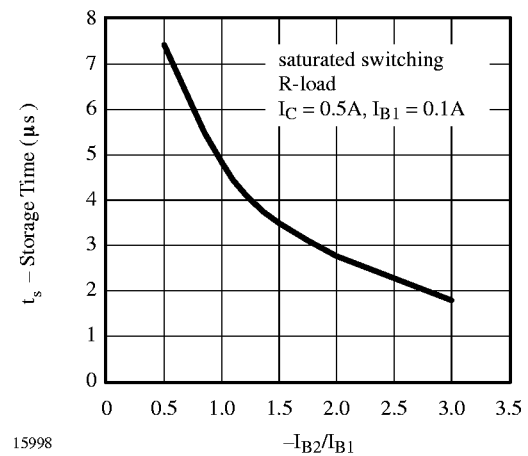
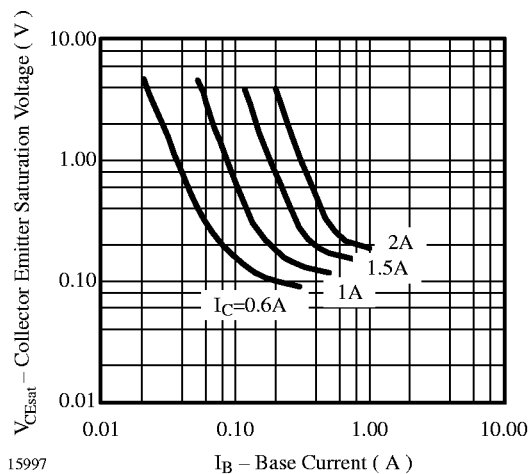
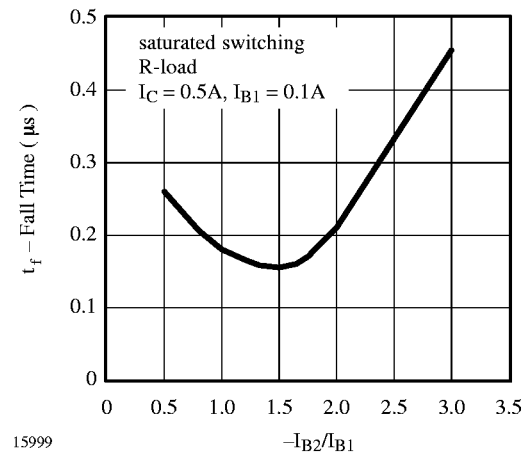
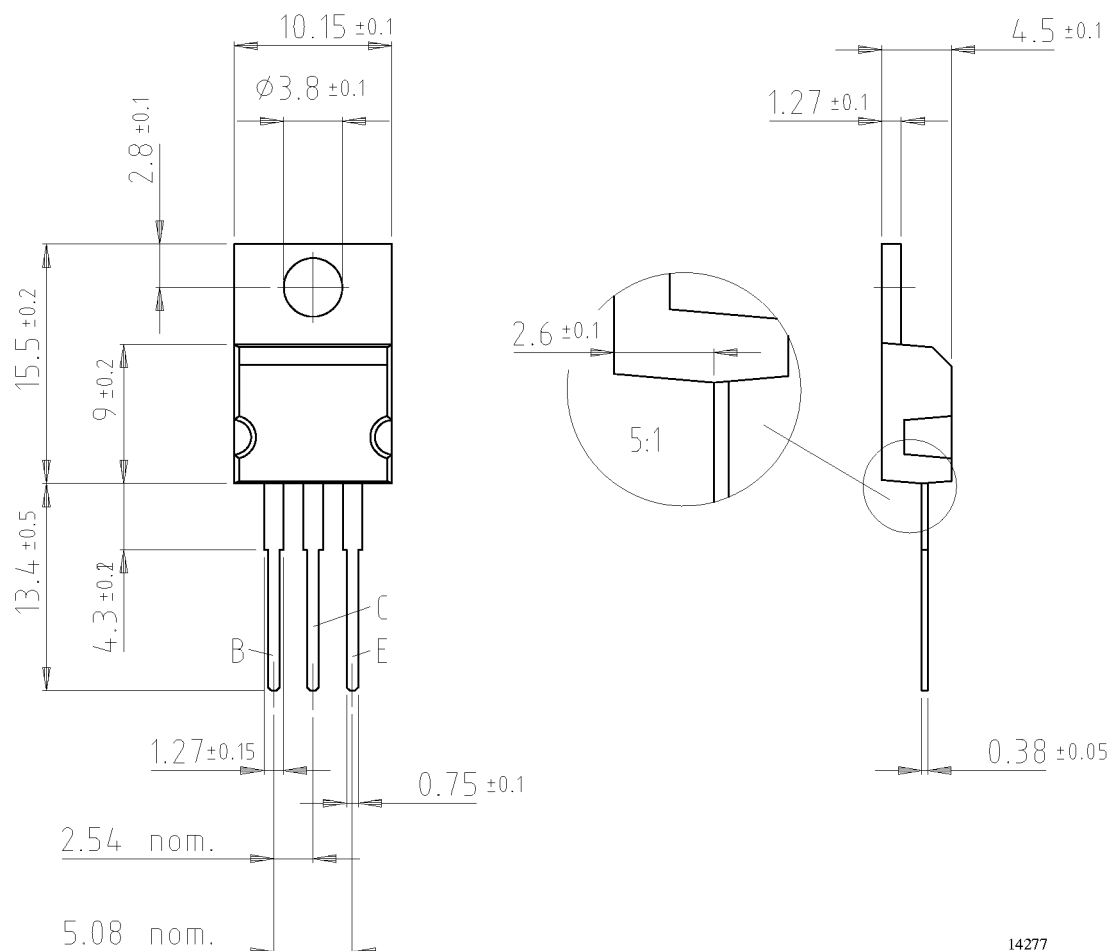


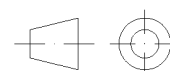
Figure 3. Test circuit for switching characteristics – inductive load

Typical Characteristics ($T_{\text{case}} = 25_{\text{C}}$ unless otherwise specified)

 Figure 4. I_C vs. V_{CE}

 Figure 7. h_{FE} vs. I_C

 Figure 5. h_{FE} vs. I_C

 Figure 8. t_s vs. $-I_{B2}/I_{B1}$

 Figure 6. V_{CEsat} vs. I_B

 Figure 9. t_f vs. $-I_{B2}/I_{B1}$

Dimensions in mm



Plastic case JEDEC TO 220
Collector connected with metallic surface



technical drawings
according to DIN
specifications

14277