

MBRS4060CT





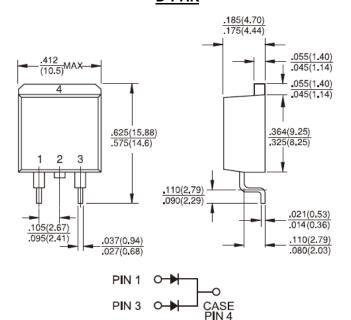


Features

- ♦ For surface mounted application
- Plastic material used carriers Underwriters Laboratory Classigication 94V-0
- ♦ Metal to silicon junction, majority carrier conduction
- ♦ Low power loss, high efficiency
- ♦ High current capability, low forward voltage drop
- High surge current capability
- For use in low voltage, high frequency inverters, free wheeling, and polarity protection applications
- ♦ Guarding for overvoltage protection
- Green compound with suffix "G" on packing code & prefix "G" on datecode

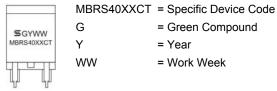
Mechanical Data

- ♦ Case: JEDEC D²PAK Molded plastic
- Terminal: Pure tin plated, lead free, solderable per MIL-STD-750, Method 2026
- Polarity: As markedMounting position: Any
- ♦ Weight: 1.53 gram



Dimensions in inches and (millimeters)

Marking Diagram



Maximum Ratings and Electrical Characteristics

Rating at 25 $^{\circ}$ C ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load.

For capacitive load, derate current by 20%

Type Number	Symbol	MBRS4060CT	Unit
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	60	V
Maximum RMS Voltage	V_{RMS}	42	V
Maximum DC Blocking Voltage	V_{DC}	60	V
Maximum Average Forward Rectified Current	I _{F(AV)}	40	Α
Peak Forward Surge Current, 8.3 ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	I _{FSM}	300	А
Maximum Instantaneous Forward Voltage (Note 1) IF=20A @ 25° C IF=20A @ 125° C IF=40A @ 25° C IF=40A @ 125° C	V _F	0.8 0.7 1.0 0.9	V
Maximum Instantaneous Reverse Current T_A =25 $^{\circ}$ C at Rated DC Blocking Voltage Per Leg T_A =125 $^{\circ}$ C	I _R	0.1 20	mA
Voltage Rate of Change (Rated V _R)	dV/dt	10000	V/uS
Typical Thermal Resistance	$R_{\theta jC}$	1.5	°C/W
Operating Temperature Range	T _J	- 65 to + 150	оС
Storage Temperature Range	T _{STG}	- 65 to + 175	οС

Note 1: Pulse Test: 300us Pulse Width, 1% Duty Cycle



RATINGS AND CHARACTERISTIC CURVES (MBRS4060CT)

