

74ALCX16244

Low-Voltage 16-Bit Buffer/Line Driver with 5V Tolerant Inputs and Outputs

General Description

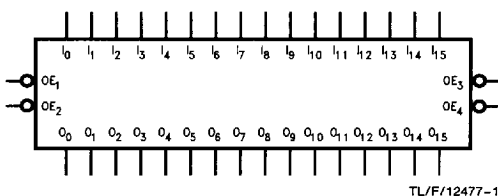
The ALCX16244 contains sixteen non-inverting buffers with TRI-STATE® outputs designed to be employed as a memory and address driver, clock driver, or bus oriented transmitter/receiver. The device is nibble controlled. Each nibble has separate TRI-STATE control inputs which can be shorted together for full 16-bit operation.

The ALCX family of devices excel in bus interface applications where very high speeds and low power consumption are required. ALCX devices are capable of interfacing to the latest high-speed busses while consuming less than 20 μ A of quiescent current. In keeping with National's *CROSSVOLT*™ philosophy, ALCX inputs and outputs are 5V tolerant allowing them to interface to both 3V and 5V components. ALCX inputs and outputs also power up/down in the high impedance state, facilitating power management and live insertion system features. Bus hold on all input, I/O, and control pins removes the need for power-hungry pull-up resistors on TRI-STATE® busses. ± 24 mA output drive means ALCX devices can drive all but the heaviest bus and backplane loads quietly due to National's patented Quiet Series™ circuitry.

Features

- 3.6 ns t_{PD} max, 20 μ A I_{CCQ} max
- 5V tolerant inputs and outputs
- Power up/down high impedance inputs and outputs
- Supports live insertion/withdrawal
- Supports power management
- 2.0V–3.6V V_{CC} supply operation
- ± 24 mA output drive
- Bus hold
- Implements patented Quiet Series noise/EMI reduction circuitry
- Functionally compatible with the 74 series 16244
- Latch-up performance exceeds 500 mA
- ESD performance:
 - Human body model > 2000V
 - Machine model > 200V

Logic Symbol

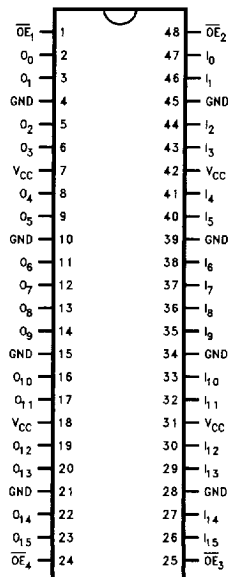


Pin Names	Description
$\overline{OE}_n$	Output Enable Input (Active Low)
I_0-I_{15}	Inputs
O_0-O_{15}	Outputs

	SSOP	TSSOP
Order Number	74ALCX16244MEA 74ALCX16244MEAX	74ALCX16244MTD 74ALCX16244MTDX
See NS Package Number	MS48A	MTD48

Connection Diagram

Pin Assignment for
SSOP and TSSOP



Functional Description

The ALCX16244 contains sixteen non-inverting buffers with TRI-STATE standard outputs. The device is nibble (4 bits) controlled with each nibble functioning identically, but independent of the other. The control pins can be shorted together to obtain full 16-bit operation. The TRI-STATE out-

puts are controlled by an Output Enable ($\overline{OE}_n$) input for each nibble. When $\overline{OE}_n$ is LOW, the outputs are in 2-state mode. When $\overline{OE}_n$ is HIGH, the outputs are in the high impedance mode, but this does not interfere with entering new data into the inputs.

Truth Tables

Inputs		Outputs
$\overline{OE}_1$	I ₀ -I ₃	O ₀ -O ₃
L	L	L
L	H	H
H	X	Z

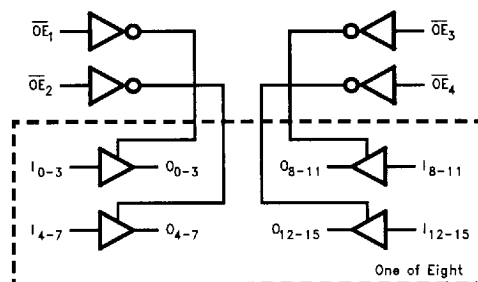
Inputs		Outputs
$\overline{OE}_2$	I ₄ -I ₇	O ₄ -O ₇
L	L	L
L	H	H
H	X	Z

Inputs		Outputs
$\overline{OE}_3$	I ₈ -I ₁₁	O ₈ -O ₁₁
L	L	L
L	H	H
H	X	Z

Inputs		Outputs
$\overline{OE}_4$	I ₁₂ -I ₁₅	O ₁₂ -O ₁₅
L	L	L
L	H	H
H	X	Z

H = High Voltage Level
L = Low Voltage Level
X = Immaterial
Z = High Impedance

Logic Diagram



TL/F/12477-3

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Symbol	Parameter	Conditions	Value	Units
V_{CC}	Supply Voltage		-0.5 to +7.0	V
V_I	DC Input Voltage		-0.5 to +7.0	V
V_O	DC Output Voltage	Output in TRI-STATE	-0.5 to +7.0	V
		Output in High or Low State (Note 2)	-0.5 to $V_{CC} + 0.5$	V
I_{IK}	DC Input Diode Current	$V_I < GND$	-50	mA
I_{OK}	DC Output Diode Current	$V_O < GND$	-50	mA
		$V_O > V_{CC}$	+50	mA
I_O	DC Output Source/Sink Current		± 50	mA
I_{CC}	DC Supply Current per Supply Pin		± 100	mA
I_{GND}	DC Ground Current per Ground Pin		± 100	mA
T_{STG}	Storage Temperature		-65 to +150	°C

Note 1: The Absolute Maximum Ratings are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the Absolute Maximum Ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Note 2: I_O Absolute Maximum Rating must be observed.

Recommended Operating Conditions

Symbol	Parameter	Min	Max	Units
V_{CC}	Supply Voltage	Operating	2.0	3.6
		Data Retention	1.5	3.6
V_I	Input Voltage	0	5.5	V
V_O	Output Voltage	HIGH or LOW State	0	V_{CC}
		TRI-STATE	0	5.5
I_{OH}/I_{OL}	Output Current	$V_{CC} = 3.0V - 3.6V$	± 24	mA
		$V_{CC} = 2.7V$	± 12	
T_A	Free-Air Operating Temperature	-40	85	°C
$\Delta t/\Delta V$	Input Edge Rate, $V_{IN} = 0.8V - 2.0V$, $V_{CC} = 3.0V$	0	10	ns/V

DC Electrical Characteristics

Symbol	Parameter	Conditions	V_{CC} (V)	$T_A = -40^\circ C$ to $+85^\circ C$		Units
				Min	Max	
V_{IH}	HIGH Level Input Voltage		2.7-3.6	2.0		V
V_{IL}	LOW Level Input Voltage		2.7-3.6		0.8	V
V_{OH}	HIGH Level Output Voltage	$I_{OH} = -100 \mu A$	2.7-3.6	$V_{CC} - 0.2$		V
		$I_{OH} = -12 mA$	2.7	2.2		V
		$I_{OH} = -18 mA$	3.0	2.4		V
		$I_{OH} = -24 mA$	3.0	2.2		V
V_{OL}	LOW Level Output Voltage	$I_{OL} = 100 \mu A$	2.7-3.6		0.2	V
		$I_{OL} = 12 mA$	2.7		0.4	V
		$I_{OL} = 16 mA$	3.0		0.4	V
		$I_{OL} = 24 mA$	3.0		0.55	V
I_I	Input Leakage Current	$V_I = 0V$ or $5.5V$	2.7-3.6		± 5.0	μA

DC Electrical Characteristics (Continued)

Symbol	Parameter	Conditions	V _{CC} (V)	T _A = -40°C to +85°C		Units
				Min	Max	
I _I (HOLD)	Bushold Leakage Current	V _I = 0.8V	3.0	-75		μA
		V _I = 2.0V	3.0	75		
I _I (OD)	Bushold Overdrive Current		3.0	±500		μA
I _{OZ}	TRI-STATE Output Leakage	0 ≤ V _O ≤ 5.5V V _I = V _{IH} or V _{IL}	2.7-3.6		±5.0	μA
I _{OFF}	Power-Off Leakage Current	0 ≤ V _I , V _O ≤ 5.5V	0		10	μA
I _{CC}	Quiescent Supply Current	V _I = V _{CC} or GND	2.7-3.6		20	μA
		3.6V ≤ V _I , V _O ≤ 5.5V	2.7-3.6		±20	μA
ΔI _{CC}	Increases in I _{CC} per Input	V _{IH} = V _{CC} - 0.6V	2.7-3.6		500	μA

AC Electrical Characteristics

Symbol	Parameter	T _A = -40°C to +85°C				Units
		V _{CC} = 3.3V ±0.3V		V _{CC} = 2.7V		
		Min	Max (Note 3)	Min	Max (Note 3)	
t _{PHL}	Propagation Delay	1.5	3.6	1.5	4.5	ns
t _{PLH}	Data to Output	1.5	3.6	1.5	4.5	
t _{PZL}	Output Enable Time	1.5	5.0	1.5	5.5	ns
t _{PZH}		1.5	5.0	1.5	5.5	
t _{PLZ}	Output Disable Time	1.5	5.0	1.5	5.5	ns
t _{PHZ}		1.5	5.0	1.5	5.5	
t _{OSHL}	Output to Output Skew (Note 4)		0.5			ns
t _{OSLH}			0.5			

Note 3: The Maximum AC limits are design target. Actual performance will be specified upon completion of characterization.

Note 4: Skew is defined as the absolute value of the difference between the actual propagation delay for any two separate outputs of the same device. The specification applies to any outputs switching in the same direction, either HIGH to LOW (t_{OSSL}) or LOW to HIGH (t_{OSLH}).

Dynamic Switching Characteristics

Symbol	Parameter	Conditions	V _{CC} (V)	T _A = 25°C	Units
				Typical	
V _{OLP}	Quiet Output Dynamic Peak V _{OL}	C _L = 50 pF, V _{IH} = 3.3V, V _{IL} = 0V	3.3	0.8	V
V _{OLV}	Quiet Output Dynamic Valley V _{OL}	C _L = 50 pF, V _{IH} = 3.3V, V _{IL} = 0V	3.3	0.8	V

Capacitance

Symbol	Parameter	Conditions	Typical	Units
C _{IN}	Input Capacitance	V _{CC} = Open, V _I = 0V or V _{CC}	7	pF
C _O	Output Capacitance	V _{CC} = 3.3V, V _I = 0V or V _{CC}	8	pF
C _{PD}	Power Dissipation Capacitance	V _{CC} = 3.3V, V _I = 0V or V _{CC} , F = 10 MHz	20	pF