T-52-07

August 1991

54BCT/74BCT244
Octal Buffer/Line Driver with TRI-STATE® Outputs

General Description

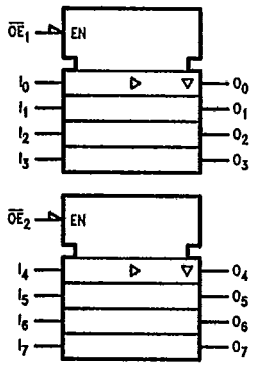
The 'BCT244 is an octal buffer and line driver with TRI-STATE outputs designed to be employed as a memory and address driver, clock driver, or bus-oriented transmitter/receiver.

Features

- Non-inverting buffers
- Low I_{CCZ} through BiCMOS techniques
- TRI-STATE outputs drive bus lines
- Output sink capability of 64 mA
- Source capability of 15 mA
- Guaranteed output skew
- Guaranteed multiple output switching specifications
- Guaranteed 4000V minimum ESD protection
- Guaranteed latchup protection
- Nondestructive hot insertion capability
- High impedance in power down (I_{ZZ} and V_{ID})

Logic Symbol

IEEE/IEC



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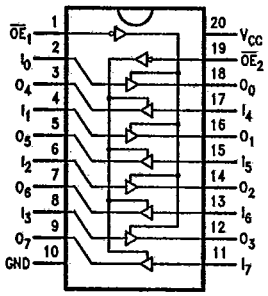
Connection Diagrams

Pin Assignment for DIP, SOIC and Flatpak



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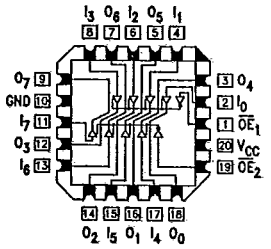
Truth Table

$\overline{OE}_1$	I_{0-3}	O_{0-3}	$\overline{OE}_2$	I_{4-7}	O_{4-7}
H	X	Z	H	X	Z
L	H	H	L	H	H
L	L	L	L	L	L

H = HIGH Voltage Level
L = LOW Voltage Level
X = Immaterial
Z = High Impedance

Pin Names	Description
$\overline{OE}_1$, $\overline{OE}_2$	Output Enable Input (Active Low)
I_0–I_7	Inputs
O_0–O_7	Outputs

Pin Assignment for LCC



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Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Storage Temperature	−65°C to +150°C
Ambient Temperature under Bias	−55°C to +125°C
Junction Temperature under Bias	
Ceramic	−55°C to +175°C
Plastic	−55°C to +150°C
V_{CC} Pin Potential to Ground Pin	−0.5V to +7.0V
Input Voltage (Note 2)	−0.5V to +7.0V
Input Current (Note 2)	−30 mA to +5.0 mA
Voltage Applied to Any Output	
in the Disable or Power-Off State	−0.5V to +5.5V
in the High State	−0.5V to V_{CC}
Current Applied to Output in LOW State (Max)	twice the rated I_{OL} (mA)
ESD Last Passing Voltage (Min)	4000V
DC Latchup Source or Sink Current	500 mA
Over Voltage Latchup	V_{CC} + 4.5V

Note 1: Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Note 2: Either voltage limit or current limit is sufficient to protect inputs.

Recommended Operating Conditions

Free Air Ambient Temperature	
Military	−55°C to +125°C
Commercial	0°C to +70°C
Supply Voltage	
Military	+4.5V to +5.5V
Commercial	+4.5V to +5.5V

DC Electrical Characteristics

Symbol	Parameter	54BCT/74BCT Min	Typ	Max	Units	V_{CC}	Conditions
V_{IH}	Input HIGH Voltage	2.0			V		Recognized as a HIGH Signal
V_{IL}	Input LOW Voltage			0.8	V		Recognized as a LOW Signal
V_{CD}	Input Clamp Diode Voltage			−1.2	V	Min	I_{IN} = −18 mA
V_{OH}	Output HIGH Voltage 54BCT/74BCT	2.4			V	Min	I_{OH} = −3 mA
	54BCT	2.0					I_{OH} = −12 mA
	74BCT	2.0					I_{OH} = −15 mA
V_{OL}	Output LOW Voltage 54BCT			0.55	V	Min	I_{OL} = 48 mA
	74BCT			0.55			I_{OL} = 64 mA
I_{IH}	Input HIGH Current			5	μA	Max	V_{IN} = 2.7V
I_{BVI}	Input HIGH Current Breakdown Test			7	μA	Max	V_{IN} = 7.0V
I_{IL}	Input LOW Current			−250	μA	Max	V_{IN} = 0.5V
I_{OS}	Output Short-Circuit Current	−100		−225	mA	Max	V_{OUT} = 0V
I_{OZH}	Output Leakage Current			20	μA	Max	V_{OUT} = 2.7V
I_{OZL}	Output Leakage Current			−20	μA	Max	V_{OUT} = 0.5V
I_{CEX}	Output HIGH Leakage Current			50	μA	Max	V_{OUT} = V_{CC}
V_{ID}	Input Leakage Test	4.75			V	0.0	I_{ID} = 1.9 μA All Other Pins Grounded
I_{ZZ}	Bus Drainage Test			100	μA	0.0	V_{OUT} = 5.25V
I_{CCH}	Power Supply Current		22	38	mA	Max	V_O = HIGH
I_{CCL}	Power Supply Current		41	66	mA	Max	V_O = LOW
I_{CCZ}	Power Supply Current		5	10	mA	Max	V_O = HIGH Z

AC Electrical Characteristics

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Symbol	Parameter	54/74BCT $T_A = +25°C$ $V_{CC} = +5.0V$ $C_L = 50$ pF			54BCT $T_A = Mil$ $V_{CC} = Mil$ $C_L = 50$ pF		74BCT $T_A = Com$ $V_{CC} = Com$ $C_L = 50$ pF		Units
		Min	Typ	Max	Min	Max	Min	Max	
t_{PLH} t_{PHL}	Propagation Delay Data to Output	1.2 1.7	2.8 3.2	4.4 5.0			0.7 1.4	5.0 5.5	ns
t_{PZH} t_{PZL}	Output Enable Time	2.0 2.0	6.0 6.2	7.8 8.1			2.0 2.0	8.7 8.9	ns
t_{PHZ} t_{PLZ}	Output Disable Time	1.0 1.0	3.8 5.8	6.7 7.8			1.0 1.0	7.7 8.9	ns
t_{OSHL} (Note 1)	Pin to Pin Skew HL Data to Output			0.75				1.0	ns
t_{OSLH} (Note 1)	Pin to Pin Skew LH Data to Output			0.70				1.0	ns
t_{OST} (Note 1)	Pin to Pin Skew LH/HL Data to Output			3.8				4.5	ns
t_{PV} (Note 2)	Device to Device Skew LH/HL Data to Output			3.8				4.8	ns

Note 1: Skew is defined as the absolute value of the difference between the actual propagation delays for any two separate outputs of the same device. The specification applies to any outputs switching HIGH to LOW (t_{OSHL}), LOW to HIGH (t_{OSLH}), or any combination switching LOW to HIGH and/or HIGH to LOW (t_{OST}). This specification is guaranteed but not tested.

Note 2: Propagation delay variation for a given set of conditions (i.e., temperature and V_{CC}) from device to device. This specification is guaranteed but not tested.

Extended AC Electrical Characteristics—Preliminary

Symbol	Parameter	74BCT $T_A = Com$ $V_{CC} = Com$ $C_L = 50$ pF 8 Outputs Switching (Note 3)		54BCT $T_A = Mil$ $V_{CC} = Mil$ $C_L = 50$ pF 8 Outputs Switching (Note 3)		74BCT $T_A = Com$ $V_{CC} = Com$ $C_L = 250$ pF (Note 4)		54BCT $T_A = Mil$ $V_{CC} = Mil$ $C_L = 250$ pF (Note 4)		Units
		Min	Max	Min	Max	Min	Max	Min	Max	
t_{PLH} t_{PHL}	Propagation Delay Data to Output	3.0 3.0	6.5 6.0			3.0 3.0	7.7 6.7			ns
t_{OSHL} (Note 1)	Pin to Pin Skew HL Data to Output									ns
t_{OSLH} (Note 1)	Pin to Pin Skew LH Data to Output									ns
t_{OST} (Note 1)	Pin to Pin Skew LH/HL Data to Output									ns
t_{PV} (Note 2)	Device to Device Skew LH/HL Data to Output									ns

Note 1: Skew is defined as the absolute value of the difference between the actual propagation delays for any two separate outputs of the same device. This specification applies to any outputs switching HIGH to LOW (t_{OSHL}), LOW to HIGH (t_{OSLH}), or any combination switching LOW to HIGH and/or HIGH to LOW (t_{OST}). This specification is guaranteed but not tested.

Note 2: Propagation delay variation for a given set of conditions (i.e., temperature, V_{CC}) from device to device. This specification is guaranteed but not tested.

Note 3: This specification is guaranteed but not tested. The limits apply to propagation delays for all paths described switching in phase (i.e., all low-to-high, high-to-low, etc.)

Note 4: These specifications guaranteed but not tested. The limits represent propagation delays with 250 pF load capacitors in place of the 50 pF load capacitors in the standard AC load. This specification pertains to single output switching only.

Capacitance

Symbol	Parameter	Typ	Units	Conditions
C_{IN}	Input Capacitance	6	pF	V_{CC} = 5.0V
C_{OUT}	Output Pin Capacitance	11	pF	V_{CC} = 5.0V

Ordering Information

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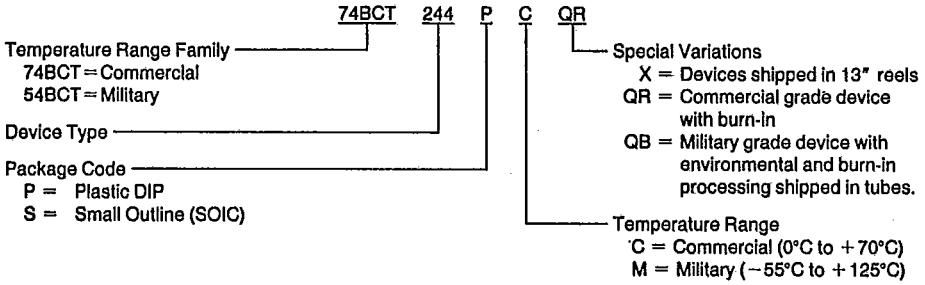
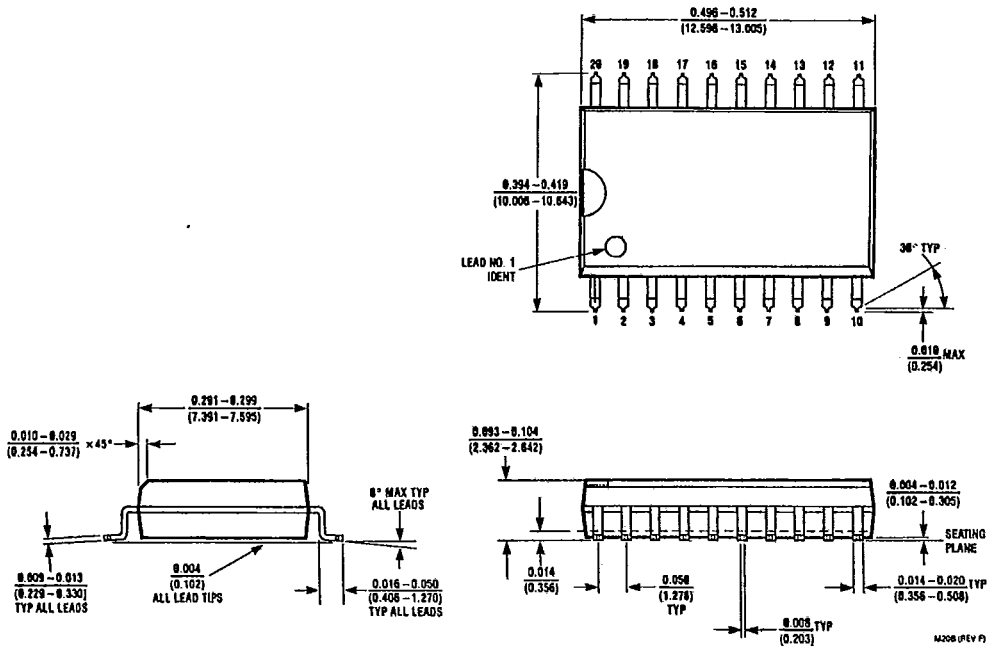
The device number is used to form part of a simplified purchasing code where the package type and temperature range are defined as follows:

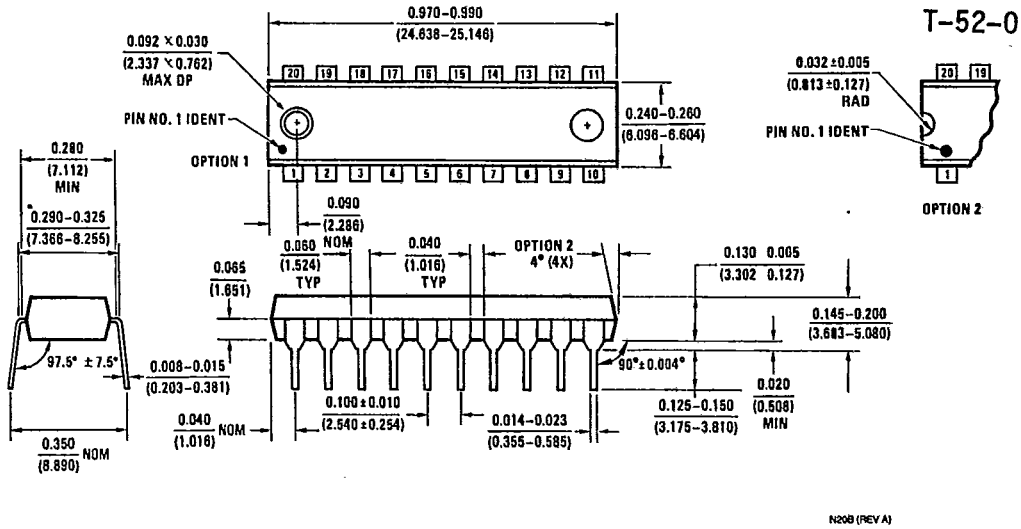


Physical Dimensions inches (millimeters)



20-Lead Small Outline Integrated Circuit (S)
NS Package Number M20B

Physical Dimensions inches (millimeters) (Continued)



20-Lead Plastic Dual-In-Line Package (P)
NS Package Number N20B

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