

100352

Low Power 8-Bit Buffer with Cut-Off Drivers

General Description

The 100352 contains an 8-bit buffer, individual inputs (D_n), outputs (Q_n), and a data output enable pin ($\overline{OEN}$). A Q output follows its D input when the $\overline{OEN}$ pin is LOW. A HIGH on $\overline{OEN}$ holds the outputs in a cut-off state. The cut-off state is designed to be more negative than a normal ECL LOW level. This allows the output emitter-followers to turn off when the termination supply is $-2.0V$, presenting a high impedance to the data bus. This high impedance reduces termination power and prevents loss of low state noise margin when several loads share the bus.

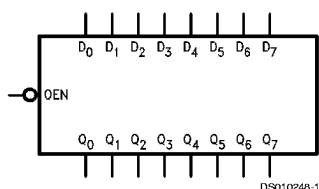
The 100352 outputs are designed to drive a doubly terminated 50Ω transmission line (25Ω load impedance). All inputs have $50\text{ k}\Omega$ pull-down resistors.

Features

- Cut-off drivers
- Drives 25Ω load
- Low power operation
- 2000V ESD protection
- Voltage compensated operating range = $-4.2V$ to $-5.7V$
- Available to industrial grade temperature range
- Available to MIL-STD-883

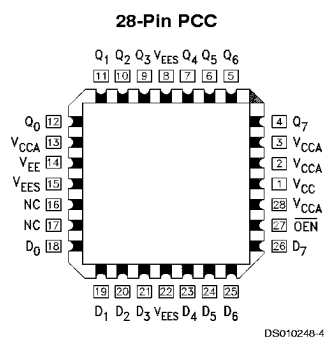
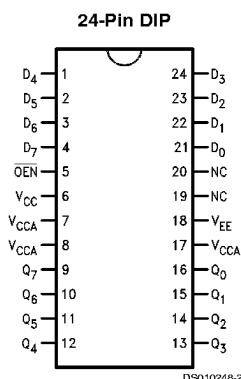
Ordering Code:

Logic Symbol

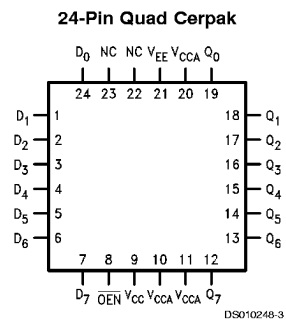


Pin Names	Description
D_0 – D_7	Data Inputs
$\overline{OEN}$	Output Enable Input
Q_0 – Q_7	Data Outputs
NC	No Connect

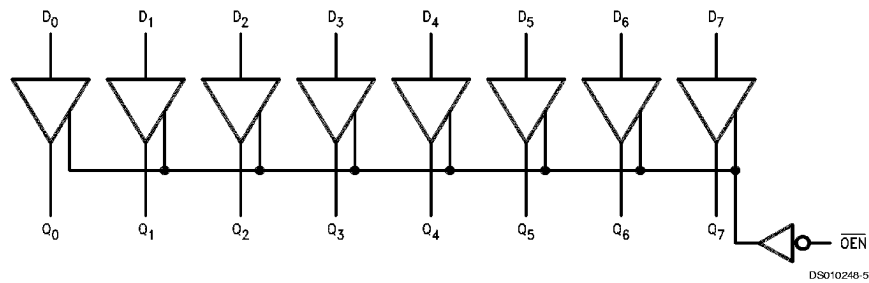
Connection Diagrams



Connection Diagrams (Continued)



Logic Diagram



Truth Table

Inputs		Outputs
Dn	$\overline{\text{OEN}}$	Qn
L	L	L
H	L	H
X	H	Cutoff

H = HIGH Voltage Level
 L = LOW Voltage Level
 Cutoff = Lower-than-LOW State
 X = Don't Care

Absolute Maximum Ratings (Note 1)

Above which the useful life may be impaired

Storage Temperature (T_{STG}) -65°C to $+150^{\circ}\text{C}$

Maximum Junction Temperature (T_J)

Ceramic $+175^{\circ}\text{C}$

Plastic $+150^{\circ}\text{C}$

V_{EE} Pin Potential to

Ground Pin -7.0V to $+0.5\text{V}$

Input Voltage (DC) V_{EE} to $+0.5\text{V}$

Output Current (DC Output HIGH) -100 mA

ESD (Note 2) $\geq 2000\text{V}$

Recommended Operating Conditions

Case Temperature (T_C)

Commercial 0°C to $+85^{\circ}\text{C}$

Industrial -40°C to $+85^{\circ}\text{C}$

Military -55°C to $+125^{\circ}\text{C}$

Supply Voltage (V_{EE}) -5.7V to -4.2V

Note 1: Absolute maximum ratings are those values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Note 2: ESD testing conforms to MIL-STD-883, Method 3015.

Commercial Version DC Electrical Characteristics

$V_{EE} = -4.2\text{V}$ to -5.7V , $V_{CC} = V_{CCA} = \text{GND}$, $T_C = 0^{\circ}\text{C}$ to $+85^{\circ}\text{C}$ (Note 3)

Symbol	Parameter	Min	Typ	Max	Units	Conditions	
V_{OH}	Output HIGH Voltage	-1025	-955	-870	mV	$V_{IN} = V_{IH}(\text{Max})$ or $V_{IL}(\text{Min})$	Loading with 25Ω to -2.0V
V_{OL}	Output LOW Voltage	-1830	-1705	-1620	mV	$V_{IN} = V_{IH}(\text{Min})$ or $V_{IL}(\text{Max})$	Loading with 25Ω to -2.0V
V_{OLC}	Output LOW Voltage			-1610			
V_{OLZ}	Cut-Off LOW Voltage			-1950	mV	$V_{IN} = V_{IH}(\text{Min})$ or $V_{IL}(\text{Max})$	$\overline{\text{OEN}} = \text{HIGH}$
V_{IH}	Input HIGH Voltage	-1165		-870	mV	Guaranteed HIGH Signal for All Inputs	
V_{IL}	Input LOW Voltage	-1830		-1475	mV	Guaranteed LOW Signal for All Inputs	
I_{IL}	Input LOW Current	0.50			μA	$V_{IN} = V_{IL}(\text{Min})$	
I_{IH}	Input HIGH Current			240	μA	$V_{IN} = V_{IH}(\text{Max})$	
I_{EE}	Power Supply Current	-138 -143		-70 -70	mA	Inputs Open $V_{EE} = -4.2\text{V}$ to -4.8V $V_{EE} = -4.2\text{V}$ to -5.7V	

Note 3: The specified limits represent the "worst case" value for the parameter. Since these values normally occur at the temperature extremes, additional noise immunity and guardbanding can be achieved by decreasing the allowable system operating ranges. Conditions for testing shown in the tables are chosen to guarantee operation under "worst case" conditions.

DIP AC Electrical Characteristics

$V_{EE} = -4.2\text{V}$ to -5.7V , $V_{CC} = V_{CCA} = \text{GND}$

Symbol	Parameter	$T_C = 0^{\circ}\text{C}$		$T_C = +25^{\circ}\text{C}$		$T_C = +85^{\circ}\text{C}$		Units	Conditions
		Min	Max	Min	Max	Min	Max		
t_{PLH}	Propagation Delay Dn to Output	0.70	2.00	0.70	2.00	0.70	2.20	ns	Figures 1, 2 (Note 4)
t_{PZH}	Propagation Delay $\overline{\text{OEN}}$ to Output	1.60	4.20	1.60	4.20	1.60	4.20	ns	Figures 1, 2 (Note 4)
t_{PHZ}		1.00	2.70	1.00	2.70	1.00	2.70		
t_{TLH}	Transition Time	0.45	2.00	0.45	2.00	0.45	2.00	ns	Figures 1, 2
t_{THL}	20% to 80%, 80% to 20%								

Note 4: The propagation delay specified is for single output switching. Delays may vary up to 300 ps with multiple outputs switching.

PCC and Cerpak AC Electrical Characteristics

$V_{EE} = 4.2V$ to $-5.7V$, $V_{CC} = V_{CCA} = GND$

Symbol	Parameter	$T_C = 0^\circ C$		$T_C = +25^\circ C$		$T_C = +85^\circ C$		Units	Conditions
		Min	Max	Min	Max	Min	Max		
t_{PLH} t_{PHL}	Propagation Delay Dn to Output	0.70	1.80	0.70	1.80	0.70	2.00	ns	Figures 1, 2 (Note 6)
t_{PZH} t_{PHZ}	Propagation Delay $\overline{OEN}$ to Output	1.60	4.00	1.60	4.00	1.60	4.00	ns	Figures 1, 2 (Note 6)
t_{TLH} t_{THL}	Transition Time 20% to 80%, 80% to 20%	0.45	1.90	0.45	1.90	0.45	1.90	ns	Figures 1, 2
t_{OSHL}	Maximum Skew Common Edge Output-to-Output Variation Data to Output Path		230		230		230	ps	PCC only (Note 5)
t_{OSLH}	Maximum Skew Common Edge Output-to-Output Variation Data to Output Path		240		240		240	ps	PCC only (Note 5)
t_{OST}	Maximum Skew Opposite Edge Output-to-Output Variation Data to Output Path		350		350		350	ps	PCC only (Note 5)
t_{PS}	Maximum Skew Pin (Signal) Transition Variation Data to Output Path		350		350		350	ps	PCC only (Note 5)

Note 5: Output-to-Output Skew is defined as the absolute value of the difference between the actual propagation delay for any outputs within the same packaged device. The specifications apply to any outputs switching in the same direction either HIGH to LOW (t_{OSHL}), or LOW to HIGH (t_{OSLH}), or in opposite directions both HL and LH (t_{OST}). Parameters t_{OST} and t_{PS} guaranteed by design.

Note 6: The propagation delay specified is for single output switching. Delays may vary up to 300 ps with multiple outputs switching.

Industrial Version PCC DC Electrical Characteristics

$V_{EE} = -4.2V$ to $-5.7V$, $V_{CC} = V_{CCA} = GND$, $T_C = -40^\circ C$ to $+85^\circ C$ (Note 7)

Symbol	Parameter	T _C = −40° C		T _C = 0° C to +85° C		Units	Conditions	
		Min	Max	Min	Max			
V _{OH}	Output HIGH Voltage	−1085	−870	−1025	−870	mV	V _{IN} = V _{IH(Max)} or V _{IL(Min)}	Loading with 25Ω to −2.0V
V _{OL}	Output LOW Voltage	−1830	−1575	−1830	−1620			
V _{OHC}	Output HIGH Voltage	−1095		−1035		mV	V _{IN} = V _{IH(Min)} or V _{IL(Max)}	Loading with 25Ω to −2.0V
V _{OLC}	Output LOW Voltage		−1565		−1610			
V _{OLZ}	Cut-Off LOW Voltage		−1950		−1950	mV	V _{IN} = V _{IH(Min)} or V _{IL (Max)}	OEN = HIGH
V _{IH}	Input HIGH Voltage	−1170	−870	−1165	−870	mV	Guaranteed HIGH Signal for All Inputs	
V _{IL}	Input LOW Voltage	−1830	−1480	−1830	−1475	mV	Guaranteed LOW Signal for All Inputs	
I _{IL}	Input LOW Current	0.50		0.50		μA	V _{IN} = V _{IL(Min)}	
I _{IH}	Input HIGH Current		340		240	μA	V _{IN} = V _{IH(Max)}	
I _{EE}	Power Supply Current						Inputs Open	
		−138	−60	−138	−70	mA	V _{EE} = −4.2V to −4.8V	
		−143	−60	−143	−70		V _{EE} = −4.2V to −5.7V	

Note 7: The specified limits represent the "worst case" value for the parameter. Since these values normally occur at the temperature extremes, additional noise immunity and guardbanding can be achieved by decreasing the allowable system operating ranges. Conditions for testing shown in the tables are chosen to guarantee operation under "worst case" conditions.

PCC AC Electrical Characteristics

$V_{EE} = 4.2V$ to $-5.7V$, $V_{CC} = V_{CCA} = GND$

Symbol	Parameter	$T_C = -40^{\circ}C$		$T_C = +25^{\circ}C$		$T_C = +85^{\circ}C$		Units	Conditions
		Min	Max	Min	Max	Min	Max		
t_{PLH}	Propagation Delay	0.60	1.80	0.70	1.80	0.70	2.00	ns	Figures 1, 2 (Note 8)
t_{PHL}	Dn to Output								
t_{PZH}	Propagation Delay	1.40	4.40	1.60	4.00	1.60	4.00	ns	Figures 1, 2 (Note 8)
t_{PHZ}	$\overline{OEN}$ to Output	1.00	2.50	1.00	2.50	1.00	2.50		
t_{TLH}	Transition Time	0.40	2.50	0.45	1.90	0.45	1.90	ns	Figures 1, 2
t_{THL}	20% to 80%, 80% to 20%								

Note 8: The propagation delay specified is for single output switching. Delays may vary up to 300 ps with multiple outputs switching.

Military Version DC Electrical Characteristics

$V_{EE} = -4.2V$ to $-5.7V$, $V_{CC} = V_{CCA} = GND$, $T_C = -55^{\circ}C$ to $+125^{\circ}C$

Symbol	Parameter	Min	Max	Units	T _C	Conditions		Notes
V _{OH}	Output HIGH Voltage	−1025 −1085	−870 −870	mV	0°C to +125°C −55°C	V _{IN} = V _{IH(Max)} or V _{IL(Min)}	Loading with 25Ω to −2.0V	(Notes 9, 10, 11)
V _{OL}	Output LOW Voltage	−1830 −1830	−1620 −1555	mV	0°C to +125°C −55°C			
V _{OHC}	Output HIGH Voltage	−1035 −1085		mV	0°C to +125°C −55°C			
V _{OLC}	Output LOW Voltage		−1610 −1555	mV	0°C to +125°C −55°C	V _{IN} = V _{IH(Min)} or V _{IL(Max)}	Loading with 25Ω to −2.0V	(Notes 9, 10, 11)
V _{OLZ}	Cut-Off LOW Voltage		−1950 −1850	mV	0°C to +125°C −55°C			
V _{IH}	Input HIGH Voltage	−1165	−870	mV	−55°C to +125°C			
V _{IL}	Input LOW Voltage	−1830	−1475	mV	−55°C to +125°C	Guaranteed LOW signal for All inputs		(Notes 9, 10, 11, 12)
I _{IL}	Input LOW Current	0.50		μA	−55°C to +125°C	V _{EE} = 4.2V V _{IN} = V _{IL(Min)}		(Notes 9, 10, 11)
I _{IH}	Input HIGH Current		240 340	μA	0°C to + 125°C −55°C	V _{EE} = −5.7V V _{IN} = V _{IH(Max)}		(Notes 9, 10, 11)
I _{EE}	Power Supply Current	−145 −150	−55	mA	−55°C to +125°C	Inputs Open V _{EE} = −4.2V to −4.8V V _{EE} = −4.2V to −5.7V		(Notes 9, 10, 11)

Note 9: F100K 300 Series cold temperature testing is performed by temperature soaking (to guarantee junction temperature equals $-55^{\circ}C$), then testing immediately without allowing for the junction temperature to stabilize due to heat dissipation after power-up. This provides "cold start" specs which can be considered a worst case condition at cold temperatures.

Note 10: Screen tested 100% on each device at $-55^{\circ}C$, $+25^{\circ}C$, and $+125^{\circ}C$, Subgroups 1, 2, 3, 7, and 8.

Note 11: Sample tested (Method 5005, Table I) on each manufactured lot at $-55^{\circ}C$, $+25^{\circ}C$, and $+125^{\circ}C$, Subgroups A1, 2, 3, 7, and 8.

Note 12: Guaranteed by applying specified input condition and testing V_{OH}/V_{OL} .

AC Electrical Characteristics

$V_{EE} = -4.2V$ to $-5.7V$, $V_{CC} = V_{CCA} = GND$

Symbol	Parameter	$T_C = -55^{\circ}C$		$T_C = +25^{\circ}C$		$T_C = +125^{\circ}C$		Units	Conditions	Notes
		Min	Max	Min	Max	Min	Max			
t_{PLH}	Propagation Delay	0.30	2.60	0.50	2.40	0.50	2.70	ns	Figures 1, 2	(Notes 13, 14, 16, 17)
t_{PHL}	Dn to Output									

AC Electrical Characteristics (Continued)

$V_{EE} = -4.2V$ to $-5.7V$, $V_{CC} = V_{CCA} = GND$

Symbol	Parameter	$T_C = -55^\circ C$		$T_C = +25^\circ C$		$T_C +125^\circ C$		Units	Conditions	Notes
		Min	Max	Min	Max	Min	Max			
t_{PZH}	Propagation Delay	1.20	4.40	1.40	4.20	1.20	4.40	ns	Figures 1, 2	(Notes 13, 14, 15, 17)
t_{PHZ}	OEN to Output	0.70	3.00	0.70	2.80	0.70	3.20	ns	Figures 1, 2	(Note 16)
t_{TLH}	Transition Time	0.40	2.50	0.40	2.40	0.40	2.70	ns	Figures 1, 2	(Note 16)
t_{THL}	20% to 80%, 80% to 20%									

Note 13: F100K 300 Series cold temperature testing is performed by temperature soaking (to guarantee junction temperature equals $-55^\circ C$), then testing immediately after power-up. This provides "cold start" specs which can be considered a worst case condition at cold temperatures.

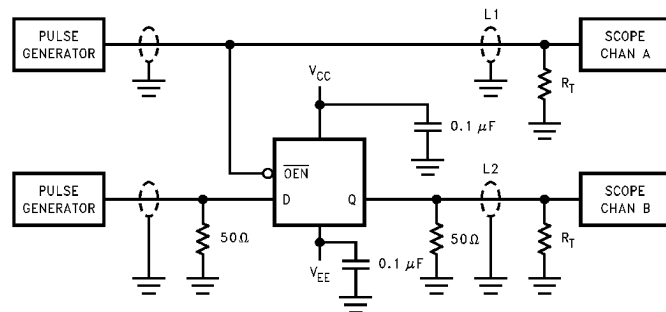
Note 14: Screen tested 100% on each device at $+25^\circ C$ temperature only, Subgroup A9.

Note 15: Sample tested (Method 5005, Table I) on each manufactured lot at $+25^\circ C$, Subgroup A9, and at $+125^\circ C$ and $-55^\circ C$ temperatures, Subgroups A10 and A11.

Note 16: Not tested at $+25^\circ C$, $+125^\circ C$, and $-55^\circ C$ temperature (design characterization data).

Note 17: The propagation delay specified is for single output switching. Delays may vary up to 300 ps with multiple outputs switching.

Test Circuitry



DS010248-6

Notes:

V_{CC} , $V_{CCA} = +2V$, $V_{EE} = -2.5V$

L1 and L2 = equal length 50 ohm impedance lines

$R_T = 50\Omega$ terminator internal to scope

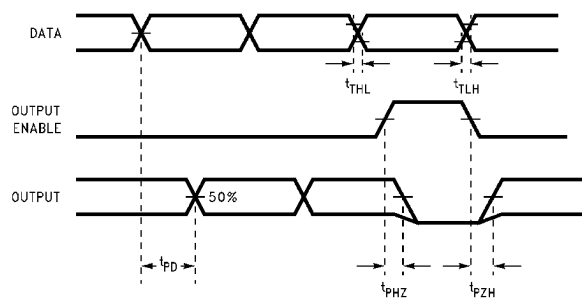
Decoupling 0.1 μF from GND to V_{CC} and V_{EE}

All unused outputs are loaded with 25 ohm to GND

C_L = Fixture and stray capacitance ≤ 3 pF

FIGURE 1. AC Test Circuit

Switching Waveforms



DS010248-7

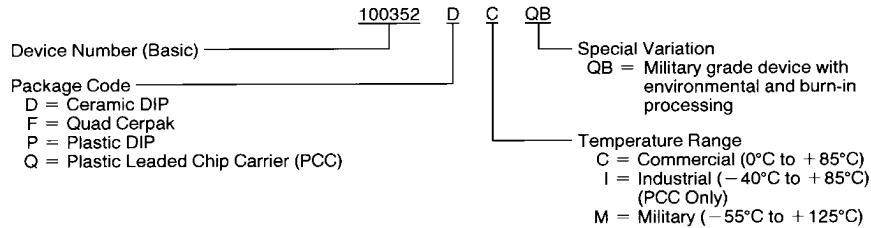
Note:

The output AC measurement point for cut-off propagation delay testing is the 50% voltage point between active V_{OL} and V_{OH} .

FIGURE 2. Propagation Delay, Cut-Off and Transition Times

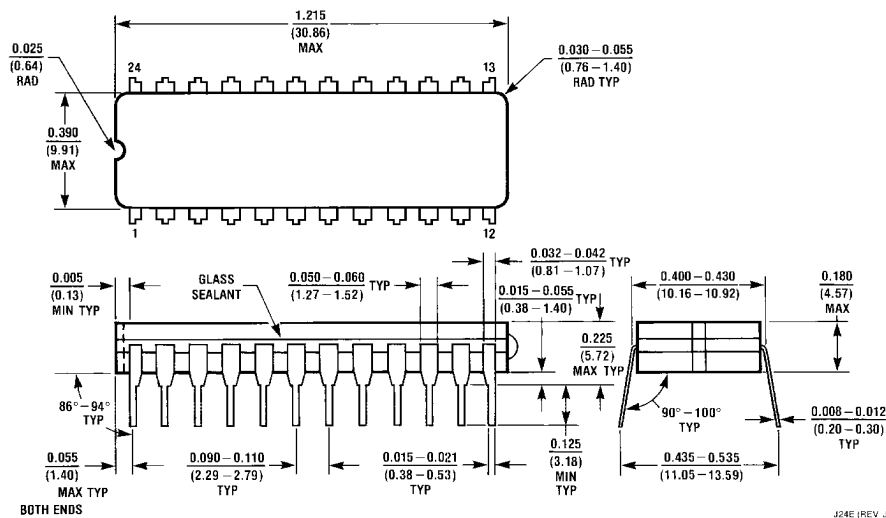
Ordering Information

The device number is used to form part of a simplified purchasing code where a package type and temperature range are defined as follows:



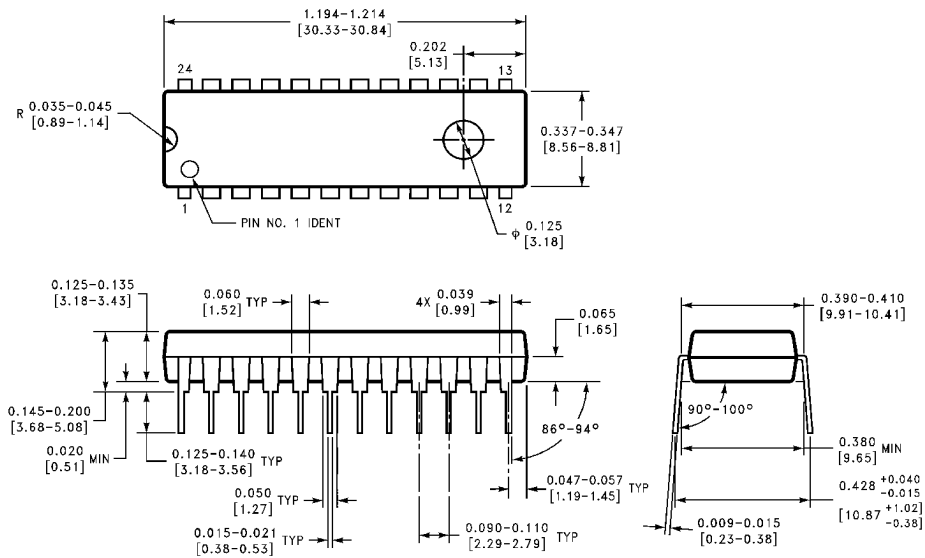
DS010248-8

Physical Dimensions inches (millimeters) unless otherwise noted



24-Lead Ceramic Dual-In-Line Package (0.400" Wide) (D)
Package Number J24E

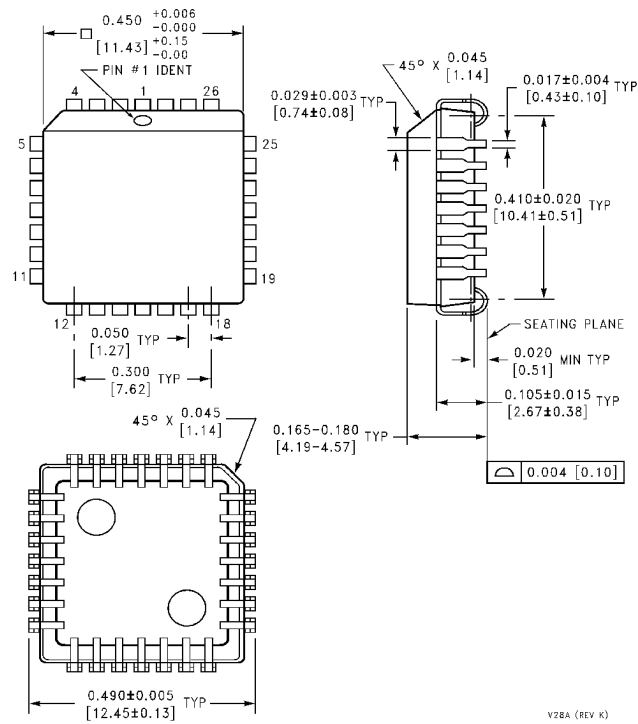
Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



24-Lead Plastic Dual-In-Line Package (P)
Package Number N24E

N24E (REV A)

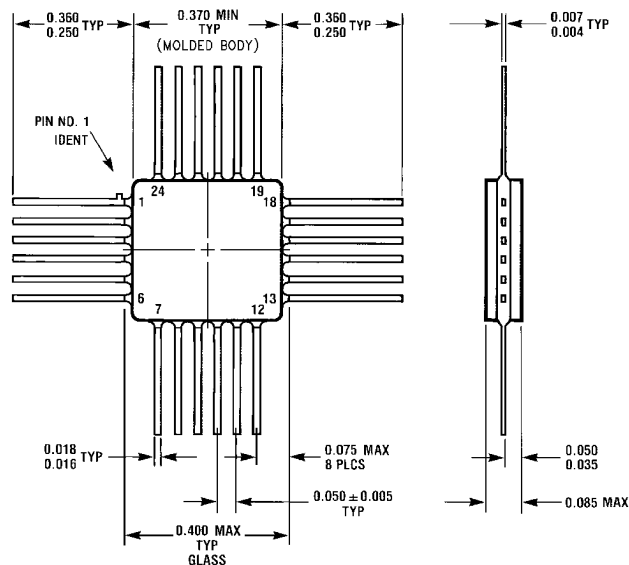
Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



28-Lead Plastic Chip Carrier (Q)
Package Number V28A

V28A (REV K)

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



24-Lead Quad Cerpak (F)
Package Number W24B

W24B (REV D)

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