



Vishay Semiconductors

Ultralow V_F Ultrafast Rectifier, 6 A FRED Pt®

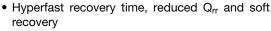


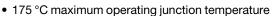


D-PAK (TO-252AA)

PRODUCT SUMMARY					
Package	D-PAK (TO-252AA)				
I _{F(AV)}	6 A				
V_{R}	600 V				
V _F at I _F	2.1 V				
t _{rr} (typ.)	18 ns				
T _J max.	175 °C				
Diode variation	Single die				

FEATURES





• For PFC CRM/CCM operation

· Low forward voltage drop

Low lorward voltage drop

Low leakage current

 Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

• Compliant to RoHS Directive 2002/95/EC

DESCRIPTION/APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS inverters or as freewheeling diodes. Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Peak repetitive reverse voltage	V_{RRM}		600	V			
Average rectified forward current	I _{F(AV)}	T _C = 144 °C	6				
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	70	А			
Peak repetitive forward current	I _{FM}	T _C = 144 °C, f = 20 kHz, d = 50 %	12				
Operating junction and storage temperatures	T _J , T _{Stg}		- 65 to 175	°C			

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-			
Forward voltage	V _F	I _F = 6 A	- 1.60 2.1		V			
		I _F = 6 A, T _J = 150 °C	-	1.26	1.7			
Develope legicage comment		$V_R = V_R$ rated	-	-	50			
Reverse leakage current	I _R	$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	-	250	μΑ		
Junction capacitance	C _T	V _R = 600 V	-	3.5	-	pF		
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8	-	nH		

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VS-6EWH06FN-E3

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS		
Reverse recovery time		$I_F = 1 A, dI_F/dt = 10$	00 A/μs, V _R = 30 V	-	18	25		
	t _{rr}	$I_F = 1 \text{ A, } dI_F/dt = 50 \text{ A/}\mu\text{s, } V_R = 30 \text{ V}$		-	22	-		
		T _J = 25 °C	$I_F = 6 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 390 \text{ V}$	-	27	-	ns	
		T _J = 125 °C		-	37	-		
Peak recovery current	I _{RRM}	T _J = 25 °C		-	4.1	-	Α	
		T _J = 125 °C		-	5.3	-		
Reverse recovery charge		T _J = 25 °C		-	57	-	nC	
	Q _{rr}	T _J = 125 °C		-	103	-		

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C	
Thermal resistance, junction to case per leg	R_{thJC}		-	-	3	°C/W	
Approximate weight				0.3		g	
			0.01		OZ.		
Marking device		Case style D-PAK (TO-252AA)	6EWH06FN				





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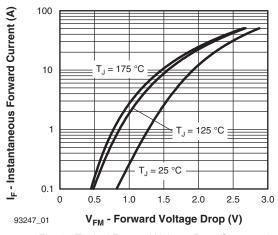


Fig. 1 - Typical Forward Voltage Drop Characteristics

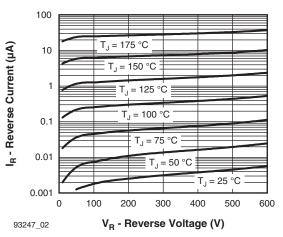


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

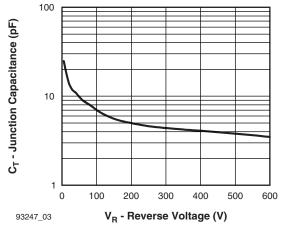


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

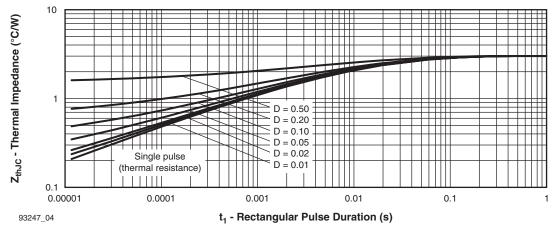


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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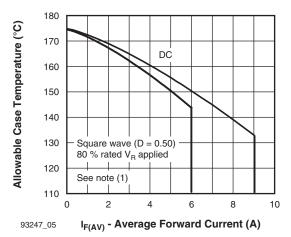
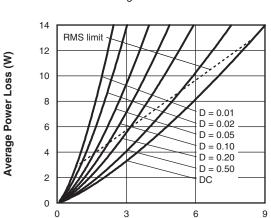


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



I_{F(AV)} - Average Forward Current (A) Fig. 6 - Forward Power Loss Characteristics

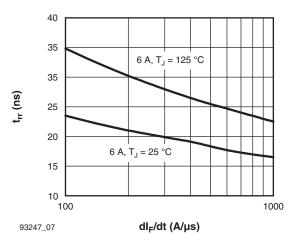


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

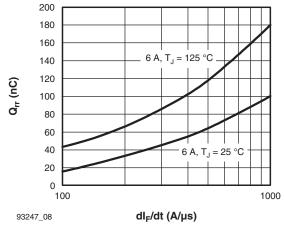


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

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 $\begin{array}{ll} \mbox{(1)} & \mbox{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \mbox{Forward power loss} = I_{F(AV)} \times V_{FM} \mbox{ at } (I_{F(AV)}/D) \mbox{ (see fig. 6)}; \\ \end{array}$ Pd_{REV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = Rated V_R



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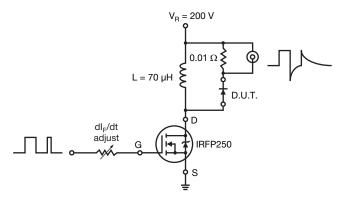
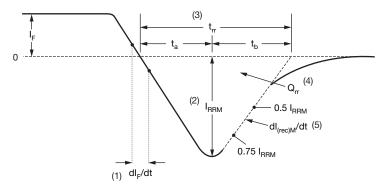


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) Q_{rr} area under curve defined by t_{rr}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

VS-6EWH06FN-E3

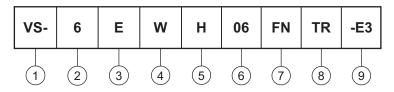
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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (6 = 6 A)

Circuit configuration:

E = Single diode

Package identifier:

W = D-PAK

5 - H = Hyperfast recovery

Voltage rating (06 = 600 V)

7 - FN = TO-252AA

8 - TR = Tape and reel

9 - Environmental digit:

-E3 = RoHS compliant and terminations lead (Pb)-free

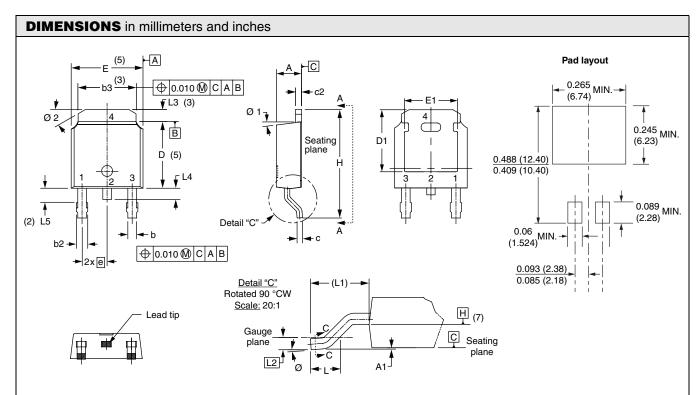
ORDERING INFORMATION (Example)						
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-6EWH06FNTR-E3	2000	2000	13" diameter reel			

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95016			
Part marking information	www.vishay.com/doc?95176			
Packaging information (TR only)	www.vishay.com/doc?95033			
SPICE model	www.vishav.com/doc?95187			



Vishay High Power Products

D-PAK (TO-252AA)



SYMBOL	MILLIM	ETERS	INC	NOTES	
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	2.18	2.39	0.086	0.094	
A1	-	0.13	-	0.005	
b	0.64	0.89	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	3
С	0.46	0.61	0.018	0.024	
c2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	5
D1	5.21	-	0.205	-	3
Е	6.35	6.73	0.250	0.265	5
E1	4.32	-	0.170	-	3

SYMBOL	MILLIMETERS		INC	NOTES	
STWBOL	MIN.	MAX.	MIN.	MAX.	NOTES
е	2.29 BSC		0.090 BSC		
Н	9.40	10.41	0.370	0.410	
L	1.40	1.78	0.055	0.070	
L1	2.74	2.74 BSC		0.108 REF.	
L2	0.51	0.51 BSC		0.020 BSC	
L3	0.89	1.27	0.035	0.050	3
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	2
Ø	0°	10°	0°	10°	
Ø1	0°	15°	0°	15°	
Ø2	25°	35°	25°	35°	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension uncontrolled in L5
- (3) Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- (4) Section C C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip
- (5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (6) Dimension b1 and c1 applied to base metal only
- $^{(7)}$ Datum A and B to be determined at datum plane H
- (8) Outline conforms to JEDEC outline TO-252AA



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