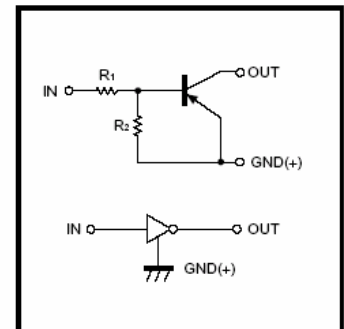


Digital transistors (PNP)

FEATURES

1. Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors(see equivalent circuit).
2. The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input.They also have the advantage of almost completely eliminating parasitic effects.
3. Only the on/off conditions need to be set for operation, making device design easy.

●Equivalent circuit



PIN CONNENCTIONS AND MARKING

DTA114EE 1.IN 2.GND 3.OUT SOT-523 Abbreviated symbol: 14	DTA114EUA 1.IN 2.GND 3.OUT SOT-323 Abbreviated symbol: 14
DTA114EKA 1.IN 2.GND 3.OUT SOT-23-3L Abbreviated symbol: 14	DTA114ECA 1.IN 2.GND 3.OUT SOT-23 Abbreviated symbol: 14
DTA114ESA 1.GND 2.OUT 3.IN TO-92S	

Absolute maximum ratings($T_a=25^{\circ}\text{C}$)

Parameter	Symbol	Limits (DTA114E□)					Unit
		E	UA	CA	KA	SA	
Supply voltage	V _{CC}	-50					V
Input voltage	V _{IN}	-40~10					V
Output current	I _O	-50					mA
	I _{C(MAX)}	-100					
Power dissipation	P _d	150	200			300	mW
Junction temperature	T _j	150					℃
Storage temperature	T _{stg}	-55~150					℃

Electrical characteristics ($T_a=25^{\circ}\text{C}$)

Parameter	Symbol	Min.	Typ	Max.	Unit	Conditions
Input voltage	$V_{I(off)}$			-0.5	V	$V_{CC}=-5V, I_O=-100\mu\text{A}$
	$V_{I(on)}$	-3				$V_O=-0.3V, I_O=-10\text{mA}$
Output voltage	$V_{O(on)}$			-0.3	V	$I_O/I_I=-10\text{mA}/-0.5\text{mA}$
Input current	I_I			-0.88	mA	$V_I=-5V$
Output current	$I_{O(off)}$			-0.5	μA	$V_{CC}=-50V, V_I=0$
DC current gain	G_I	30				$V_O=-5V, I_O=-5\text{mA}$
Input resistance	R_I	7	10	13	$\text{K}\Omega$	
Resistance ratio	R_2/R_1	0.8	1	1.2		
Transition frequency	f_T		250		MHz	$V_{CE}=-10V, I_E=5\text{mA}, f=100\text{MHz}$

Typical Characteristics

●Electrical characteristic curves

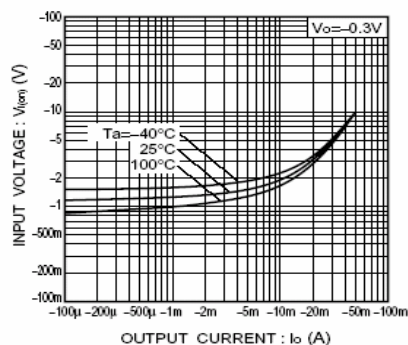


Fig.1 Input voltage vs. output current (ON characteristics)

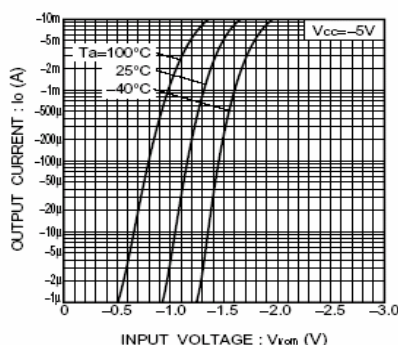


Fig.2 Output current vs. input voltage (OFF characteristics)

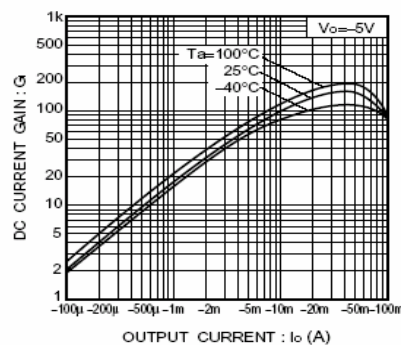


Fig.3 DC current gain vs. output current

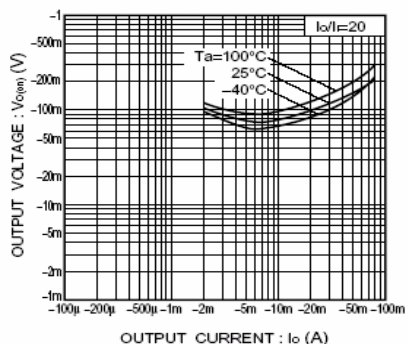


Fig.4 Output voltage vs. output current