

SANYO Semiconductors DATA SHEET

SCH2102 — PNP Epitaxial Planar Silicon Transistor Switching, Driver Applications

Applications

· Low-frequency power amplifier, high-speed switching, motor drivers, muting.

Features

- · Composite type with 2 PNP transistors contained in a single package, facilitating high-density mounting.
- Ultrasmall package permitting applied sets to be small and slim.
- · Low Ron.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		-15	V
Collector-to-Emitter Voltage	VCEO		-12	V
Emitter-to-Base Voltage	VEBO		-5	V
Collector Current	Ic		-500	mA
Collector Current (Pulse)	ICP		-1.0	Α
Collector Dissipation	PC	Mounted on a ceramic board (600mm ² X0.8mm) 1unit	0.4	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			1.1
			min	typ	max	Unit
Collector Cutoff Current	ІСВО	V _{CB} =-12V, I _E =0			-100	nA
Emitter Cutoff Current	IEBO	VEB=-4V, IC=0			-100	nA
DC Current Gain	hFE	V _{CE} =-2V, I _C =-10mA	300		700	
Gain-Bandwidth Product	f⊤	V _{CE} =-2V, I _C =-50mA		490		MHz
Output Capacitance	Cob	VCB=-10V, f=1MHz		4		pF
Collector-to-Emitter Saturation Voltage	V _{CE} (sat)	I _C =-200mA, I _B =-10mA		-150	-300	mV
Base-to-Emitter Saturation Voltage	V _{BE} (sat)	I _C =-200mA, I _B =-10mA		-0.9	-1.2	V
Collector-to-Base Breakdown Voltage	V(BR)CBO	IC=-10μA, IE=0	-15			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	IC=-1mA, RBE=∞	-12			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =-10μA, I _C =0	-5			V

Marking: EF Continued on next page.

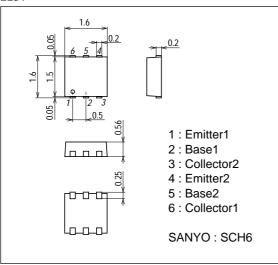
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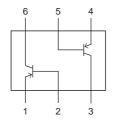
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Turm-ON Time	ton	See specified test circuit.		30		ns
Storage Time	t _{stg}	See specified test circuit.		57		ns
Fall Time	tf	See specified test circuit.		15		ns

Package Dimensions

unit : mm 2237



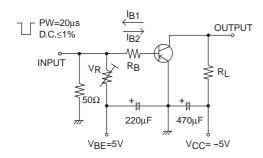
Electrical Connection



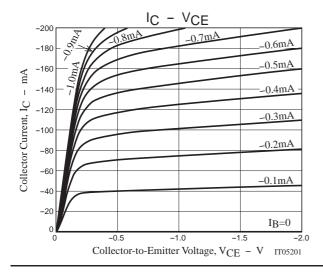
1 : Emitter1
2 : Base1
3 : Collector2
4 : Emitter2
5 : Base2
6 : Collector1

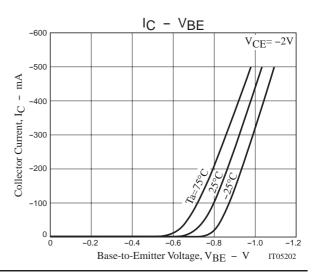
Top view

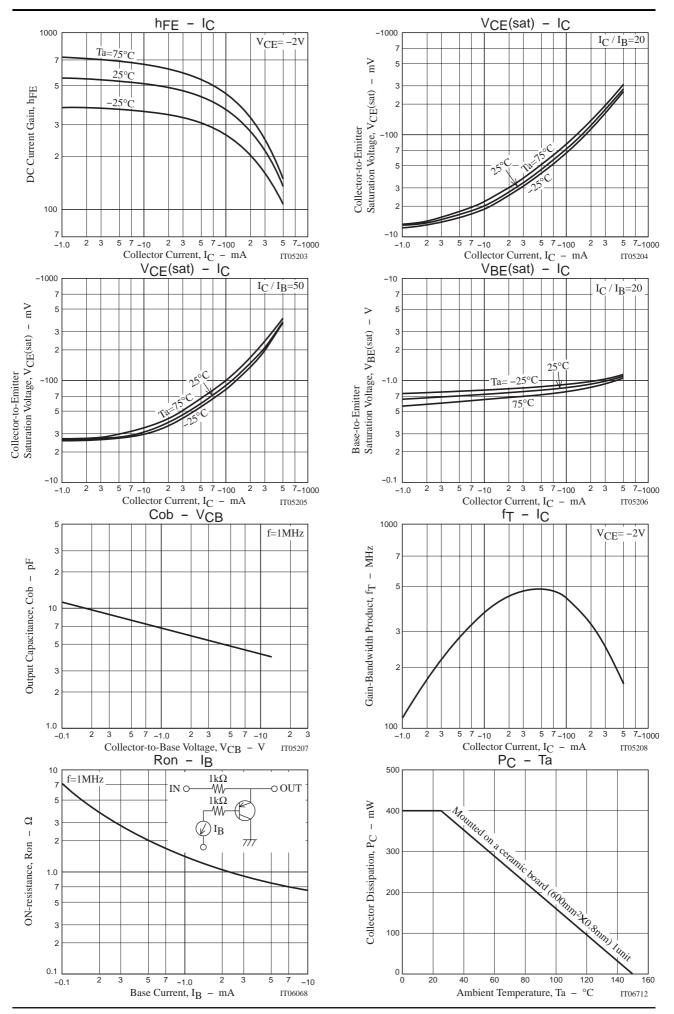
Switching Time Test Circuit



$$I_{C}=20I_{B1}=-20I_{B2}=-400mA$$







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