

RJK6018DPM

Silicon N Channel MOS FET High Speed Power Switching

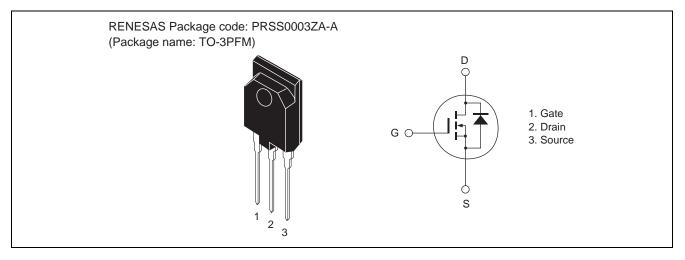
R07DS0131EJ0100 Rev.1.00 Sep 09, 2010

Datasheet

Features

- Low on-resistance
- $R_{DS(on)} = 0.2 \ \Omega$ typ. (at $I_D = 15 \ A$, $V_{GS} = 10 \ V$, $Ta = 25^{\circ}C$)
- Low leakage current
- High speed switching

Outline



Absolute Maximum Ratings

			$(Ta = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	600	V
Gate to source voltage	V _{GSS}	±30	V
Drain current	ID ^{Note4}	30	А
Drain peak current	I _{D (pulse)} Note1	90	А
Body-drain diode reverse drain current	I _{DR}	30	А
Body-drain diode reverse drain peak current	I _{DR (pulse)} Note1	90	А
Avalanche current	I _{AP} ^{Note3}	6	А
Avalanche energy	E _{AR} ^{Note3}	1.9	mJ
Channel dissipation	Pch ^{Note2}	60	W
Channel to case thermal impedance	θch-c	2.08	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. $PW \leq 10~\mu s,\,duty~cycle \leq 1\%$

- 2. Value at $Tc = 25^{\circ}C$
- 3. STch = 25° C, Tch $\leq 150^{\circ}$ C
- 4. Limited by maximum safe operation area



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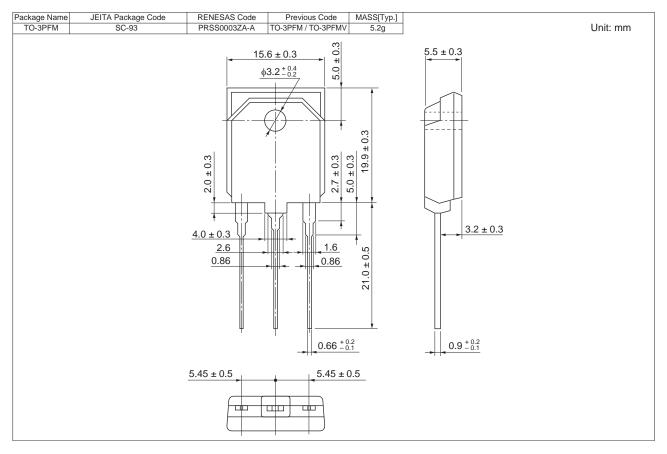
Electrical Characteristics

						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	V _{(BR)DSS}	600	_	—	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I _{DSS}	_		1	μΑ	$V_{DS} = 600 V, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS}=\pm 30~V,~V_{DS}=0$
Gate to source cutoff voltage	V _{GS(off)}	3.0	_	4.5	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	_	0.200	0.235	Ω	$I_D = 15 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note5}}$
resistance						
Input capacitance	Ciss	_	4100	_	pF	V _{DS} = 25 V
Output capacitance	Coss	—	380	—	pF	V _{GS} = 0 f = 1 MHz
Reverse transfer capacitance	Crss	_	37	—	pF	
Turn-on delay time	t _{d(on)}		50	—	ns	$I_D = 15 \text{ A}$ $V_{GS} = 10 \text{ V}$ $R_L = 20 \Omega$ $Rg = 10 \Omega$
Rise time	tr	_	88	—	ns	
Turn-off delay time	t _{d(off)}	_	140	—	ns	
Fall time	t _f	_	81	—	ns	
Total gate charge	Qg	_	92	—	nC	V _{DD} = 480 V V _{GS} = 10 V I _D = 30 A
Gate to source charge	Qgs	_	22	—	nC	
Gate to drain charge	Qgd	_	38	—	nC	
Body-drain diode forward voltage	V _{DF}	_	0.90	1.50	V	$I_F = 30 \text{ A}, V_{GS} = 0^{Note5}$
Body-drain diode reverse recovery time	t _{rr}	—	480	—	ns	$I_F = 30 \text{ A}, V_{GS} = 0$
						di _F /dt = 100 A/µs

Notes: 5. Pulse test



Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RJK6018DPM-00-T1	360 pcs	Box (Tube)



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