



# BYD47-20

Fast soft-recovery rectifier

Rev. 04 — 4 February 2005

Product data sheet

## 1. Product profile

### 1.1 General description

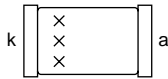
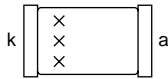
Cavity free cylindrical glass SOD87 package through Implotec™ technology. This package is hermetically sealed and fatigue free as coefficients of expansion of all used parts are matched.

### 1.2 Features

- Glass passivated
- High maximum operating temperature
- Low leakage current
- Excellent stability
- Shipped in 8 mm embossed tape
- Smallest surface mount rectifier outline

## 2. Pinning information

Table 1: Pinning

Pin	Description	Simplified outline	Symbol
1	cathode (K)	<a href="#">[1]</a>	 <i>sym006</i>
2	anode (A)		

[1] The marking bar indicates the cathode.

## 3. Ordering information

Table 2: Ordering information

Type number	Package		
	Name	Description	Version
BYD47-20	-	hermetically sealed glass surface mounted package; Implotec™ technology; 2 connectors	SOD87

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## 4. Limiting values

**Table 3: Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{RSM}$	non-repetitive peak reverse voltage		-	2100	V
$V_{RRM}$	repetitive peak reverse voltage		-	2000	V
$I_{F(AV)}$	average forward current	$T_{tp} = 105\text{ °C}$ ; see <a href="#">Figure 1</a> ; averaged over any 20 ms period; see also <a href="#">Figure 5</a>	-	0.80	A
		$T_{amb} = 25\text{ °C}$ ; printed-circuit board mounting (see <a href="#">Figure 13</a> ); see <a href="#">Figure 2</a> ; averaged over any 20 ms period; see also <a href="#">Figure 5</a>	-	0.34	A
$I_{FRM}$	repetitive peak forward current	$T_{tp} = 85\text{ °C}$ ; see <a href="#">Figure 3</a>	-	8.0	A
		$T_{amb} = 65\text{ °C}$ ; see <a href="#">Figure 4</a>	-	2.8	A
$I_{FSM}$	non-repetitive peak forward current	$t = 10\text{ ms}$ half sine wave; $T_j = T_{j(max)}$ prior to surge; $V_R = V_{RRMmax}$	-	10	A
$T_{stg}$	storage temperature		-65	+175	°C
$T_j$	junction temperature	see <a href="#">Figure 6</a>	-65	+175	°C

## 5. Thermal characteristics

**Table 4: Thermal characteristics**

Symbol	Parameter	Conditions	Typ	Unit
$R_{th(j-tp)}$	thermal resistance from junction to tie-point		30	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient		[1] 150	K/W

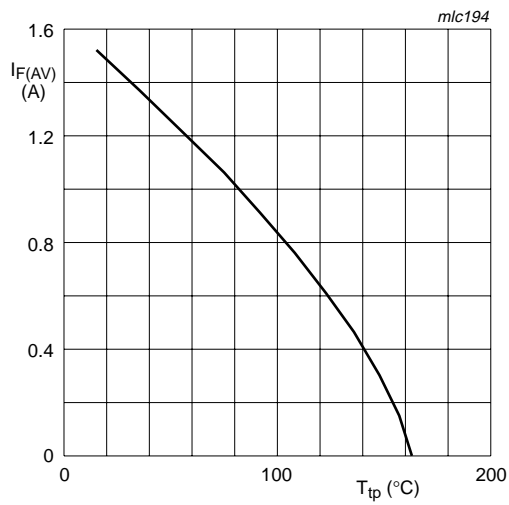
[1] Device mounted on an epoxy-glass printed-circuit board, 1.5 mm thick; thickness of Cu-layer  $\geq 40\text{ }\mu\text{m}$ , see [Figure 13](#).

## 6. Characteristics

**Table 5: Characteristics**

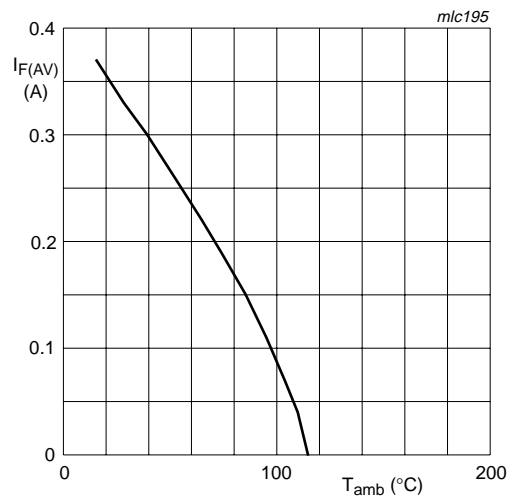
$T_j = 25\text{ °C}$ ; unless otherwise stated.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_F$	forward voltage	$I_F = 1\text{ A}$ ; $T_j = T_{j(max)}$ ; see <a href="#">Figure 7</a>	-	-	2.05	V
		$I_F = 1\text{ A}$ ; see <a href="#">Figure 7</a>	-	-	2.40	V
$I_R$	reverse current	$V_R = V_{RRMmax}$ ; see <a href="#">Figure 8</a>	-	-	5	$\mu\text{A}$
		$V_R = V_{RRMmax}$ ; $T_j = 125\text{ °C}$ ; see <a href="#">Figure 8</a>	-	-	50	$\mu\text{A}$
$t_{rr}$	reverse recovery time	when switched from $I_F = 0.5\text{ A}$ to $I_R = 1\text{ A}$ ; measured at $I_R = 0.25\text{ A}$ ; see <a href="#">Figure 10</a>	-	-	300	ns
$C_d$	diode capacitance	$f = 1\text{ MHz}$ ; $V_R = 0\text{ V}$ ; see <a href="#">Figure 9</a>	-	15	-	pF
$\left  \frac{dI_R}{dt} \right $	maximum slope of reverse recovery current	when switched from $I_F = 1\text{ A}$ to $V_R \geq 30\text{ V}$ and $dI_F/dt = -1\text{ A}/\mu\text{s}$ ; see <a href="#">Figure 11</a>	-	-	5	A/ $\mu\text{s}$



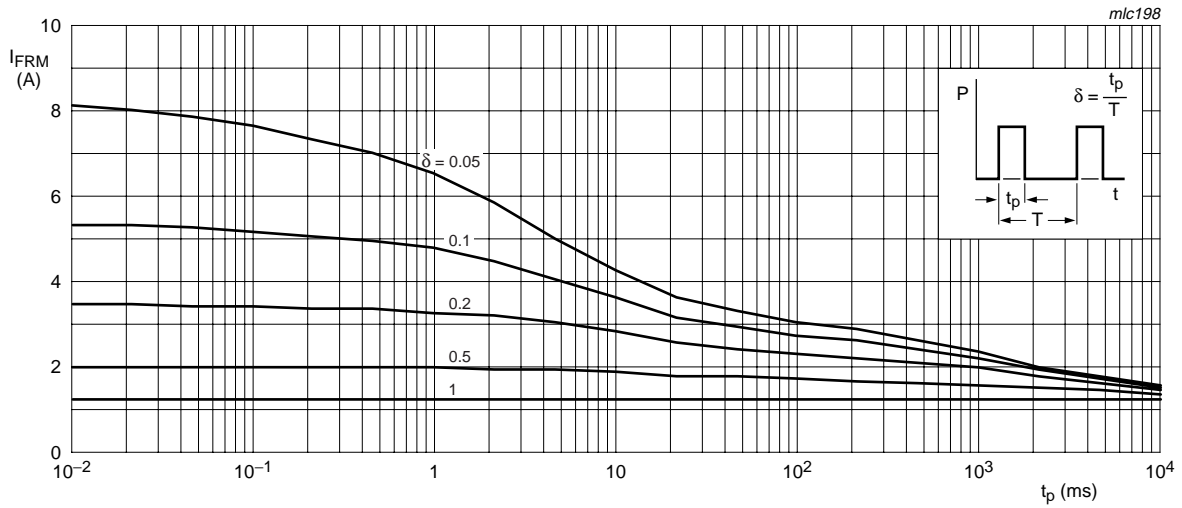
$a = I_{F(RMS)}/I_{F(AV)} = 1.42$ ;  $V_R = V_{RRMmax}$ ;  $\delta = 0.5$ .  
Switched mode application.

**Fig 1. Maximum permissible average forward current as a function of tie-point temperature (including losses due to reverse leakage)**



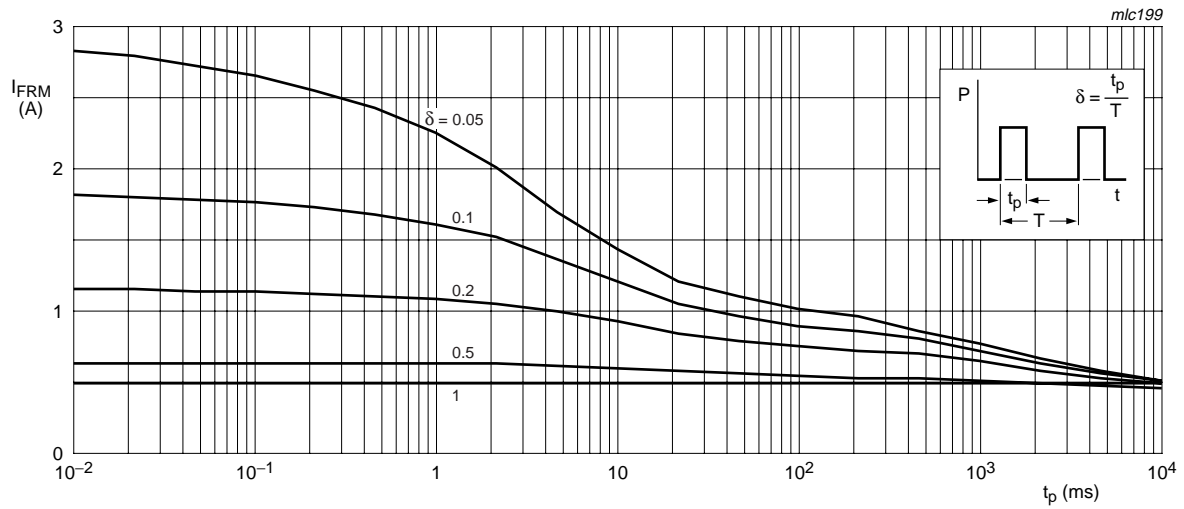
$a = I_{F(RMS)}/I_{F(AV)} = 1.42$ ;  $V_R = V_{RRMmax}$ ;  $\delta = 0.5$ .  
Device mounted as shown in [Figure 13](#).  
Switched mode application.

**Fig 2. Maximum permissible average forward current as a function of ambient temperature (including losses due to reverse leakage)**



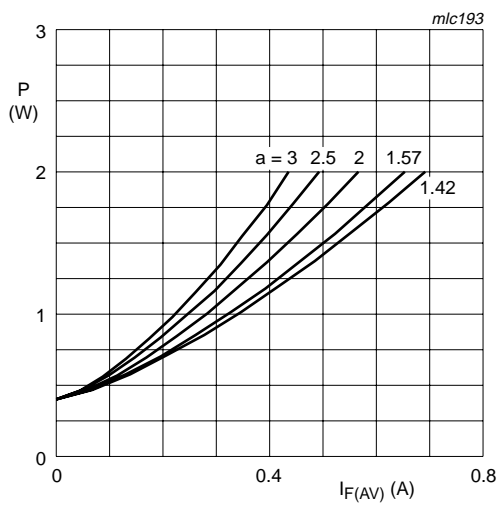
$T_{tp} = 85 \text{ °C}$ ;  $R_{th(j-tp)} = 30 \text{ K/W}$ .  
 $V_{RRMmax}$  during  $1 - \delta$ ; curves include derating for  $T_{j(max)}$  at  $V_{RRM} = 2000 \text{ V}$ .

**Fig 3. Maximum repetitive peak forward current as a function of pulse time (square pulse) and duty factor**



$T_{amb} = 65\text{ }^\circ\text{C}$ ;  $R_{th(j-a)} = 150\text{ K/W}$ .  
 $V_{RRMmax}$  during  $1 - \delta$ ; curves include derating for  $T_{j(max)}$  at  $V_{RRM} = 2000\text{ V}$ .

Fig 4. Maximum repetitive peak forward current as a function of pulse time (square pulse) and duty factor



$a = I_{F(RMS)}/I_{F(AV)}$ ;  $V_R = V_{RRMmax}$ ;  $\delta = 0.5$ .

Fig 5. Maximum steady state power dissipation (forward plus leakage current losses, excluding switching losses) as a function of average forward current

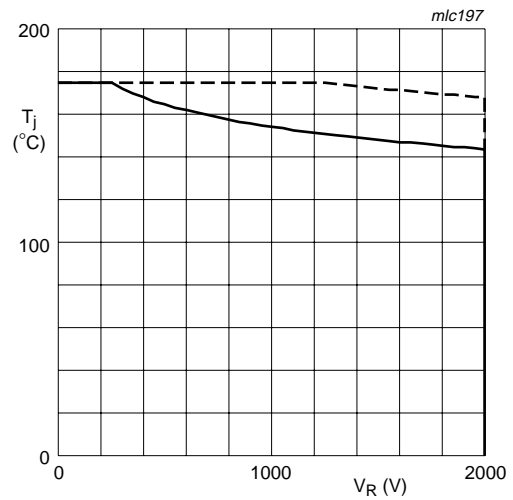
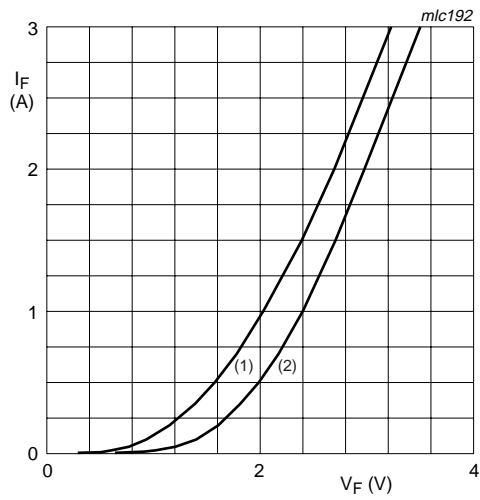
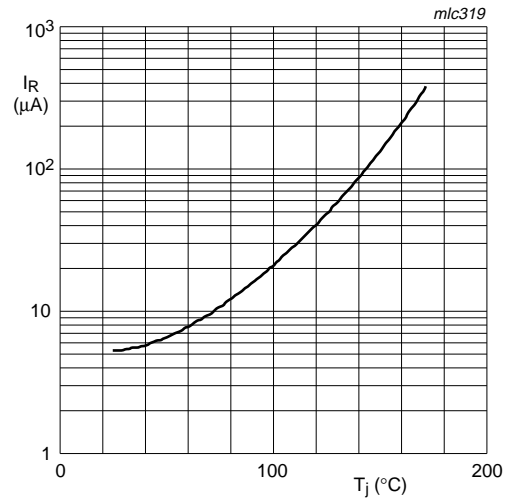


Fig 6. Maximum permissible junction temperature as a function of reverse voltage



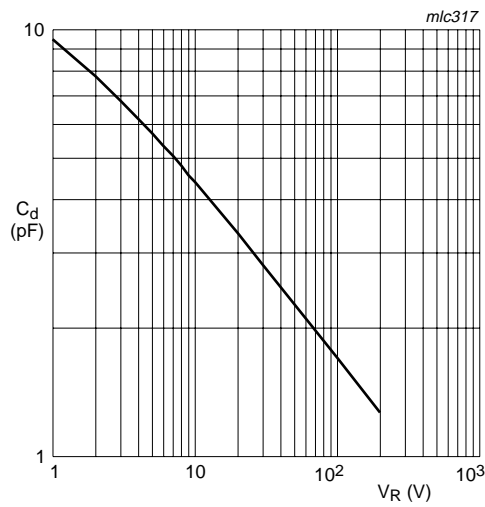
- (1)  $T_j = 175\text{ }^\circ\text{C}$ .
- (2)  $T_j = 25\text{ }^\circ\text{C}$ .

**Fig 7. Forward current as a function of forward voltage; maximum values**



$V_R = V_{RRMmax}$ .

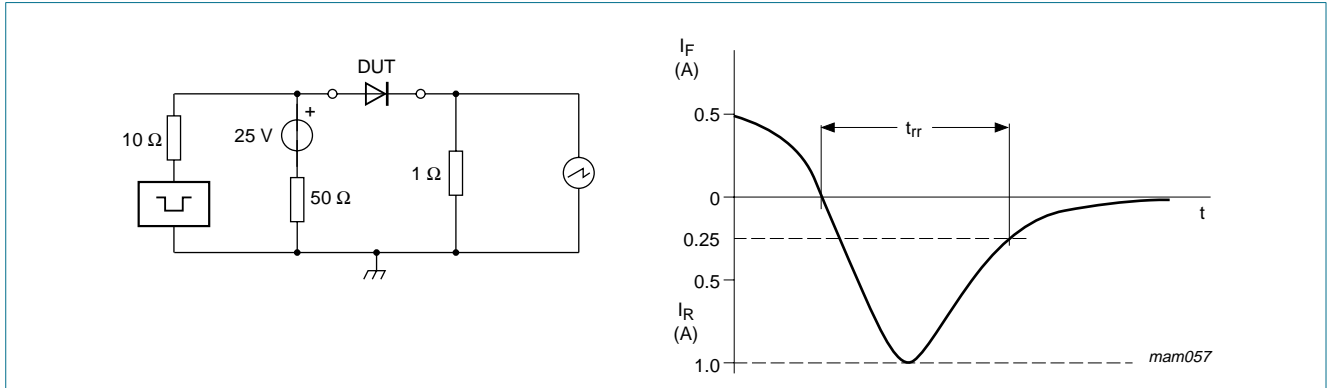
**Fig 8. Reverse current as a function of junction temperature; maximum values**



$f = 1\text{ MHz}; T_j = 25\text{ }^\circ\text{C}$ .

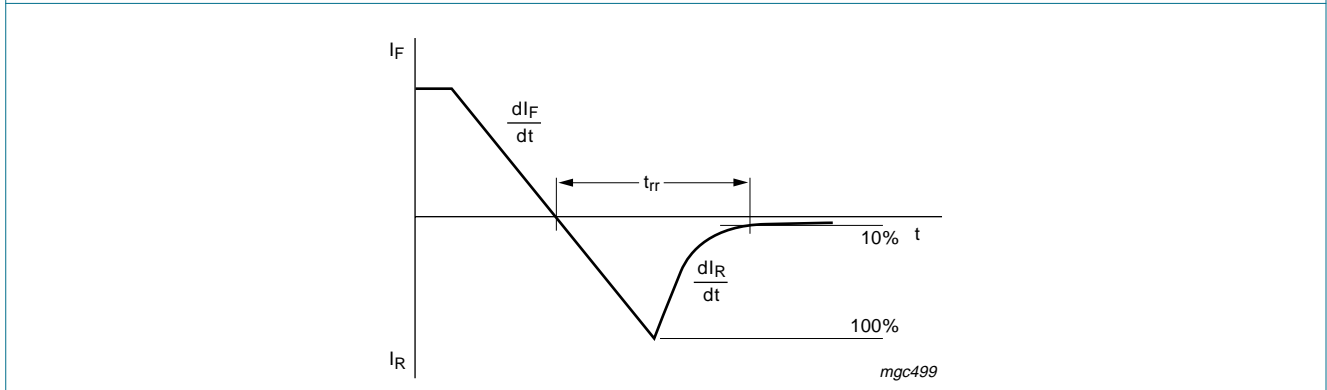
**Fig 9. Diode capacitance as a function of reverse voltage; typical values**

**7. Test information**



Input impedance oscilloscope: 1 MΩ, 22 pF;  $t_r \leq 7$  ns.  
 Source impedance: 50 Ω;  $t_r \leq 15$  ns.

**Fig 10. Test circuit and reverse recovery time waveform and definition**



**Fig 11. Reverse recovery definitions**

## 8. Package outline

Hermetically sealed glass surface mounted package;  
Implotec™(1) technology; 2 connectors

SOD87

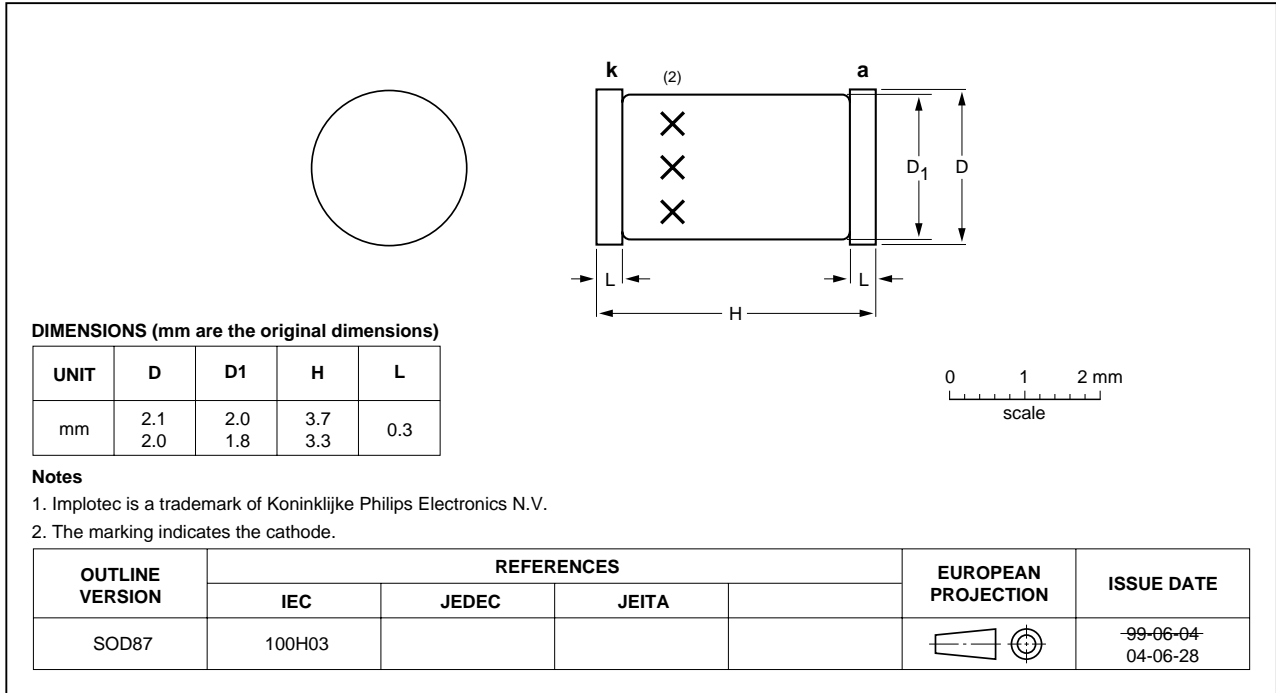


Fig 12. Package outline SOD87

## 9. Mounting

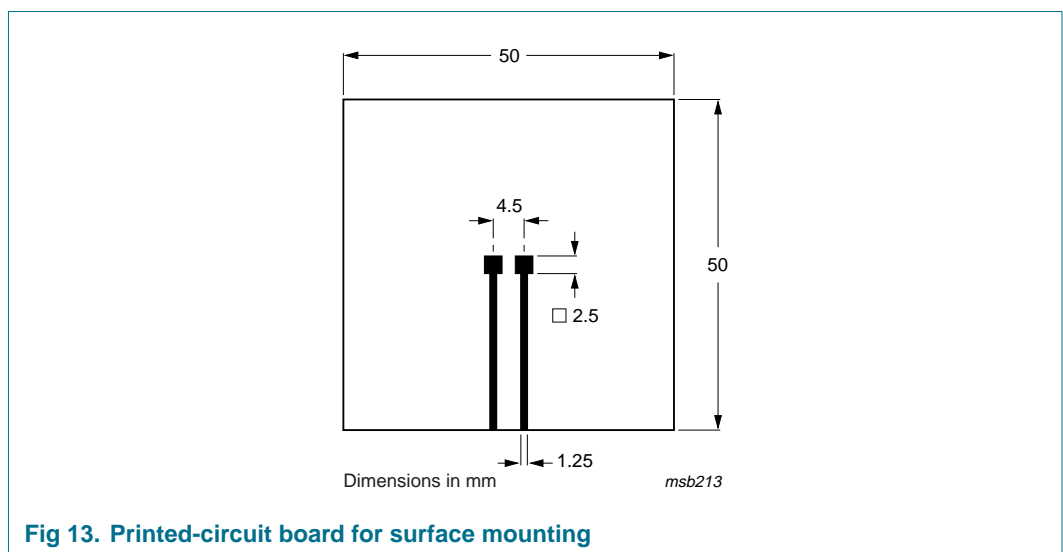


Fig 13. Printed-circuit board for surface mounting

## 10. Revision history

**Table 6: Revision history**

Document ID	Release date	Data sheet status	Change notice	Doc. number	Supersedes
BYD47-20_4	20050204	Product data sheet	-	9397 750 14417	BYD47_SERIES_3
Modifications:					
					<ul style="list-style-type: none"><li>• The format of this data sheet has been redesigned to comply with the new presentation and information standard of Philips Semiconductors.</li><li>• Type numbers BYD47-16 and BYD47-18 removed</li><li>• Title changed to Fast soft-recovery rectifier</li></ul>
BYD47_SERIES_3	19991111	Product specification	-	9397 750 06273	BYD47_2
BYD47_2	19960605	Product specification	-	n.a.	BYD47SERIES_1
BYD47SERIES_1	19941114	Product specification	-	n.a.	-



## 11. Data sheet status

Level	Data sheet status <sup>[1]</sup>	Product status <sup>[2] [3]</sup>	Definition
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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[3] For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

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