

## ST6371 ST6372

## 8-BIT HCMOS MCU FOR **D**IGITAL **C**ONTROLLED **M**ULTI **F**REQUENCY MONITOR

**PRODUCT OVERVIEW** 

- 4.5 to 5.5V supply operating range
- 8MHz Maximum Clock Frequency
- User Program ROM: up to 16076 bytes
- Reserved Test ROM: up to 308 bytes
- Data ROM: user selectable size
- Data RAM:

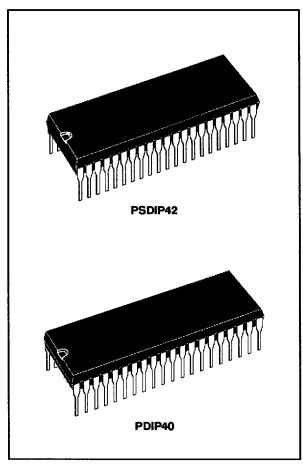
192 bytes

Data EEPROM: up to

512 bytes

(Including 128 bytes for DDC SPI in ST6371 only)

- 42-Pin Shrink Dual in Line Plastic Package
- 40-Pin Dual in Line Plastic Package
- Up to 24 software programmable general purpose Inputs/Outputs, including 8 direct LED driving Outputs
- Three Timers each including an 8-bit counter with a 7-bit programmable prescaler
- Digital Watchdog Function
- Sync Processor for Sync frequency, Polarity and Phase Unlock analysis
- Serial Peripheral Interface (SPI) supporting Multimaster and Slave I<sup>2</sup>C BUS and standard serial protocols
- Serial Peripheral Interface (DDC SPI) supporting Multimaster and Slave I<sup>2</sup>C BUS and standard serial protocols plus VESA DDC Modes 1, 2B and 2AB (ST6371 only)
- One 14-Bit PWM D/A Converter
- Nine 7-Bit PWM D/A Converters
- One 8 bit A/D converter with 8 analog inputs
- Five interrupt vectors (NMI, Timer 3/Ports, Timer 2/VSYNC/SPI, Timer 1/DDC SPI, PWR/ADC/Phase Unlock)
- On-chip clock oscillator
- ST6371/72 is supported by pin-to-pin EPROM and OTP versions.
- The development tool of the ST6371/72 microcontroller consists of the ST6371/72-EMU emulation and development system to be connected via a standard RS232 serial line to an MS-DOS Personal Computer.



Datasheet not available. Contact your ST sales office for further information

April 1995

1/1