

Fast Logic

Gated - Delay Line Oscillator

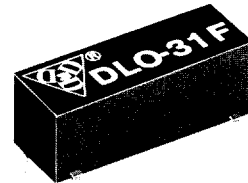
SERIES: DLO-31F

**14 pins DIP
TTL Interfaced**



Features:

- Auto-insertable.
- T²L interfaced.
- Continuous or keyable wavetrain.
- Locked synchronization achieved with random gating signal.
- 14 pins DIP package.
- Low profile module.
- Available from 2 MHz to 40 MHz frequencies.



Specifications:

- Frequencies: See table; others on request.
- Tolerance: ± 2%.
- Temperature coefficient: 100 PPM/°C.
- Operating temperature: 0°-70°C.
- Supply voltage: 5 Vdc ± 5%.
- Supply current: 40 ma.
- Storage temperature: -55°C to 125°C.
- DC parameters: See TTL-Fast Schottky Logic Table on Page 6.
- Inherent delay (T_{DO}): 4 ns typ.

| Part Number | Frequency | Part Number | Frequency |
|-------------|-----------|-------------|-----------|
| DLO-31F-2 | 2 MHz | DLO-31F-10 | 10 MHz |
| DLO-31F-2.5 | 2.5 MHz | DLO-31F-11 | 11 MHz |
| DLO-31F-3 | 3 MHz | DLO-31F-12 | 12 MHz |
| DLO-31F-3.5 | 3.5 MHz | DLO-31F-13 | 13 MHz |
| DLO-31F-4 | 4 MHz | DLO-31F-14 | 14 MHz |
| DLO-31F-4.5 | 4.5 MHz | DLO-31F-15 | 15 MHz |
| DLO-31F-5 | 5 MHz | DLO-31F-20 | 20 MHz |
| DLO-31F-5.5 | 5.5 MHz | DLO-31F-25 | 25 MHz |
| DLO-31F-6 | 6 MHz | DLO-31F-30 | 30 MHz |
| DLO-31F-7 | 7 MHz | DLO-31F-35 | 35 MHz |
| DLO-31F-8 | 8 MHz | DLO-31F-40 | 40 MHz |
| DLO-31F-9 | 9 MHz | | |

