

Engineering Development Model

Power Splitter/Combiner

ADP-ED9774/2

2 Way-0°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



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CASE STYLE : CD542

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C					
Parameter		Min.	Typ.	Max.	Units
Frequency		5		1400	MHz
Isolation	5 - 50 MHz		25		dB
	50 - 700 MHz		30		dB
	700 - 1400 MHz		26		dB
Insertion Loss Above 3.0 dB	5 - 50 MHz		0.20		dB
	50 - 700 MHz		0.40		dB
	700 - 1400 MHz		0.70		dB
Phase Unbalance	5 - 50 MHz		0.041		deg.
	50 - 700 MHz		0.698		deg.
	700 - 1400 MHz		1.628		deg.
Amplitude Unbalance	5 - 50 MHz		0.003		dB
	50 - 700 MHz		0.008		dB
	700 - 1400 MHz		0.135		dB
VSWR	SUM Port		1.10		(:1)
	OUT Ports		1.15		(:1)

Note: Denotes 75 ohm model.

MAXIMUM RATINGS	
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS	
SUM PORT	1
PORT 1	3
PORT 2	4
GND EXT	2,5,6

Functional Diagram

