



STB18NM80, STF18NM80 STP18NM80, STW18NM80

N-channel 800 V, 0.25 Ω 17 A, MDmesh™ Power MOSFET
D²PAK, TO-220FP, TO-220, TO-247

Features

Type	V _{DSS} (@T _{jmax})	R _{DS(on)} max	I _D
STB18NM80	800 V	< 0.295 Ω	17 A
STF18NM80	800 V	< 0.295 Ω	17 A
STP18NM80	800 V	< 0.295 Ω	17 A ⁽¹⁾
STW18NM80	800 V	< 0.295 Ω	17 A

1. Limited only by maximum temperature allowed
- 100% avalanche tested
 - Low input capacitance and gate charge
 - Low gate input resistance

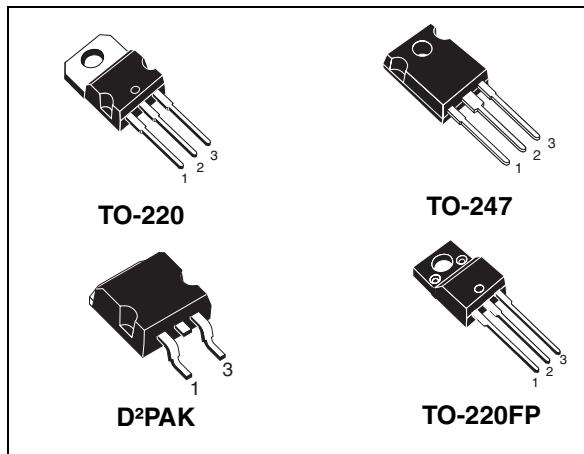


Figure 1. Internal schematic diagram

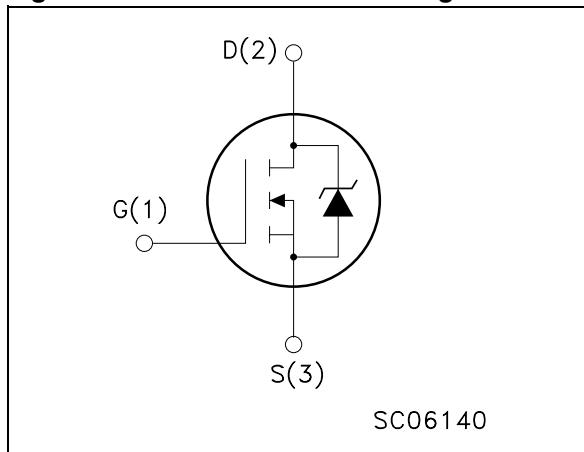


Table 1. Device summary

Order codes	Marking	Package	Packaging
STB18NM80	18NM80	D ² PAK	Tape and reel
STF18NM80	18NM80	TO-220FP	Tube
STP18NM80	18NM80	TO-220	Tube
STW18NM80	18NM80	TO-247	Tube

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1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value				Unit
		TO-220	D ² PAK	TO-247	TO-220FP	
V_{DS}	Drain-source voltage ($V_{GS} = 0$)	800				V
V_{GS}	Gate-source voltage	± 30				V
I_D	Drain current (continuous) at $T_C = 25^\circ\text{C}$	17		17 ⁽¹⁾		A
I_D	Drain current (continuous) at $T_C = 100^\circ\text{C}$	10.71		10.71 ⁽¹⁾		A
$I_{DM}^{(2)}$	Drain current (pulsed)	68		68 ⁽¹⁾		A
P_{TOT}	Total dissipation at $T_C = 25^\circ\text{C}$	190		40		W
V_{ISO}	Insulation withstand voltage (RMS) from all three leads to external heat sink ($t = 1\text{ s}; T_C = 25^\circ\text{C}$)				2500	V
T_{stg}	Storage temperature	-65 to 150				$^\circ\text{C}$
T_j	Max. operating junction temperature	150				$^\circ\text{C}$

1. Limited only by maximum temperature allowed

2. Pulse width limited by safe operating area

Table 3. Thermal data

Symbol	Parameter	Value				Unit
		TO-220	D ² PAK	TO-247	TO-220FP	
$R_{thj-case}$	Thermal resistance junction-case	0.66			3.13	$^\circ\text{C}/\text{W}$
$R_{thj-amb}$	Thermal resistance junction-amb	62.5		50	62.5	$^\circ\text{C}/\text{W}$
$R_{thj-pcb}$	Thermal resistance junction-pcb		30			$^\circ\text{C}/\text{W}$
T_I	Maximum lead temperature for soldering purpose	300				$^\circ\text{C}$

Table 4. Avalanche characteristics

Symbol	Parameter	Max value	Unit
I_{AS}	Avalanche current, repetitive or not-repetitive (pulse width limited by T_j max)	4	A
E_{AS}	Single pulse avalanche energy (starting $T_j = 25^\circ\text{C}$, $I_D = I_{AS}$, $V_{DD} = 50\text{ V}$)	600	mJ

2 Electrical characteristics

($T_{CASE} = 25^\circ\text{C}$ unless otherwise specified)

Table 5. On/off states

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$V_{(BR)DSS}$	Drain-source breakdown voltage	$I_D = 1 \text{ mA}, V_{GS} = 0$	800			V
I_{DSS}	Zero gate voltage drain current ($V_{GS} = 0$)	$V_{DS} = \text{Max rating}, V_{DS} = \text{Max rating}, T_c = 125^\circ\text{C}$			10 100	μA μA
I_{GSS}	Gate body leakage current ($V_{DS} = 0$)	$V_{GS} = \pm 30 \text{ V}$			± 100	nA
$V_{GS(\text{th})}$	Gate threshold voltage	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	3	4	5	V
$R_{DS(\text{on})}$	Static drain-source on-resistance	$V_{GS} = 10\text{V}, I_D = 8.5 \text{ A}$		0.25	0.295	Ω

Table 6. Dynamic

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$g_{fs}^{(1)}$	Forward transconductance	$V_{DS} = 15 \text{ V}, I_D = 8.5 \text{ A}$	-	14	-	S
C_{iss} C_{oss} C_{rss}	Input capacitance Output capacitance Reverse transfer capacitance	$V_{DS} = 50 \text{ V}, f = 1 \text{ MHz}, V_{GS} = 0$	-	2070 210 29	-	pF pF pF
$C_{oss \text{ eq.}}^{(2)}$	Equivalent output capacitance	$V_{GS} = 0, V_{DS} = 0 \text{ to } 640 \text{ V}$	-	316	-	pF
R_G	Gate input resistance	$f = 1 \text{ MHz}$ Gate DC Bias = 0 Test Signal Level = 20 mV Open Drain	-	4	-	Ω
Q_g Q_{gs} Q_{gd}	Total gate charge Gate-source charge Gate-drain charge	$V_{DD} = 640 \text{ V}, I_D = 17 \text{ A}$ $V_{GS} = 10 \text{ V}$ (see Figure 17)	-	70 13 40	-	nC nC nC

1. Pulsed: pulse duration = 300 μs , duty cycle 1.5%
2. $C_{oss \text{ eq.}}$ is defined as a constant equivalent capacitance giving the same charging time as C_{oss} when V_{DS} increases from 0 to 80% V_{DSS}

Table 7. Switching times

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on delay time	$V_{DD} = 400 \text{ V}$, $I_D = 8.5 \text{ A}$,		18		ns
t_r	Rise time	$R_G = 4.7 \Omega$, $V_{GS} = 10 \text{ V}$	-	28	-	ns
$t_{d(off)}$	Turn-off delay time	(see Figure 16 and Figure 21)		96		ns
t_f	Fall time			50		ns

Table 8. Source drain diode

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
I_{SD}	Source-drain current		-		7	A
$I_{SDM}^{(1)}$	Source-drain current (pulsed)				28	A
$V_{SD}^{(2)}$	Forward on voltage	$I_{SD} = 17 \text{ A}$, $V_{GS} = 0$	-		1.6	V
t_{rr}	Reverse recovery time	$I_{SD} = 17 \text{ A}$, $dI/dt = 100 \text{ A}/\mu\text{s}$, $V_{DD} = 100 \text{ V}$,		618		ns
Q_{rr}	Reverse recovery charge	(see Figure 18)	-	9.6		μC
I_{RRM}	Reverse recovery current			31.2		A
t_{rr}	Reverse recovery time	$I_{SD} = 17 \text{ A}$, $dI/dt = 100 \text{ A}/\mu\text{s}$,		822		ns
Q_{rr}	Reverse recovery charge	$V_{DD} = 100 \text{ V}$, $T_j=150^\circ\text{C}$	-	13		μC
I_{RRM}	Reverse recovery current	(see Figure 18)		31.8		A

1. Pulse width limited by safe operating area
2. Pulsed: pulse duration = 300 μs , duty cycle 1.5%

2.1 Electrical characteristics (curves)

Figure 2. Safe operating area for TO-220, D²PAK

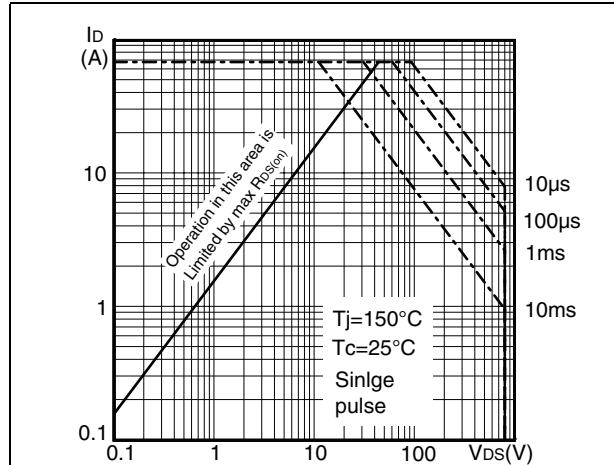


Figure 3. Thermal impedance for TO-220, D²PAK

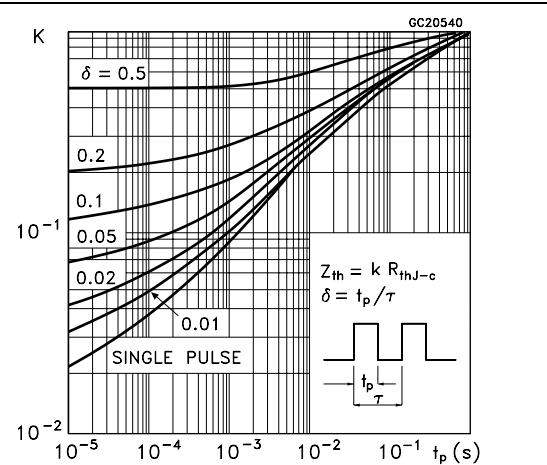


Figure 4. Safe operating area for TO-247

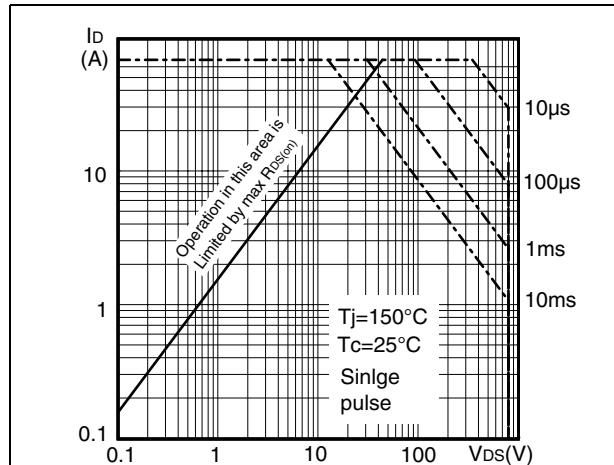


Figure 5. Thermal impedance for TO-247

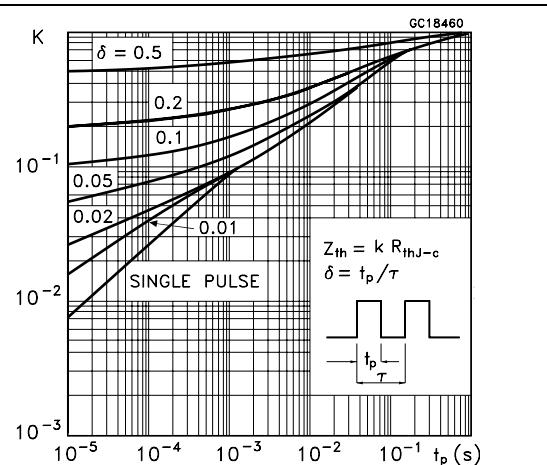


Figure 6. Safe operating area for TO-220FP

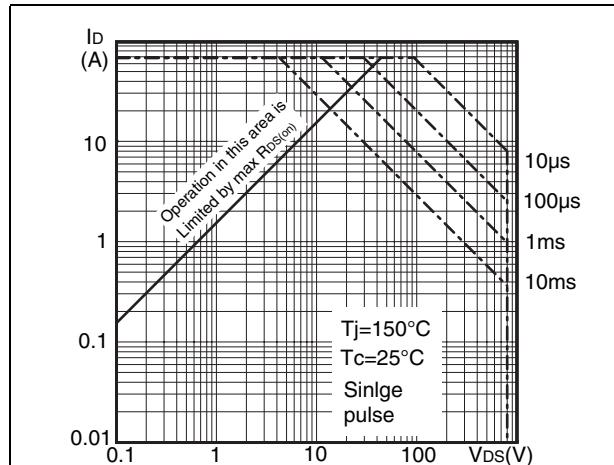


Figure 7. Thermal impedance for TO-220FP

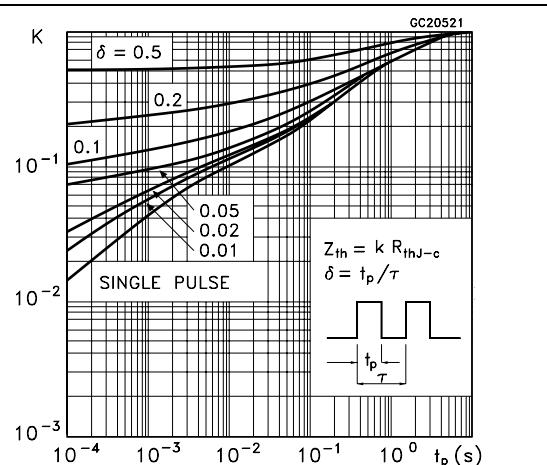


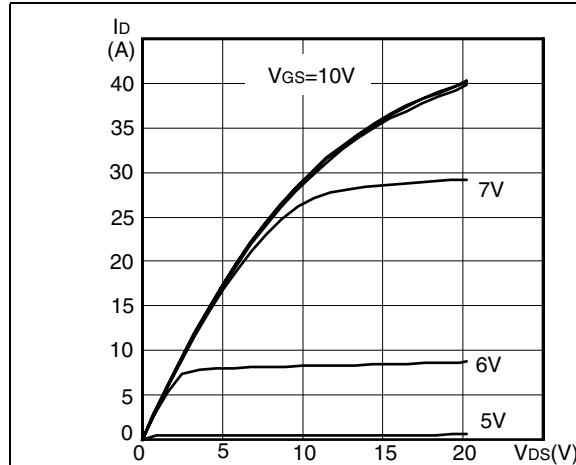
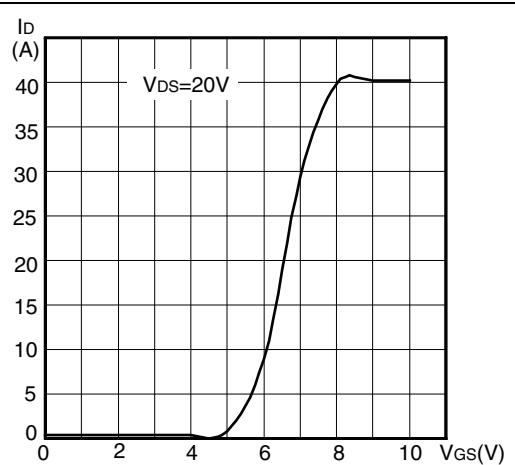
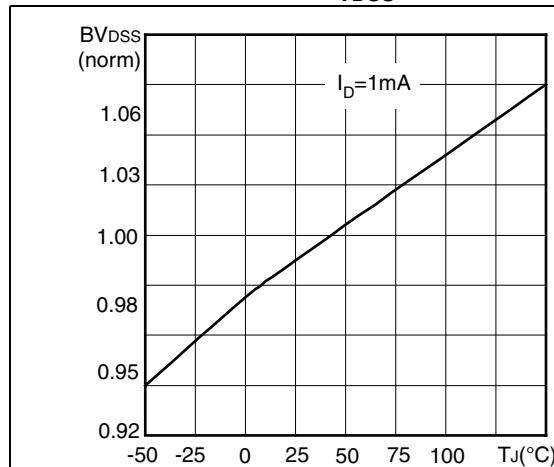
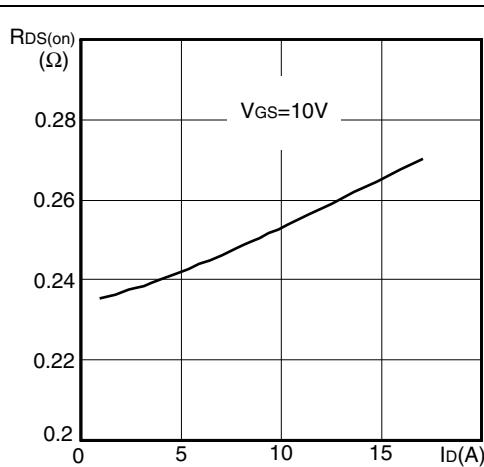
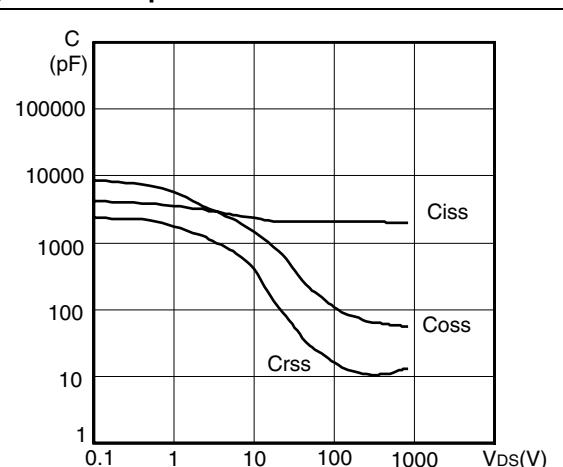
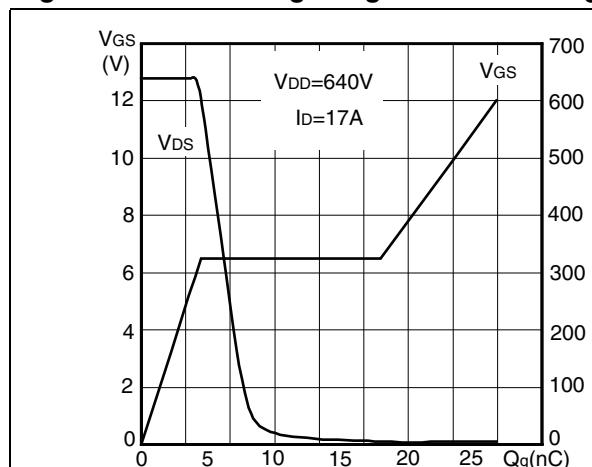
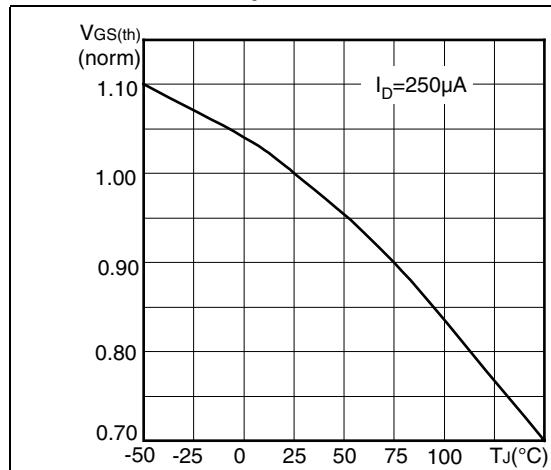
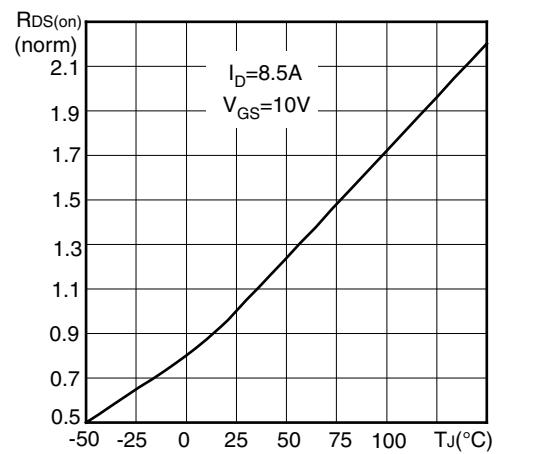
Figure 8. Output characteristics**Figure 9. Transfer characteristics****Figure 10. Normalized B_{VDSS} vs temperature****Figure 11. Static drain-source on-resistance****Figure 12. Gate charge vs gate-source voltage** **Figure 13. Capacitance variations**

Figure 14. Normalized gate threshold voltage vs temperature**Figure 15. Normalized on-resistance vs temperature**

3 Test circuits

Figure 16. Switching times test circuit for resistive load

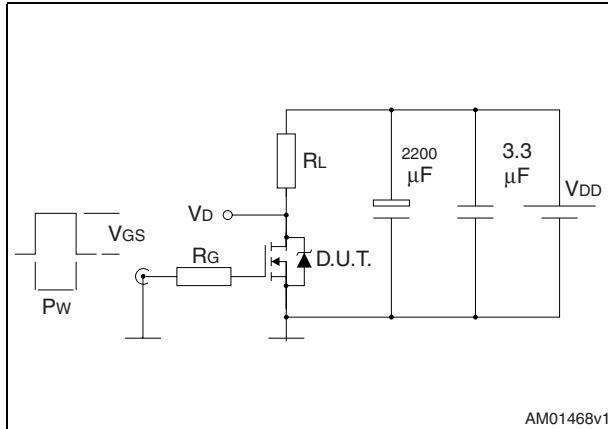


Figure 17. Gate charge test circuit

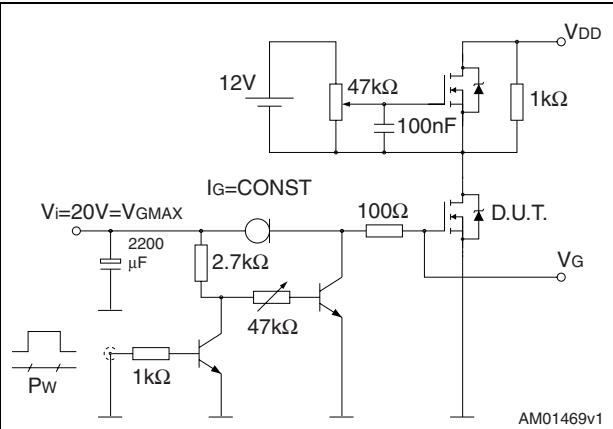


Figure 18. Test circuit for inductive load switching and diode recovery times

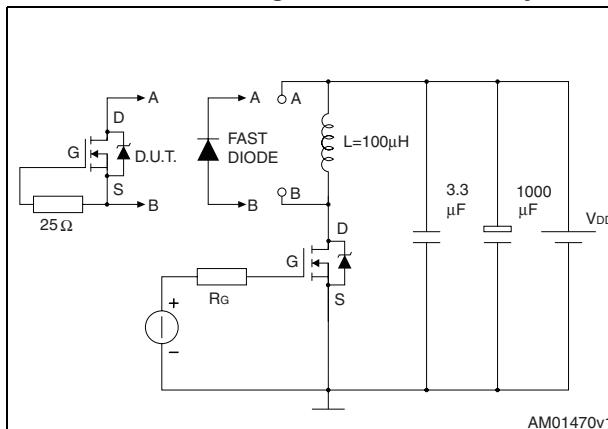


Figure 19. Unclamped inductive load test circuit

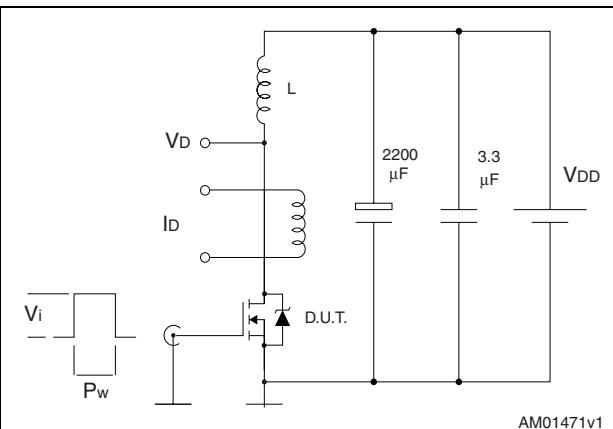


Figure 20. Unclamped inductive waveform

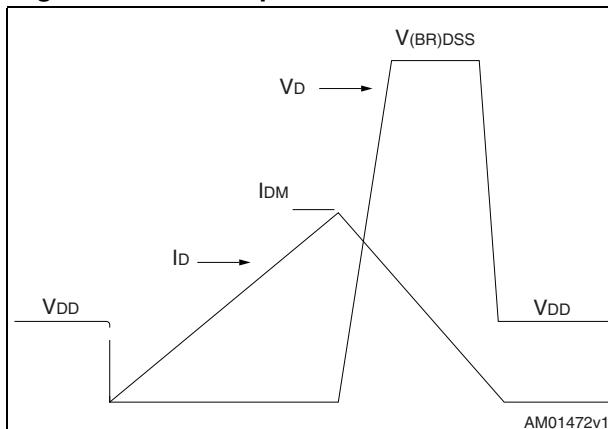
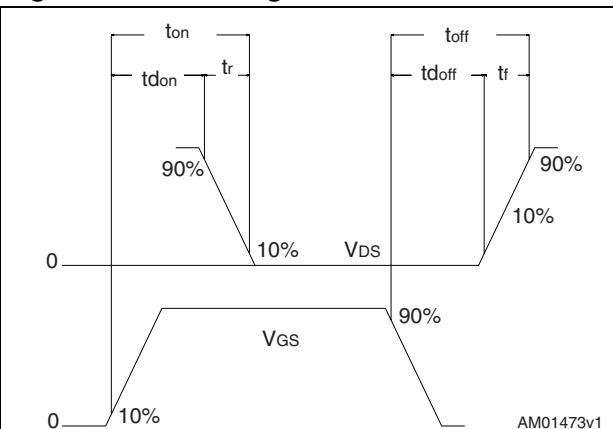


Figure 21. Switching time waveform

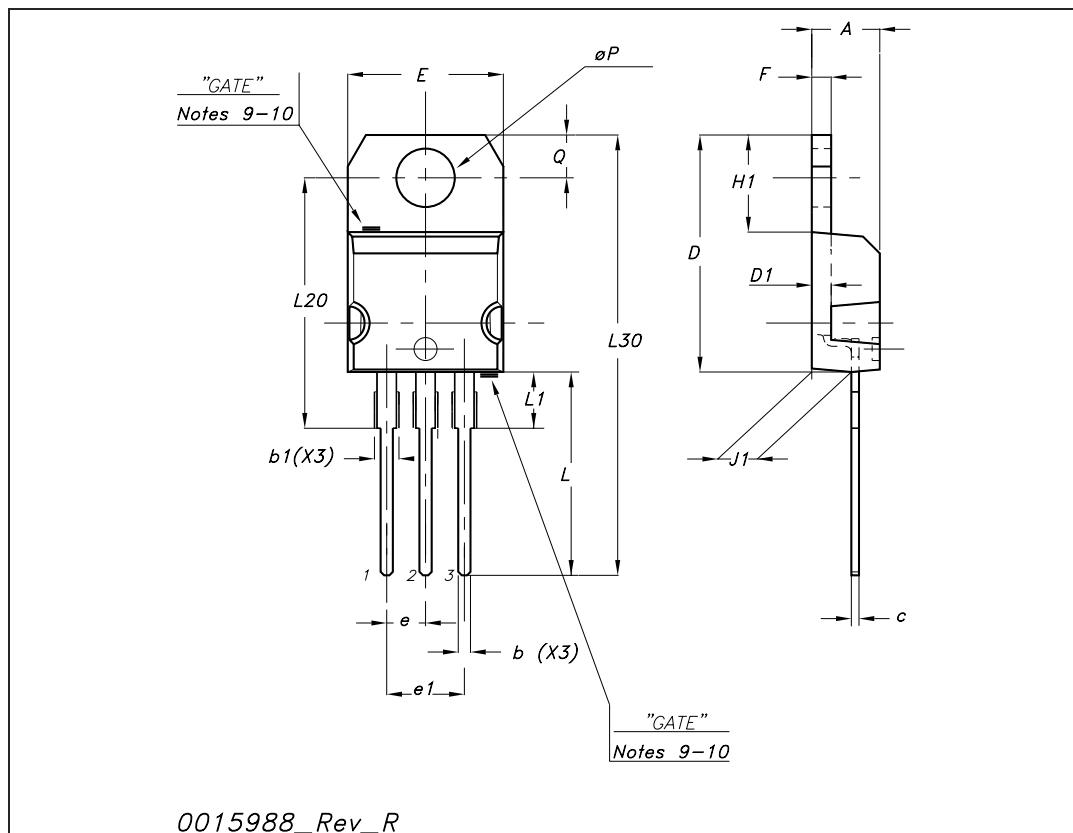


4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com.
ECOPACK is an ST trademark.

TO-220 mechanical data

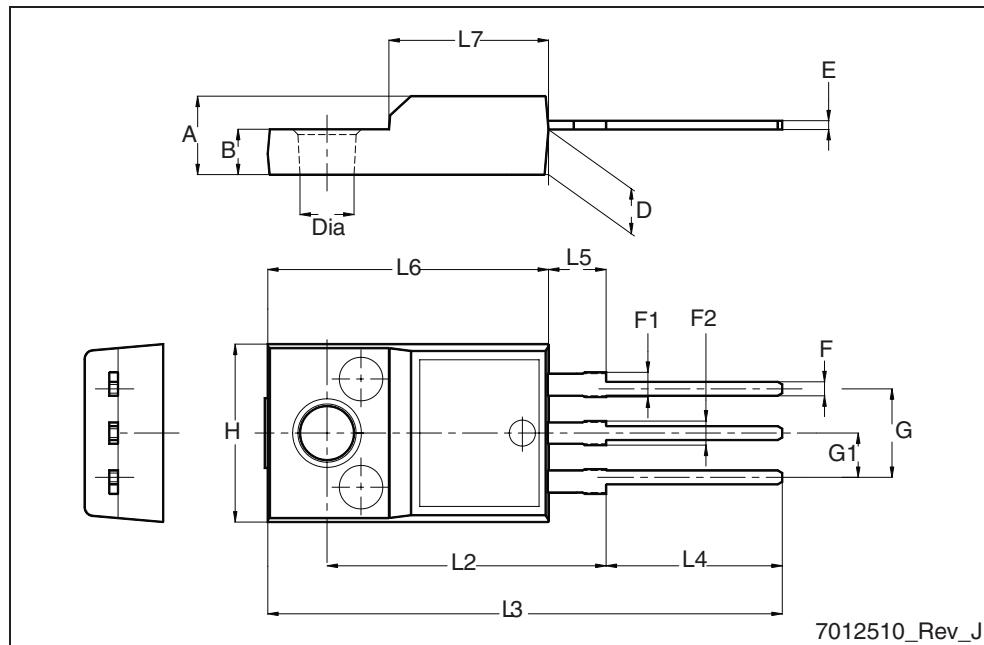
Dim	mm			inch		
	Min	Typ	Max	Min	Typ	Max
A	4.40		4.60	0.173		0.181
b	0.61		0.88	0.024		0.034
b1	1.14		1.70	0.044		0.066
c	0.48		0.70	0.019		0.027
D	15.25		15.75	0.6		0.62
D1		1.27			0.050	
E	10		10.40	0.393		0.409
e	2.40		2.70	0.094		0.106
e1	4.95		5.15	0.194		0.202
F	1.23		1.32	0.048		0.051
H1	6.20		6.60	0.244		0.256
J1	2.40		2.72	0.094		0.107
L	13		14	0.511		0.551
L1	3.50		3.93	0.137		0.154
L20		16.40			0.645	
L30		28.90			1.137	
$\emptyset P$	3.75		3.85	0.147		0.151
Q	2.65		2.95	0.104		0.116



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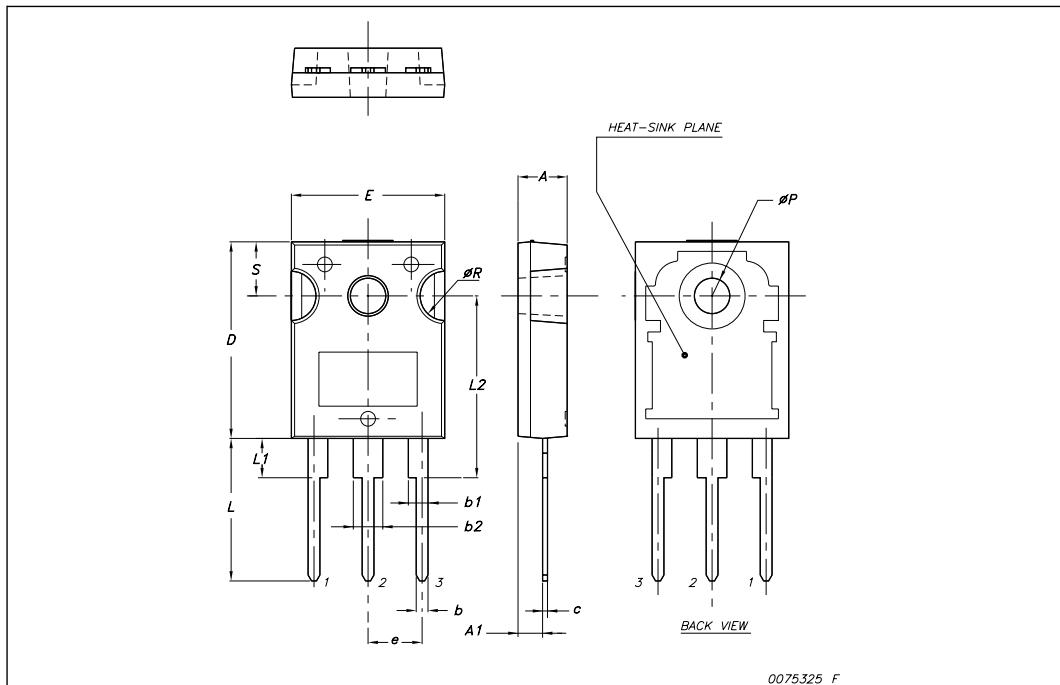
TO-220FP mechanical data

Dim.	mm		
	Min.	Typ.	Max.
A	4.4		4.6
B	2.5		2.7
D	2.5		2.75
E	0.45		0.7
F	0.75		1
F1	1.15		1.70
F2	1.15		1.5
G	4.95		5.2
G1	2.4		2.7
H	10		10.4
L2		16	
L3	28.6		30.6
L4	9.8		10.6
L5	2.9		3.6
L6	15.9		16.4
L7	9		9.3
Dia	3		3.2



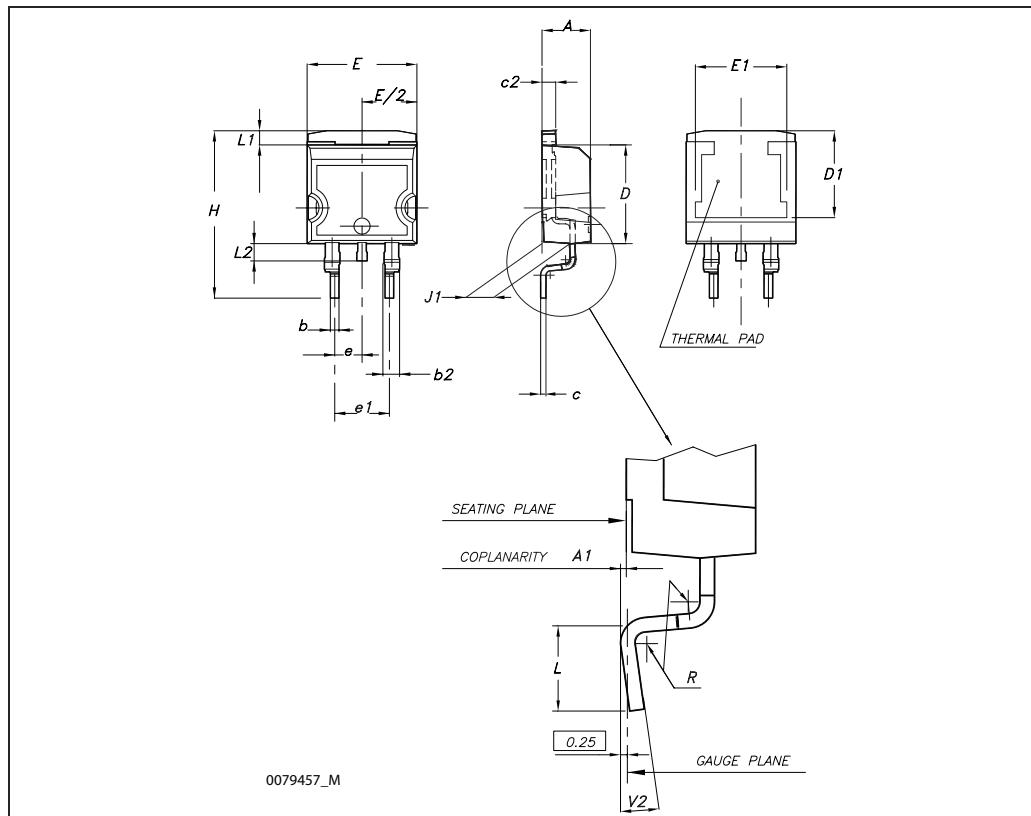
TO-247 Mechanical data

Dim.	mm.		
	Min.	Typ	Max.
A	4.85		5.15
A1	2.20		2.60
b	1.0		1.40
b1	2.0		2.40
b2	3.0		3.40
c	0.40		0.80
D	19.85		20.15
E	15.45		15.75
e		5.45	
L	14.20		14.80
L1	3.70		4.30
L2		18.50	
ϕP	3.55		3.65
ϕR	4.50		5.50
S		5.50	

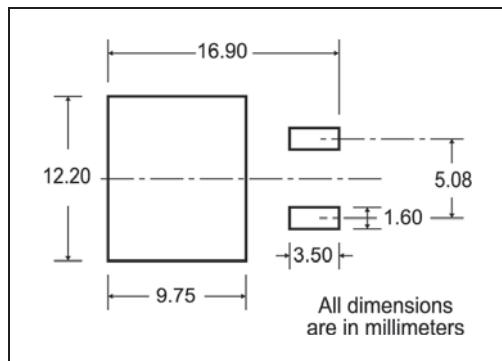


D²PAK (TO-263) mechanical data

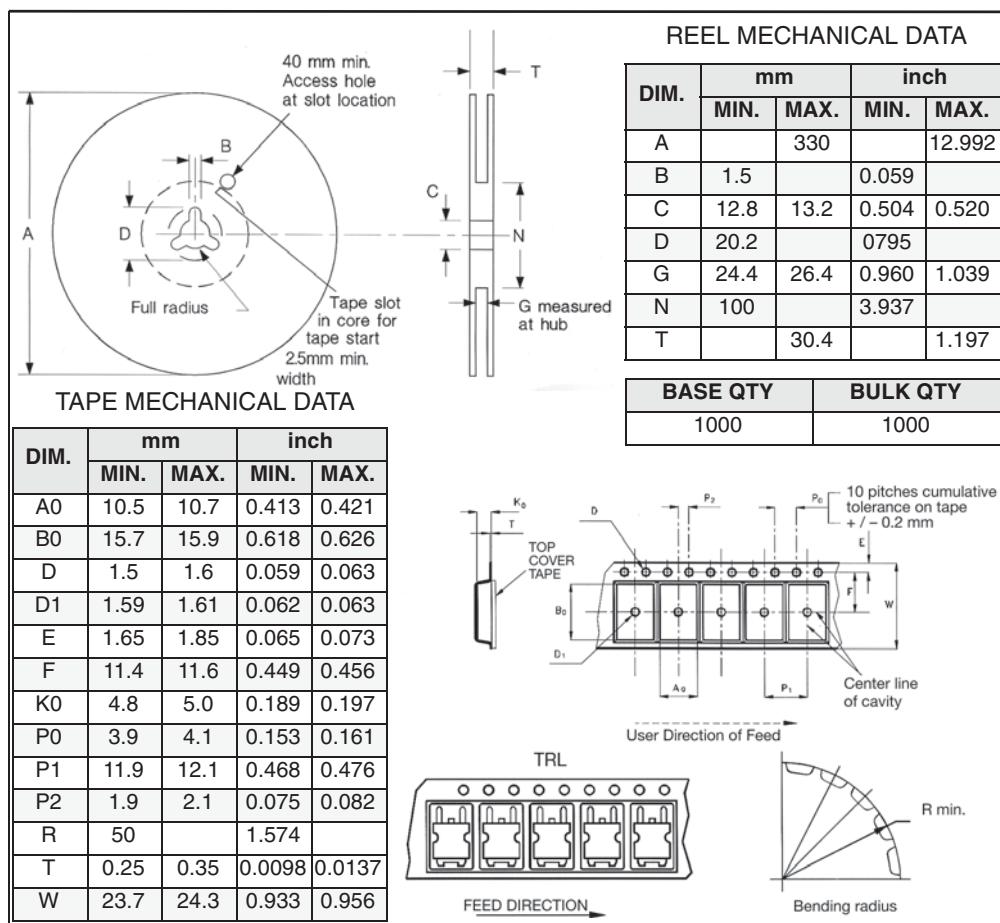
Dim	mm			inch		
	Min	Typ	Max	Min	Typ	Max
A	4.40		4.60	0.173		0.181
A1	0.03		0.23	0.001		0.009
b	0.70		0.93	0.027		0.037
b2	1.14		1.70	0.045		0.067
c	0.45		0.60	0.017		0.024
c2	1.23		1.36	0.048		0.053
D	8.95		9.35	0.352		0.368
D1	7.50			0.295		
E	10		10.40	0.394		0.409
E1	8.50			0.334		
e		2.54			0.1	
e1	4.88		5.28	0.192		0.208
H	15		15.85	0.590		0.624
J1	2.49		2.69	0.099		0.106
L	2.29		2.79	0.090		0.110
L1	1.27		1.40	0.05		0.055
L2	1.30		1.75	0.051		0.069
R		0.4			0.016	
V2	0°		8°	0°		8°



5 Packaging mechanical data

D²PAK FOOTPRINT

TAPE AND REEL SHIPMENT



6 Revision history

Table 9. Document revision history

Date	Revision	Changes
25-Feb-2009	1	First release
07-Apr-2009	2	<i>Section 4: Package mechanical data has been modified</i>
20-Apr-2009	3	$R_{DS(on)}$ max value has been corrected
09-Sep-2009	4	Document status promoted from preliminary data to datasheet

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