

SANYO Semiconductors DATA SHEET

SCH1305

P-Channel Silicon MOSFET

General-Purpose Switching Device Applications

Featurest

- Low ON-resistance.
- High-speed switching.
- 1.8V drive.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		-12	V
Gate-to-Source Voltage	VGSS		±10	V
Drain Current (DC)	ΙD		-1.5	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	-6	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (900mm ² X0.8mm)	0.7	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Lloit
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=-1mA, VGS=0	-12			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =-12V, V _{GS} =0			-10	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±8V, V _{DS} =0			±10	μΑ
Cutoff Voltage	VGS(off)	VDS=-6V, ID=-1mA	-0.3		-1.0	V
Forward Transfer Admittance	yfs	V _{DS} =-6V, I _D =-0.8A	1.08	1.8		S
	R _{DS} (on)1	I _D =-0.8A, V _G S=-4.5V		235	310	mΩ
Static Drain-to-Source On-State Resistance	RDS(on)2	ID=-0.4A, VGS=-2.5V		335	470	mΩ
	RDS(on)3	I _D =-0.1A, V _G S=-1.8V		445	670	mΩ
Input Capacitance	Ciss	V _{DS} =-6V, f=1MHz		160		pF
Output Capacitance	Coss	V _{DS} =-6V, f=1MHz		45		pF
Reverse Transfer Capacitance	Crss	V _{DS} =-6V, f=1MHz		35		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit		11		ns
Rise Time	tr	See specified Test Circuit		45		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		29		ns
Fall Time	t _f	See specified Test Circuit		30		ns

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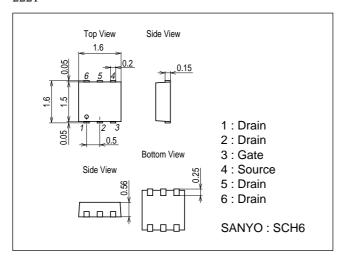
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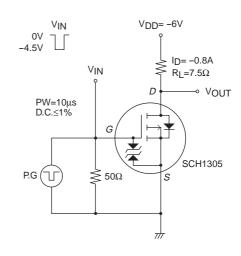
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Total Gate Charge	Qg	V _{DS} =-6V, V _{GS} =-4.5V, I _D =-1.5A		2.6		nC
Gate-to-Source Charge	Qgs	V _{DS} =-6V, V _{GS} =-4.5V, I _D =-1.5A		0.25		nC
Gate-to-Drain "Miller" Charge	Qgd	V _D S=-6V, V _G S=-4.5V, I _D =-1.5A		0.65		nC
Diode Forward Voltage	V _{SD}	I _S =-1.5A, V _G S=0		-0.92	-1.5	V

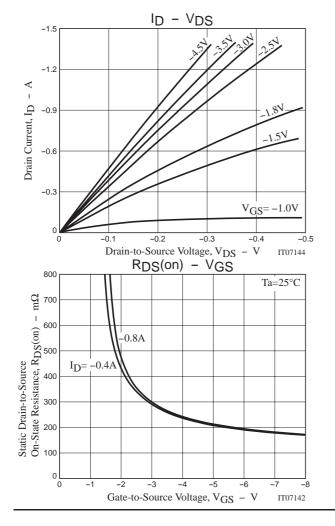
Package Dimensions

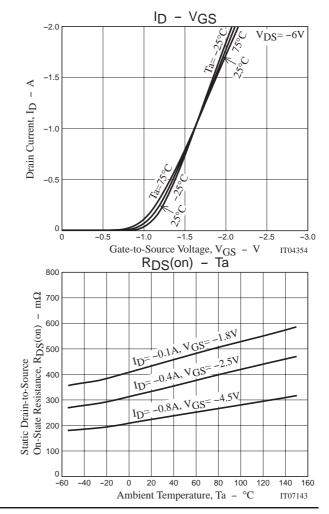
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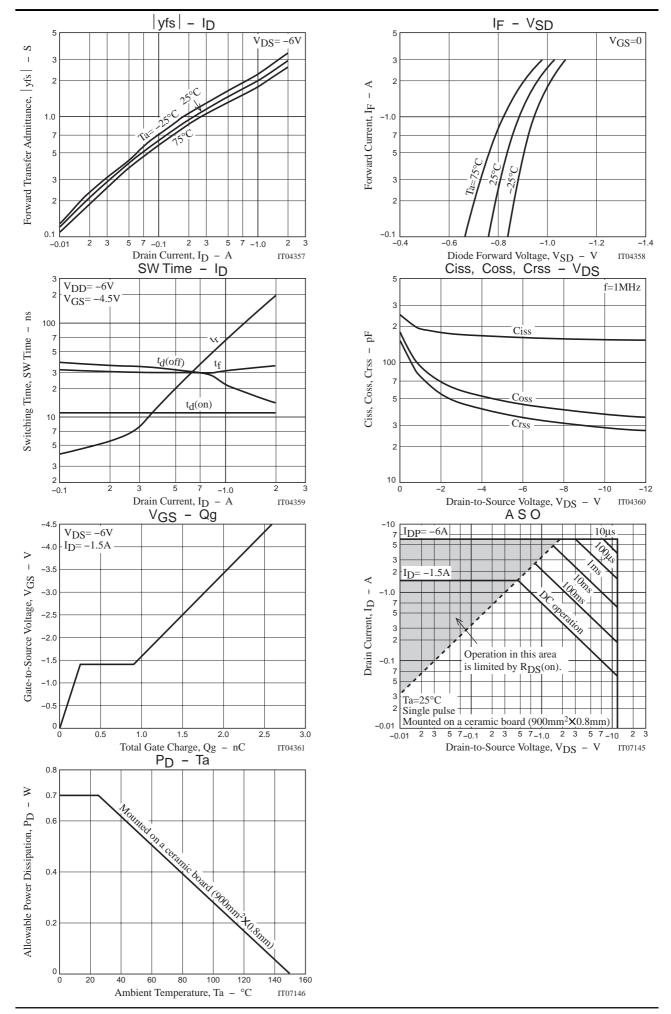


Switching Time Test Circuit









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