

NOTES:

1.0 PINS NOT ELECTRICALLY CONNECTED ARE OMITTED.

ELECTRICAL SPECIFICATIONS:

1.0 INDUCTANCE: P1-J1=P2-J2 ; P4-J4=P5-J5

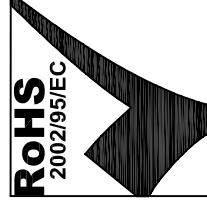
: 65 uH 0.01V, 10 KHz, nominal.

2.0 DC RESISTANCE: P1-J1
P2-J2
P4-J4
P5-J5

: .5 ohm MAX
: .5 ohm MAX
: .5 ohm MAX
: .5 ohm MAX

3.0 COMMON MODE:

FREQUENCY (MHZ)	CMA (-dB)	
	TYPICAL	MIN
1	8	5
5	17	15
20	23	15
70	25	20
200	22	17
500	11	6



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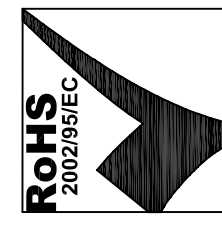
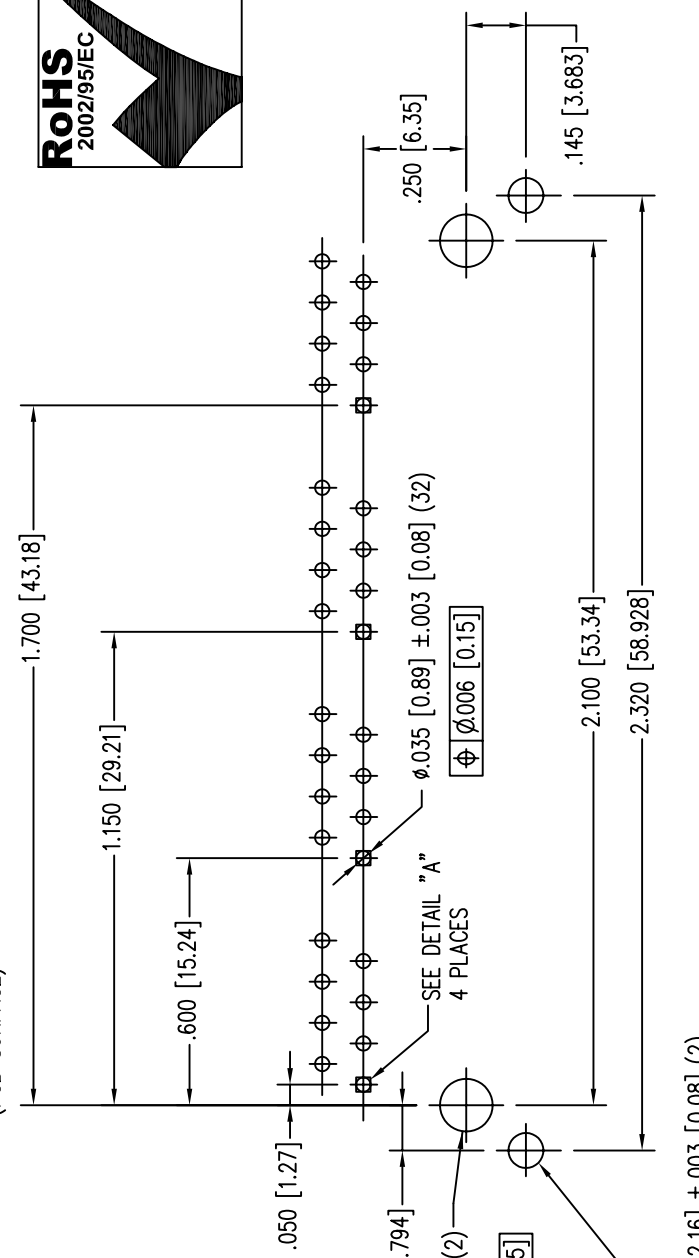
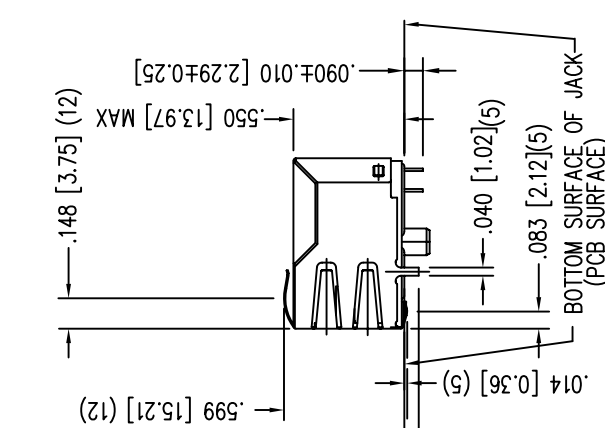
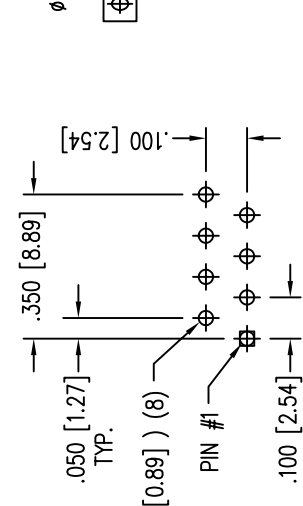
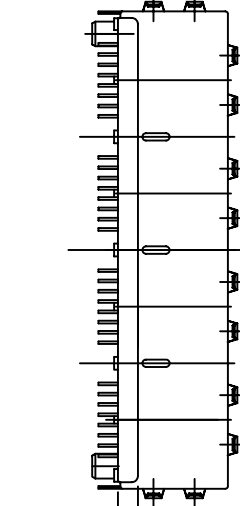
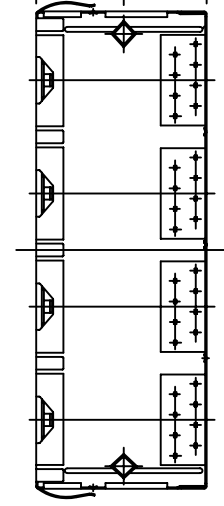
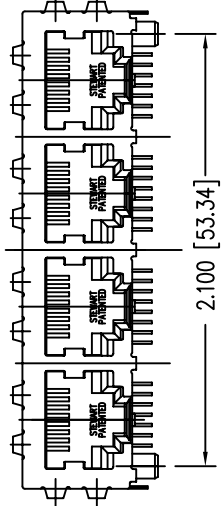
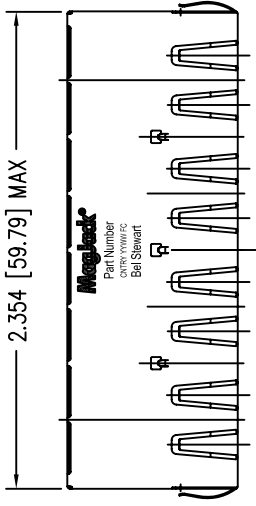
<http://www.stewartconnector.com>

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SHEET 1 OF 3

DRAWING NO. SI-60043-F

REV. 13



- NOTES:
- CONNECTOR MATERIALS:
HOUSING: THERMOPLASTIC UL94 V-0
CONTACT/SHIELD: COPPER ALLOY
SHIELD PLATING: NICKEL OR TIN
CONTACT PLATING: SELECTIVE GOLD,
50 MICRO-INCHES MIN. IN CONTACT AREA.
 - PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED.
SEE ELECTRICAL DRAWING FOR OMITTED PINS.
 - TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
 - ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]
 - REFLOW AND WAVE SOLDER COMPATIBLE -260°C FOR
10 SECONDS MAX.
 - ALL LEDS AND SOLDER TAILS TO BE TRIMED TO .090 [2.29]
 - REAR SHIELD SOLDER TAILS TO BE TRIMED FLUSH AND
SIDE TAILS TO BE TRIMED TO .070 [1.78]

DETAIL "A"
TYPICAL HOLE LAYOUT

P.C.B. RECOMMENDED HOLE LAYOUT
SEEN FROM COMPONENT SIDE

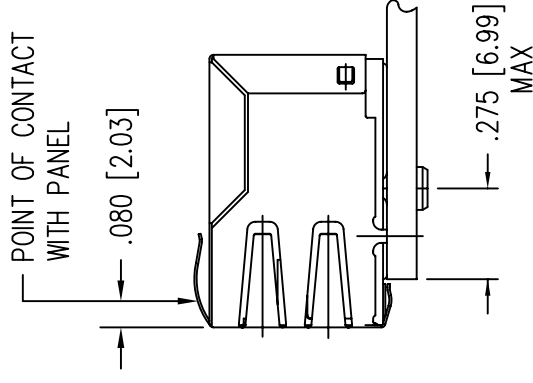
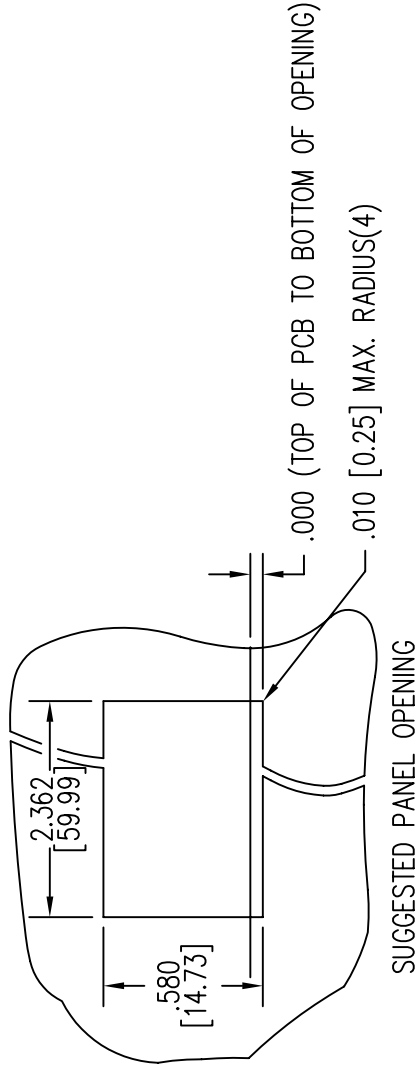
ALL CENTERLINE DIMENSIONS ARE BASIC.

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SHEET 2 OF 3
DRAWING NO. SI-60043-F
REV. 08



1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ± 0.005 [0.13]

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SHEET 3 OF 3
 DRAWING NO. SI-60043-E
 REV. 04