

**Features**

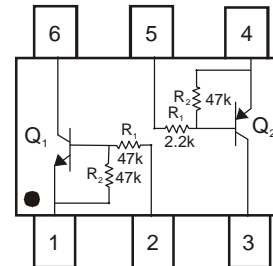
- Epitaxial Planar Die Construction
- Surface Mount Package Suited for Automated Assembly
- Simplifies Circuit Design and Reduces Board Space
- **Lead Free/RoHS Compliant (Note 1)**
- **"Green" Device (Note 2)**



SOT-563

**Mechanical Data**

- Case: SOT-563
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish — Matte Tin Finish annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.005 grams (approximate)



Reference	Device Type	R1(Nom)	R2(Nom)
Q <sub>1</sub>	NPN	47kΩ	47kΩ
Q <sub>2</sub>	PNP	2.2 kΩ	47kΩ

**Maximum Ratings, Total Device** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3)	P <sub>D</sub>	300	mW
Thermal Resistance, Junction to Ambient Air (Note 3)	R <sub>θJA</sub>	417	°C/W
Operating and Storage Temperature Range	T <sub>j</sub> , T <sub>STG</sub>	-55 to +150	°C

**Maximum Ratings, Pre-Biased NPN Transistor, Q<sub>1</sub>** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	50	V
Collector-Emitter Voltage	V <sub>CEO</sub>	50	V
Emitter-Base Voltage	V <sub>EBO</sub>	10	V
Input Voltage	V <sub>IN</sub>	-10 to +40	V
Output Current (DC)	I <sub>O</sub>	100	mA
Peak Collector Current	I <sub>CM</sub>	100	mA

**Maximum Ratings, Pre-Biased PNP Transistor, Q<sub>2</sub>** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-50	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-50	V
Emitter-Base Voltage	V <sub>EBO</sub>	-10	V
Input Voltage	V <sub>IN</sub>	-12 to +5	V
Output Current (DC)	I <sub>O</sub>	-100	mA
Peak Collector Current	I <sub>CM</sub>	-100	mA

- Notes:
1. No purposefully added lead.
  2. Diodes Inc.'s "Green" policy can be found on our website at [http://www.diodes.com/products/lead\\_free/index.php](http://www.diodes.com/products/lead_free/index.php).
  3. Device mounted on FR-4 PCB; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at <http://www.diodes.com/datasheets/ap02001.pdf>.

## Electrical Characteristics, Pre-Biased NPN Transistor, Q<sub>1</sub> @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Cut-Off Current	I <sub>CBO</sub>	-	-	100	nA	V <sub>CB</sub> = 50V, I <sub>E</sub> = 0A
Collector-Emitter Cut-Off Current	I <sub>CEO</sub>	-	-	1 50	μA	V <sub>CE</sub> = 30V, I <sub>B</sub> = 0A V <sub>CE</sub> = 30V, I <sub>B</sub> = 0A, T <sub>A</sub> = 150°C
Emitter-Base Cut-Off Current	I <sub>EBO</sub>	-	-	90	μA	V <sub>EB</sub> = 5V, I <sub>C</sub> = 0A
Input Voltage	V <sub>I(off)</sub>	-	1.2	0.8	V	V <sub>CE</sub> = 5V, I <sub>O</sub> = 100μA
	V <sub>I(on)</sub>	3	1.6	-	V	V <sub>CE</sub> = 0.3V, I <sub>O</sub> = 2mA
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	-	-	0.15	V	I <sub>C</sub> /I <sub>B</sub> = 10mA/0.5mA
DC Current Gain	h <sub>FE</sub>	80	-	-	-	V <sub>CE</sub> = 5V, I <sub>C</sub> = 5mA
Input Resistance	R <sub>1</sub>	33	47	61	kΩ	-
Resistance Ratio	R <sub>2</sub> /R <sub>1</sub>	0.8	1	1.2	-	-
Collector Capacitance	C <sub>C</sub>	-	-	2.5	pF	V <sub>CB</sub> = 10V, I <sub>E</sub> = 0, f = 1MHz

## Electrical Characteristics, Pre-Biased PNP Transistor, Q<sub>2</sub> @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Cut-Off Current	I <sub>CBO</sub>	-	-	-100	nA	V <sub>CB</sub> = -50V, I <sub>E</sub> = 0A
Collector-Emitter Cut-Off Current	I <sub>CEO</sub>	-	-	-1 -50	μA	V <sub>CE</sub> = -30V, I <sub>B</sub> = 0A V <sub>CE</sub> = -30V, I <sub>B</sub> = 0A, T <sub>A</sub> = 150°C
Emitter-Base Cut-Off Current	I <sub>EBO</sub>	-	-	-180	μA	V <sub>EB</sub> = -5V, I <sub>C</sub> = 0A
Input Voltage	V <sub>I(off)</sub>	-	-0.6	-0.5	V	V <sub>CC</sub> = -5V, I <sub>O</sub> = -100μA
	V <sub>I(on)</sub>	-1.1	-0.75	-	V	V <sub>O</sub> = -0.3V, I <sub>O</sub> = -5mA
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	-	-	-0.1	V	I <sub>C</sub> /I <sub>B</sub> = -5mA/-0.25mA
DC Current Gain	h <sub>FE</sub>	100	-	-	-	V <sub>CE</sub> = -5V, I <sub>C</sub> = -10mA
Input Resistance	R <sub>1</sub>	1.54	2.2	2.86	kΩ	-
Resistance Ratio	R <sub>2</sub> /R <sub>1</sub>	17	21	26	-	-
Collector Capacitance	C <sub>C</sub>	-	-	3.0	pF	V <sub>CB</sub> = -10V, I <sub>E</sub> = 0, f = 1MHz

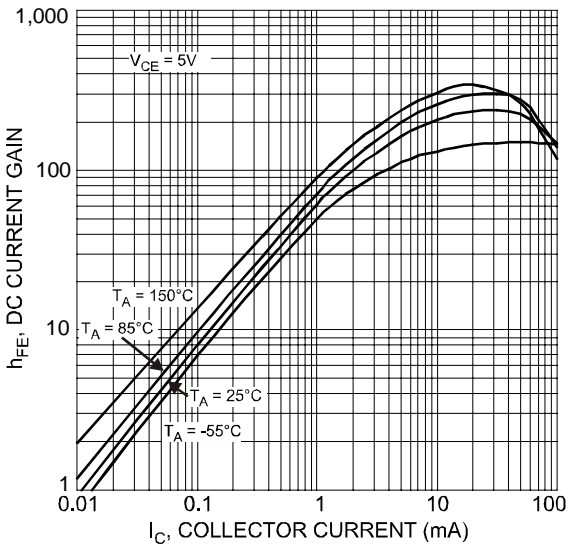


Fig. 1 Typical DC Current Gain vs. Collector Current (Q1, NPN)

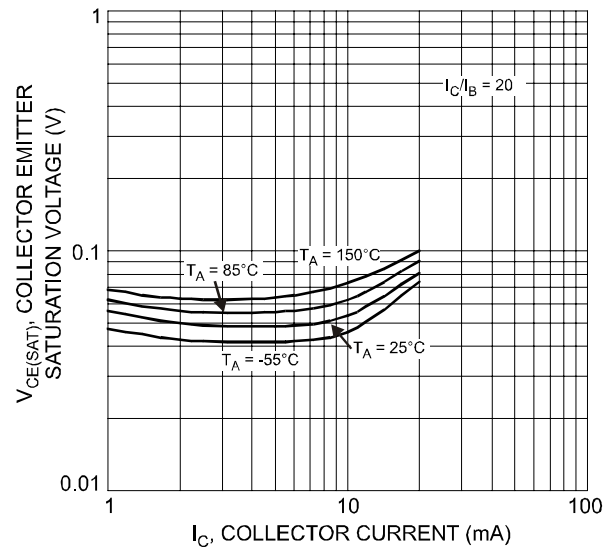


Fig. 2 Typical Collector-Emitter Saturation Voltage vs. Collector Current (Q1, NPN)

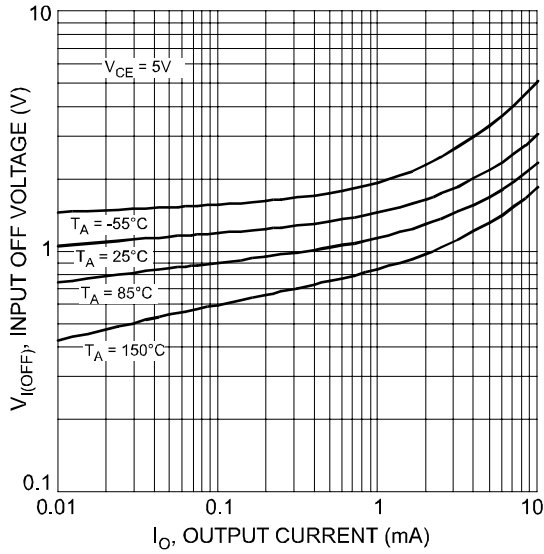


Fig. 3 Typical Input Off Voltage vs. Output Current (Q1, NPN)

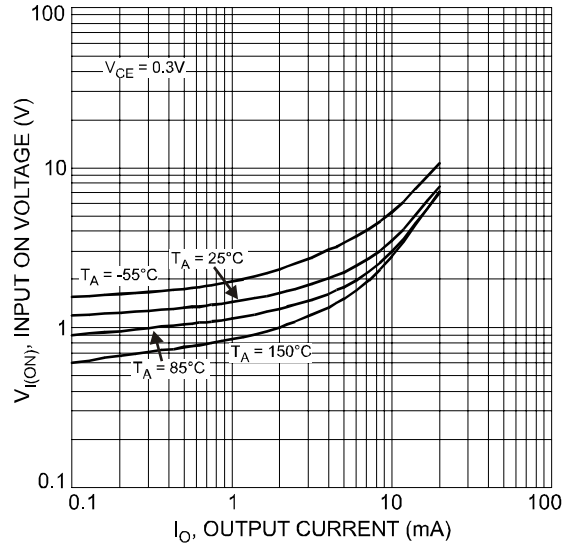


Fig. 4 Typical Input ON Voltage vs. Output Current (Q1, NPN)

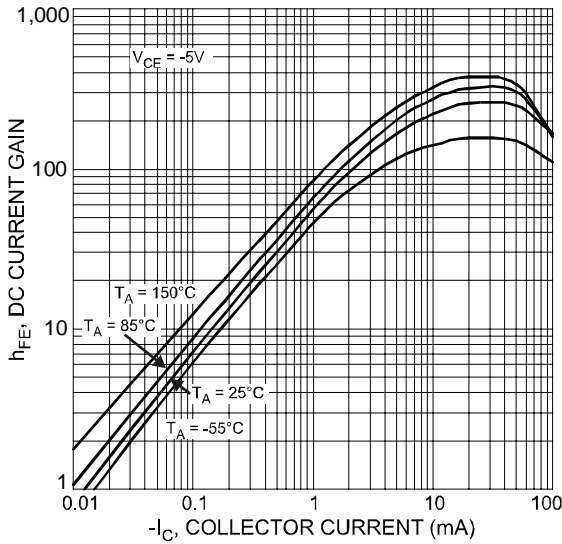


Fig. 5 Typical DC Current Gain vs. Collector Current (Q2, PNP)

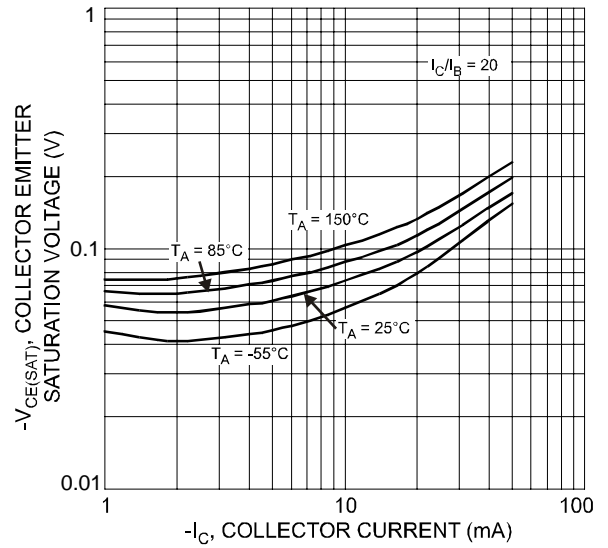


Fig. 6 Typical Collector-Emitter Saturation Voltage vs. Collector Current (Q2, PNP)

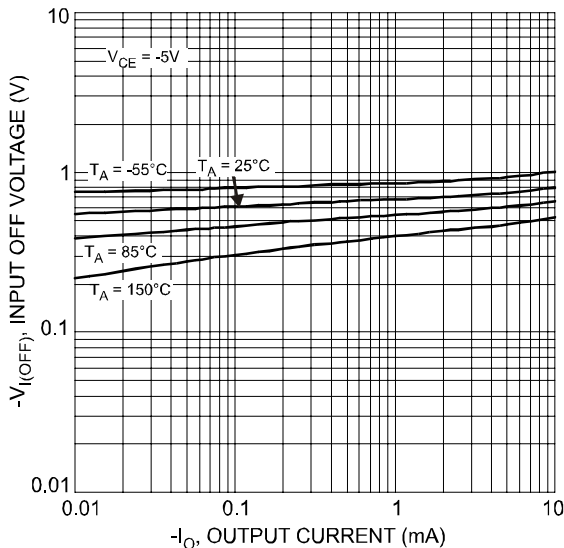


Fig. 7 Typical Input Off Voltage vs. Output Current (Q2, PNP)

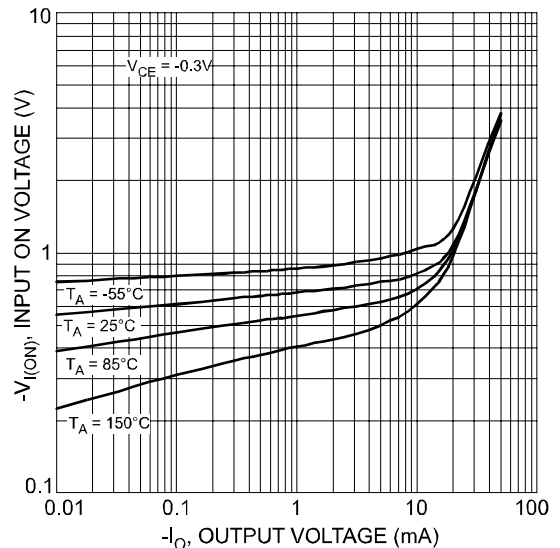


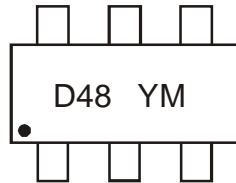
Fig. 8 Typical Input ON Voltage vs. Output Current (Q2, PNP)

## Ordering Information (Note 4)

Device	Packaging	Shipping
DEMD48-7	SOT-563	3000/Tape & Reel

Note: 4. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

## Marking Information



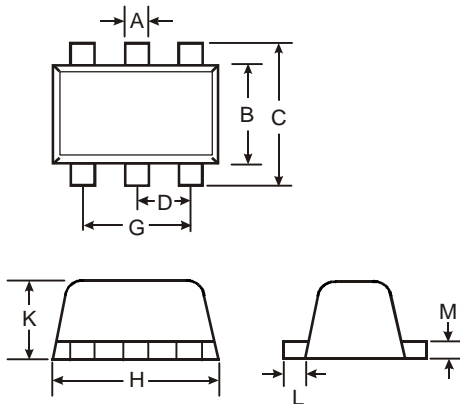
D48 = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year ex: U = 2007  
 M = Month ex: 9 = September

### Date Code Key

Year	2007	2008	2009	2010	2011	2012
Code	U	V	W	X	Y	Z

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

## Package Outline Dimensions



SOT-563			
Dim	Min	Max	Typ
A	0.15	0.30	0.20
B	1.10	1.25	1.20
C	1.55	1.70	1.60
D	-	-	0.50
G	0.90	1.10	1.00
H	1.50	1.70	1.60
K	0.55	0.60	0.60
L	0.10	0.30	0.20
M	0.10	0.18	0.11
All Dimensions in mm			

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