

**G2313****P-CHANNEL ENHANCEMENT MODE POWER MOSFET**

BV <sub>DSS</sub>	-20V
R <sub>DS(ON)</sub>	160mΩ
I <sub>D</sub>	-2.5A

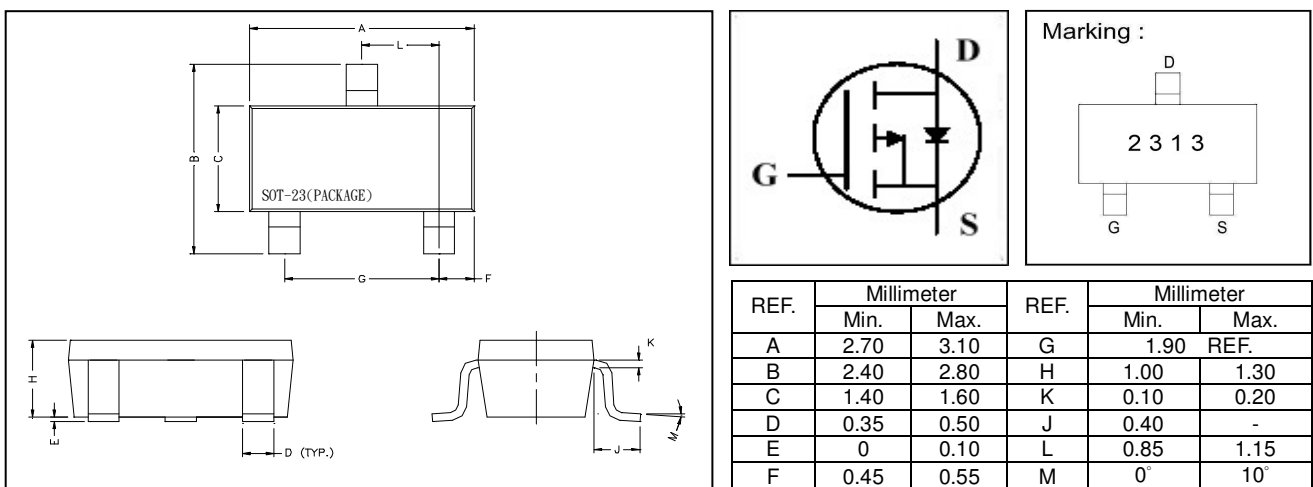
**Description**

The G2313 provide the designer with best combination of fast switching, low on-resistance and cost-effectiveness.

The G2313 is universally preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

**Features**

- \*Simple Drive Requirement
- \*Small Package Outline

**Package Dimensions****Absolute Maximum Ratings**

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V <sub>DS</sub>	-20	V
Gate-Source Voltage	V <sub>GS</sub>	±12	V
Continuous Drain Current <sup>3</sup>	I <sub>D</sub> @TA=25°C	-2.5	A
Continuous Drain Current <sup>3</sup>	I <sub>D</sub> @TA=70°C	-1.97	A
Pulsed Drain Current <sup>1</sup>	I <sub>DM</sub>	10	A
Power Dissipation	P <sub>D</sub> @TA=25°C	1.38	W
Linear Derating Factor		0.01	W/°C
Operating Junction and Storage Temperature Range	T <sub>j</sub> , T <sub>stg</sub>	-55 ~ +150	°C

**Thermal Data**

Parameter	Symbol	Ratings	Unit
Thermal Resistance Junction-ambient <sup>3</sup> Max.	R <sub>thj-a</sub>	90	°C/W

**Electrical Characteristics(T<sub>j</sub> = 25°C Unless otherwise specified)**

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20	-	-	V	V <sub>GS</sub> =0, I <sub>D</sub> =-250uA
Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS} / \Delta T_j$	-	-0.01	-	V/°C	Reference to 25°C, I <sub>D</sub> =-1mA
Gate Threshold Voltage	V <sub>GS(th)</sub>	-	-	-1.2	V	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA
Forward Transconductance	g <sub>fs</sub>	-	4.0	-	S	V <sub>DS</sub> =-5V, I <sub>D</sub> =-2A
Gate-Source Leakage Current	I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> = ±12V
Drain-Source Leakage Current(T <sub>j</sub> =25°C)	I <sub>DSS</sub>	-	-	-1	uA	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0
Drain-Source Leakage Current(T <sub>j</sub> =70°C)		-	-	-25	uA	V <sub>DS</sub> =-16V, V <sub>GS</sub> =0
Static Drain-Source On-Resistance <sup>2</sup>	R <sub>DS(ON)</sub>	-	-	120	mΩ	V <sub>GS</sub> =-10V, I <sub>D</sub> =-2.8A
		-	-	160		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2.5A
		-	-	300		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-2A
Total Gate Charge <sup>2</sup>	Q <sub>g</sub>	-	5	8	nC	I <sub>D</sub> =-2A V <sub>DS</sub> =-16V V <sub>GS</sub> =-4.5V
Gate-Source Charge	Q <sub>gs</sub>	-	1	-		
Gate-Drain ("Miller") Change	Q <sub>gd</sub>	-	2	-		
Turn-on Delay Time <sup>2</sup>	T <sub>d(on)</sub>	-	6	-	ns	V <sub>DS</sub> =-10V I <sub>D</sub> =-1A V <sub>GS</sub> =-10V R <sub>G</sub> =3.3Ω R <sub>D</sub> =10Ω
Rise Time	T <sub>r</sub>	-	17	-		
Turn-off Delay Time	T <sub>d(off)</sub>	-	16	-		
Fall Time	T <sub>f</sub>	-	5	-		
Input Capacitance	C <sub>iss</sub>	-	270	430	pF	V <sub>GS</sub> =0V V <sub>DS</sub> =-20V f=1.0MHz
Output Capacitance	C <sub>oss</sub>	-	70	-		
Reverse Transfer Capacitance	C <sub>rss</sub>	-	55	-		

**Source-Drain Diode**

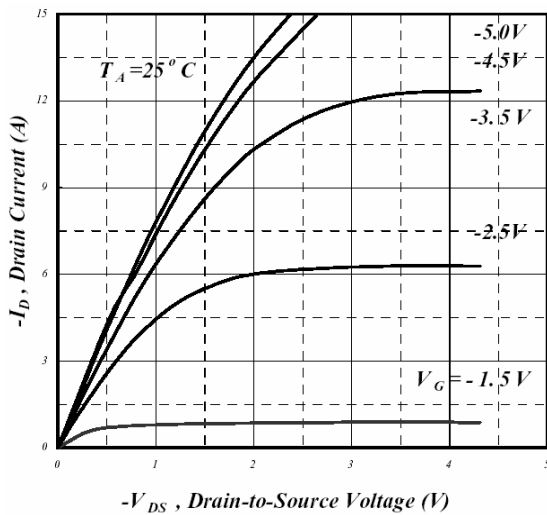
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Forward On Voltage <sup>2</sup>	V <sub>SD</sub>	-	-	-1.2	V	I <sub>S</sub> =-1.2A, V <sub>GS</sub> =0V
Reverse Recovery Time <sup>2</sup>	T <sub>rr</sub>	-	20	-	ns	I <sub>S</sub> =-2A, V <sub>GS</sub> =0V dI/dt=100A/μs
Reverse Recovery Charge	Q <sub>rr</sub>	-	15	-	nC	

Notes: 1. Pulse width limited by Max. junction temperature.

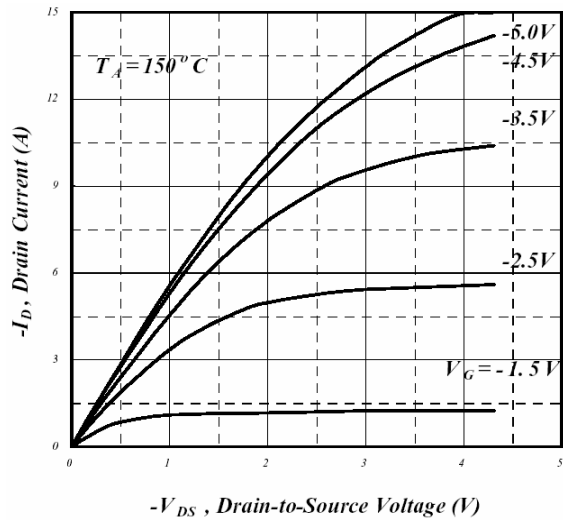
2. Pulse width ≤ 300us, duty cycle ≤ 2%.

3. Surface mounted on 1 in<sup>2</sup> copper pad of FR4 board, t ≤ 10sec; 270°C/W when mounted on Min. copper pad.

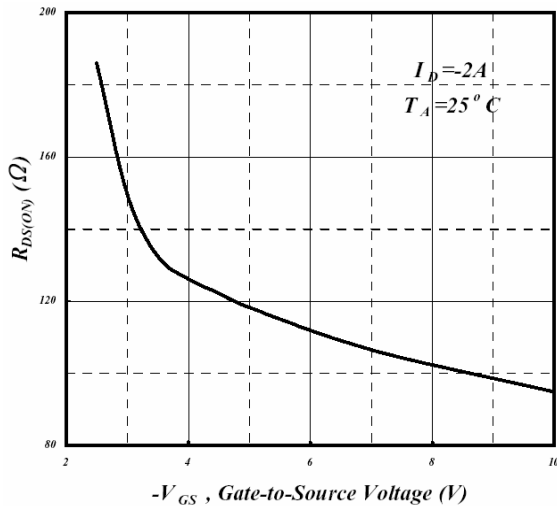
## Characteristics Curve



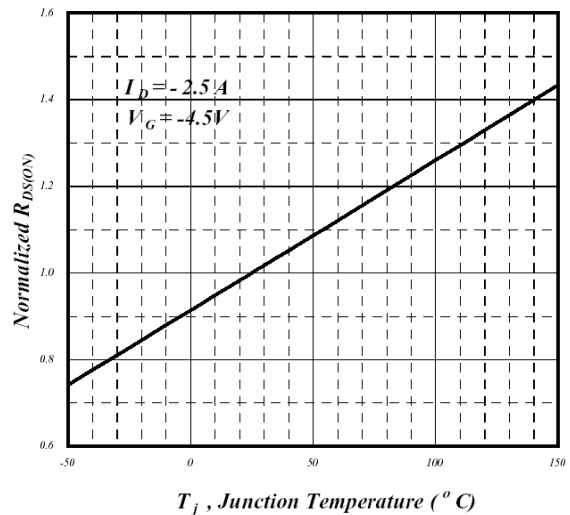
**Fig 1. Typical Output Characteristics**



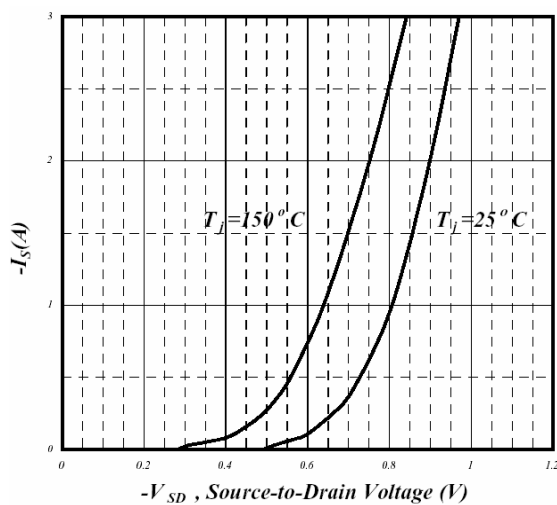
**Fig 2. Typical Output Characteristics**



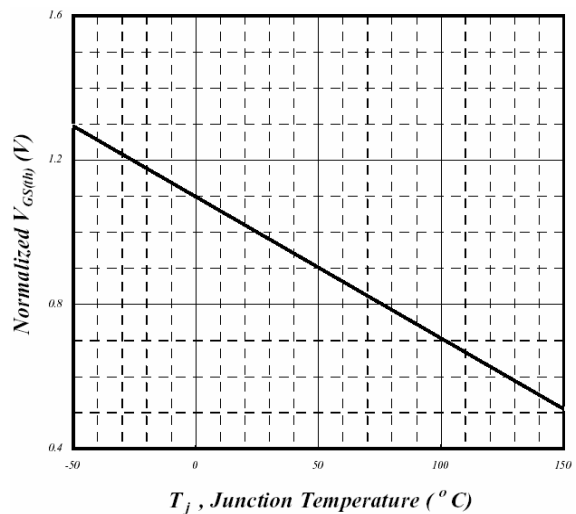
**Fig 3. On-Resistance v.s. Gate Voltage**



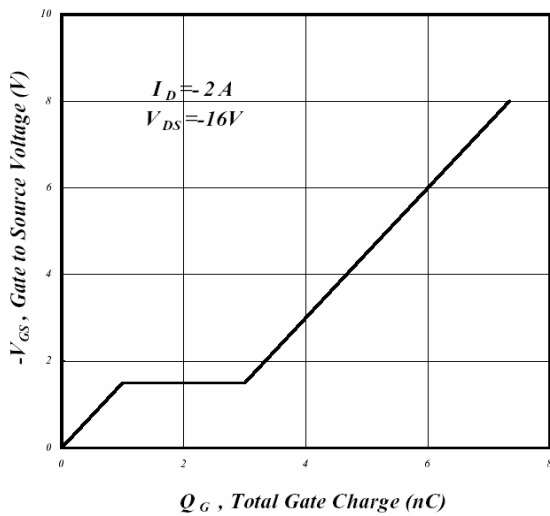
**Fig 4. Normalized On-Resistance v.s. Junction Temperature**



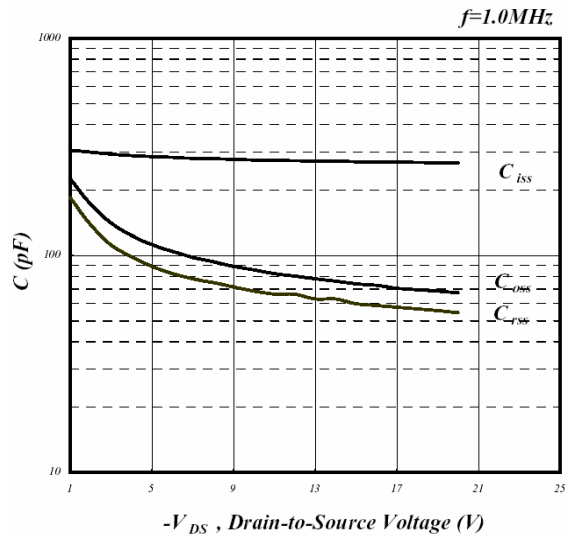
**Fig 5. Forward Characteristics of Reverse Diode**



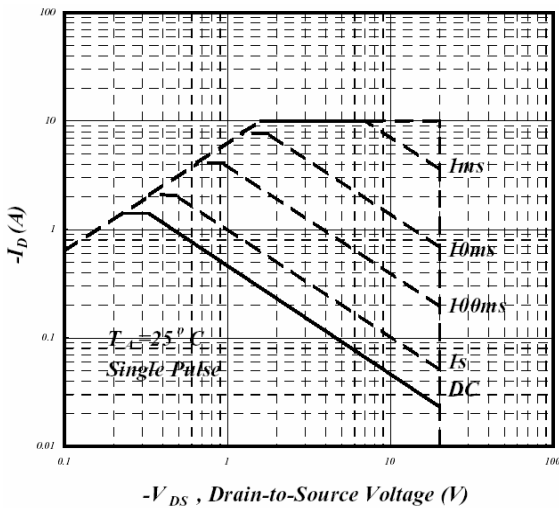
**Fig 6. Gate Threshold Voltage v.s. Junction Temperature**



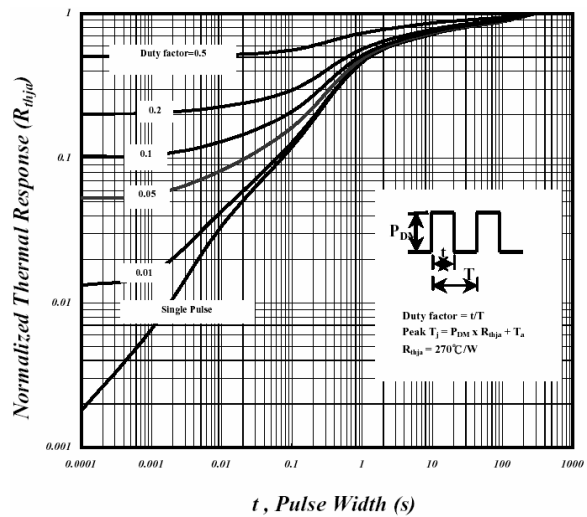
**Fig 7. Gate Charge Characteristics**



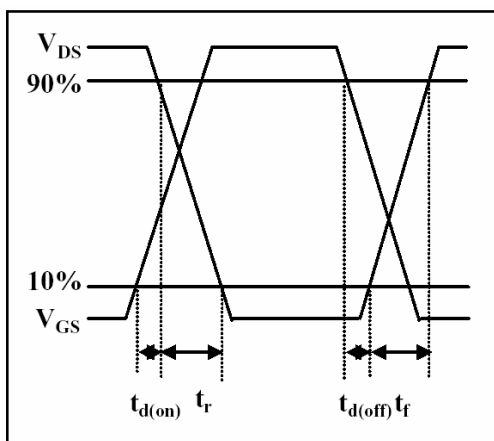
**Fig 8. Typical Capacitance Characteristics**



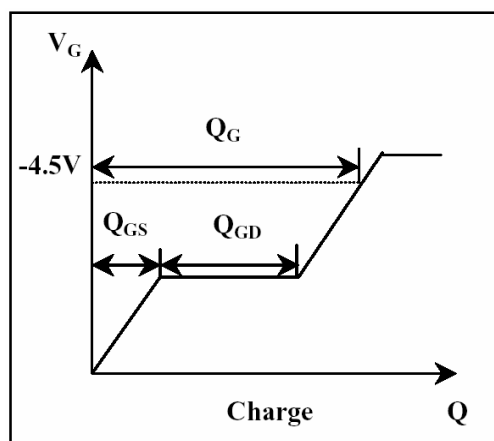
**Fig 9. Maximum Safe Operating Area**



**Fig 10. Effective Transient Thermal Impedance**



**Fig 11. Switching Time Waveform**



**Fig 12. Gate Charge Waveform**

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