

LS320 BiFET Amplifier



Linear Systems High Input Impedance BiFET Amplifier

The LS320 is a high input impedance amplifer produced using a BiFET process and packaged in TO-72

The hermetically sealed TO-72 package is well suited for hi-rel and harsh environment applications.

(See Packaging Information).

LS320 Features:

- High Input Impedance
- High Transconductance

FEATURES					
HIGH INPUT IMPEDANCE	r _{Gs} ≥ = 100GΩ				
HIGH TRANSCONDUCTANCE	Y _{FS} = 30,000μS				
ABSOLUTE MAXIMUM RATINGS 1@ 25°C (unless otherwise noted)					
Maximum Temperatures					
Storage Temperature	-65°C to +150°C				
Operating Junction Temperature	-55°C to +125°C				
Maximum Power Dissipation					
Continuous Power Dissipation @ +125°C	200mW				
Maximum Currents	2.3mW/°C				
Drain Current	I _D = 25mA				
Maximum Voltages					
Drain to Source	$V_{DSO} = 20V$				
Gate to Source	V _{GSS} = 20V				

ELECTRICAL CHARACTERISTICS @ 25°C (unless otherwise noted)

SYMBOL	CHARACTERISTICS	MIN.	TYP.	MAX.	UNITS	CONDITIONS
V_{DS}	Drain to Source Voltage	-20			V	$I_{DS} = 100 \mu A, V_{GS} = 0$
V_{GS}	Collector to Source Voltage	-12	-10	-7	V	$I_{DS} = 10 \text{mA}, V_{GS} = -10 \text{V}^{23}$
g_fs	Common Source Forward	30000			μS	
	Transconductance					I _{DS} = 10mA, V _{GS} = -10V, f =1kHz
g _{oss}	Common Source Output Conductance		300			
r_Gs	Gate to Source Input Resistance	100			GΩ	$V_{GS} = 0$ to 20V, T_J to 125°C
C _{ISS}	Input Capacitance		8		рА	$I_{DS} = 100 \mu A, V_{DS} = -10 V$
C _{RSS}	Reverse Transfer Capacitance		1. 5		pF	$I_{DS} = 10 \text{mA}, V_{GS} = -10 \text{V}$
e _n	Noise Voltage		2 5		μV	$I_{DS} = 10 \text{mA}$, $V_{DS} = 10 \text{V}$, BW = 50 to 15 kHz

Notes:

- 1. Absolute Maximum ratings are limiting values above which serviceability may be impaired
- 2. The gate to source voltage must never exceed 100V, t < 10ms
- 3. Additional screening available

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Available Packages:

LS320 in TO-72 LS320 in bare die.

Please contact Micross for full package and die dimensions

TO-72 (Bottom View)

