

PHOTO



PRODUCT

SMD-PLL QUARTZ STABILIZED OSCILLATOR / 3.0 VDC

DESCRIPTION

Series M1320 is a highly stable PLL signal source with internal crystal reference. This part employs TCXO inside to achieve ± 2.5 ppm frequency stability.

Other versions with built-in SMD-Clock-Oscillator for frequency stabilities in the range of $\pm 15 \sim \pm 100$ ppm are also available.

It is supposed to give design simplification benefits to the design engineers by reducing the design time, costs and board space.

FEATURES

- RF Frequency range between 600.0 MHz ~ 2.5 GHz
- SMT package
- Internal crystal reference clock (XO and TCXO)
- High reliability for low cost
- 3.0 VDC supply voltage
- 22 x 22 x 6 mm package dimension
- A lot of different functions are integrated in low volume

APPLICATIONS

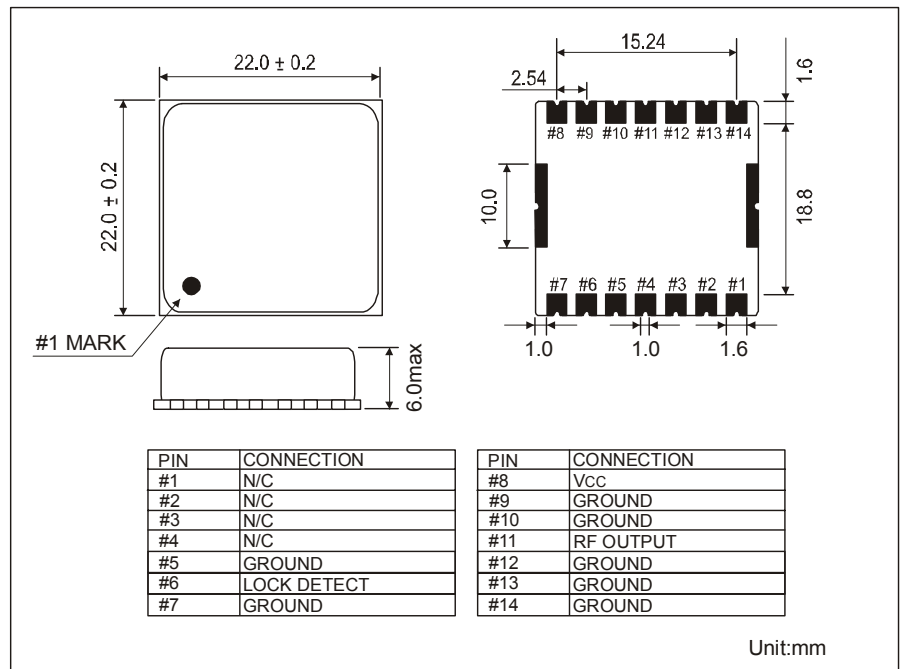
- Mobile Telephone and portable telephone
- Wireless LAN
- SONET / SDH / ATM
- Wireless CCTV
- etc.

SPECIFICATIONS

Frequency range:	600.0 MHz ~ 2.5 GHz
Frequency stability:	+2.5 ppm max.
Output power:	0 dBm +-3 dBm
Supply voltage:	+3.0 VDC +-5%
Input current:	100 mA max.
2'nd harmonics :	-15 dBc max.
Operating temperature range:	-30/+80°C
Storage temperature range:	-40/+125°C
Phase noise (typ.)	-90 dBc/Hz @ 10 kHz offset -110 dBc/Hz @ 100 kHz offset

OTHER PARAMETERS ARE AVAILABLE ON REQUEST

OUTLINE DRAWING



MARKING

Line 1: MFI
 Line 2: M1320-25K
 Line 3: Frequency
 Line 4: Date Code

BLOCK DIAGRAM

