

## High voltage fast-switching NPN power transistor

Preliminary data

### Features

- High voltage capability
- Minimum lot-to-lot spread for reliable operation
- Very high switching speed

### Applications

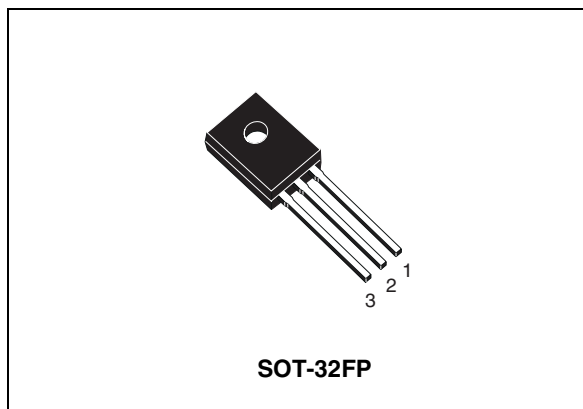
- Electronic ballast for fluorescent lighting
- Flyback and forward single transistor low power converters

### Description

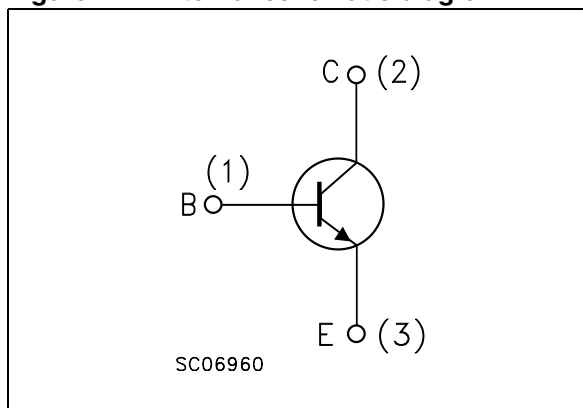
The device is manufactured using high voltage multi-epitaxial planar technology for high switching speeds and medium voltage capability.

It uses a cellular emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA.

The device is designed for use in lighting applications and low cost switch-mode power supplies.



**Figure 1. Internal schematic diagram**



**Table 1. Device summary**

Order codes	Marking	Packages	Packaging
STT13005FP	T13005FP	SOT-32FP	Bulk

**1 Electrical ratings****Table 2. Absolute maximum ratings**

Symbol	Parameter	Value	Unit
$V_{CES}$	Collector-emitter voltage ( $V_{BE} = 0$ )	700	V
$V_{CEO}$	Collector-emitter voltage ( $I_B = 0$ )	400	V
$V_{EBO}$	Emitter-base voltage ( $I_C = 0$ )	9	V
$I_C$	Collector current	2	A
$I_{CM}$	Collector peak current ( $t_P < 5$ ms)	4	A
$I_B$	Base current	1	A
$I_{BM}$	Base peak current ( $t_P < 5$ ms)	2	A
$P_{tot}$	Total dissipation at $T_C = 25$ °C	30	W
$T_{stg}$	Storage temperature	-65 to 150	°C
$T_J$	Max. operating junction temperature	150	°C

## 2 Electrical characteristics

( $T_{\text{case}} = 25\text{ °C}$  unless otherwise specified)

**Table 3. Electrical characteristics**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$I_{\text{CES}}$	Collector cut-off current ( $V_{\text{BE}} = 0$ )	$V_{\text{CE}} = 700\text{ V}$			100	$\mu\text{A}$
		$V_{\text{CE}} = 700\text{ V}$ $T_{\text{C}} = 125\text{ °C}$			500	$\mu\text{A}$
$I_{\text{CEO}}$	Collector cut-off current ( $I_{\text{B}} = 0$ )	$V_{\text{CE}} = 400\text{ V}$			250	$\mu\text{A}$
$V_{\text{EBO}}$	Emitter-base voltage ( $I_{\text{C}} = 0$ )	$I_{\text{E}} = 10\text{ mA}$	9			V
$V_{\text{CEO(sus)}}^{(1)}$	Collector-emitter sustaining voltage ( $I_{\text{B}} = 0$ )	$I_{\text{C}} = 10\text{ mA}$	400			V
$V_{\text{CE(sat)}}^{(1)}$	Collector-emitter saturation voltage	$I_{\text{C}} = 0.5\text{ A}$ $I_{\text{B}} = 125\text{ mA}$			0.5	V
		$I_{\text{C}} = 0.8\text{ A}$ $I_{\text{B}} = 0.2\text{ A}$			1	V
		$I_{\text{C}} = 1.6\text{ A}$ $I_{\text{B}} = 0.4\text{ A}$			1.5	V
$V_{\text{BE(sat)}}^{(1)}$	Base-emitter saturation voltage	$I_{\text{C}} = 0.5\text{ A}$ $I_{\text{B}} = 125\text{ mA}$			1	V
		$I_{\text{C}} = 0.8\text{ A}$ $I_{\text{B}} = 0.2\text{ A}$			1.3	V
		$I_{\text{C}} = 1.6\text{ A}$ $I_{\text{B}} = 0.4\text{ A}$			1.5	V
$h_{\text{FE}}^{(1)}$	DC current gain	$I_{\text{C}} = 0.5\text{ A}$ $V_{\text{CE}} = 5\text{ V}$	10		50	
		$I_{\text{C}} = 2\text{ A}$ $V_{\text{CE}} = 5\text{ V}$	8			
$t_{\text{r}}$ $t_{\text{s}}$ $t_{\text{f}}$	Resistive load Rise time Storage time Fall time	$I_{\text{C}} = 1\text{ A}$ $V_{\text{CC}} = 125\text{ V}$ $I_{\text{B1}} = -I_{\text{B2}} = 0.2\text{ A}$		0.4	0.7	$\mu\text{s}$
				3.2	4.5	$\mu\text{s}$
				0.25	0.4	$\mu\text{s}$
$t_{\text{s}}$ $t_{\text{f}}$	Inductive load Storage time Fall time	$I_{\text{C}} = 1\text{ A}$ $I_{\text{B1}} = 0.2\text{ A}$ $V_{\text{BE(off)}} = -5\text{ V}$ $L = 50\text{ mH}$ $V_{\text{Clamp}} = 300\text{ V}$		0.8		$\mu\text{s}$
				0.16		$\mu\text{s}$

1. Pulsed duration = 300  $\mu\text{s}$ , duty cycle  $\leq 1.5\%$

## 2.1 Electrical characteristics (curves)

Figure 2. Safe operating area

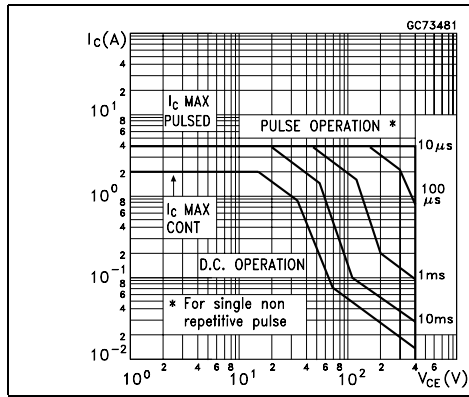


Figure 3. Derating curve

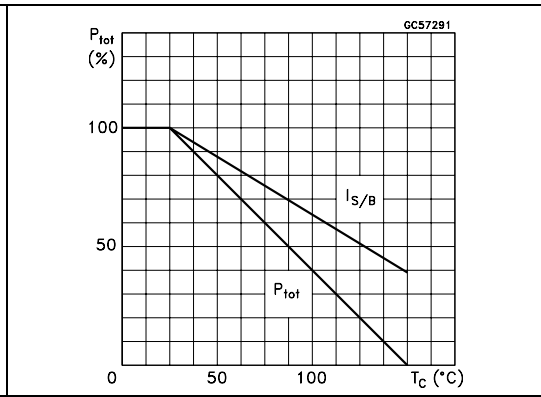


Figure 4. DC current gain

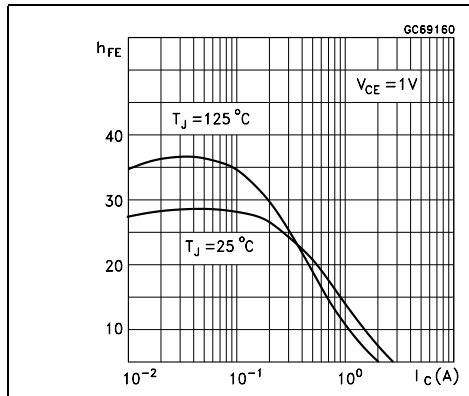


Figure 5. DC current gain

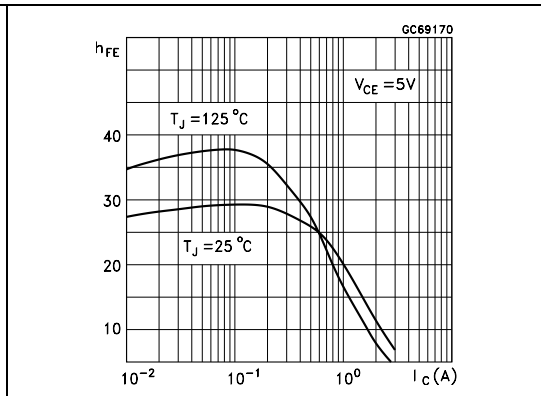


Figure 6. Collector-emitter saturation voltage

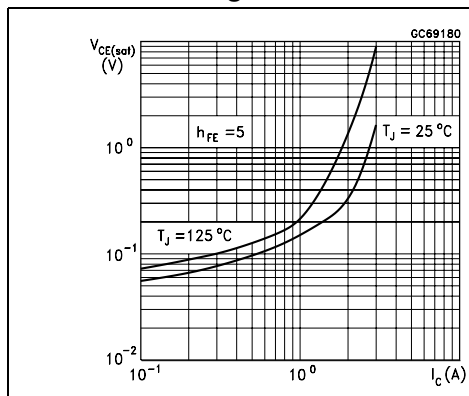


Figure 7. Base-emitter saturation voltage

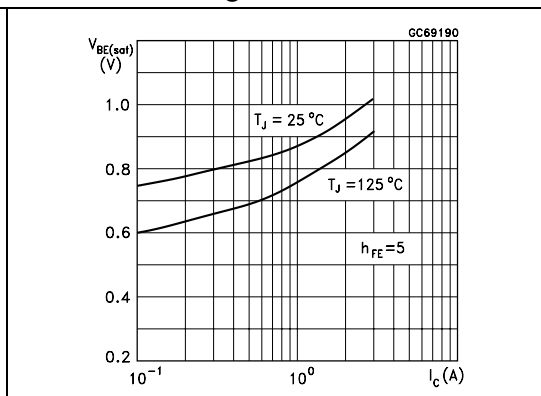


Figure 8. Inductive load fall time

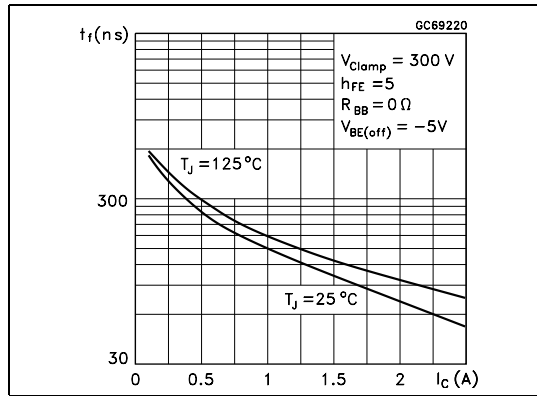


Figure 9. Inductive load storage time

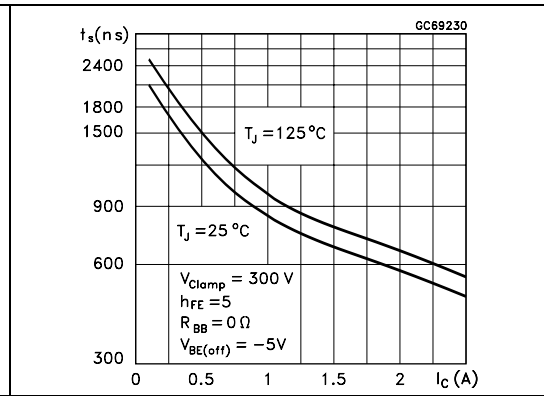


Figure 10. Resistive load fall time

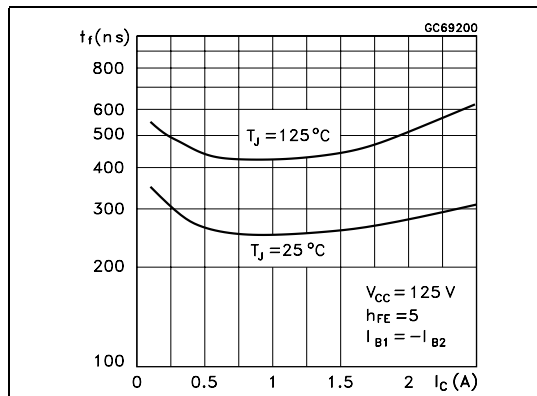


Figure 11. Resistive load storage time

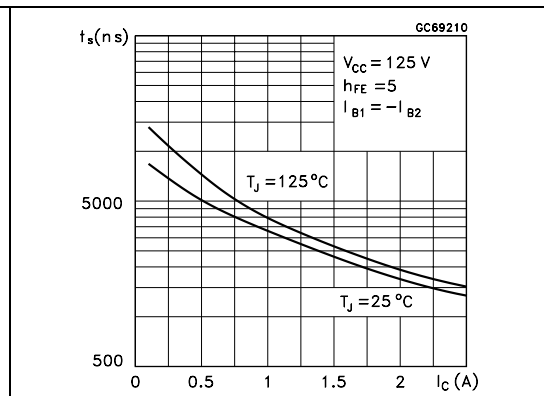
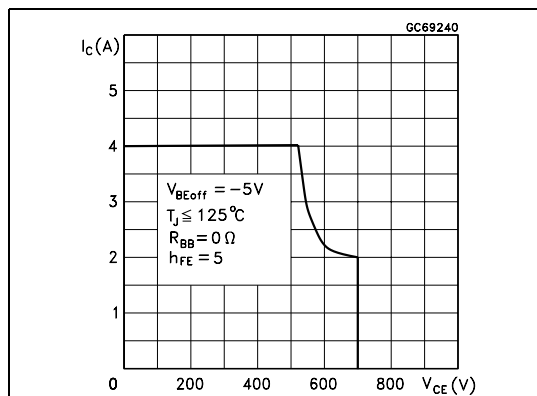
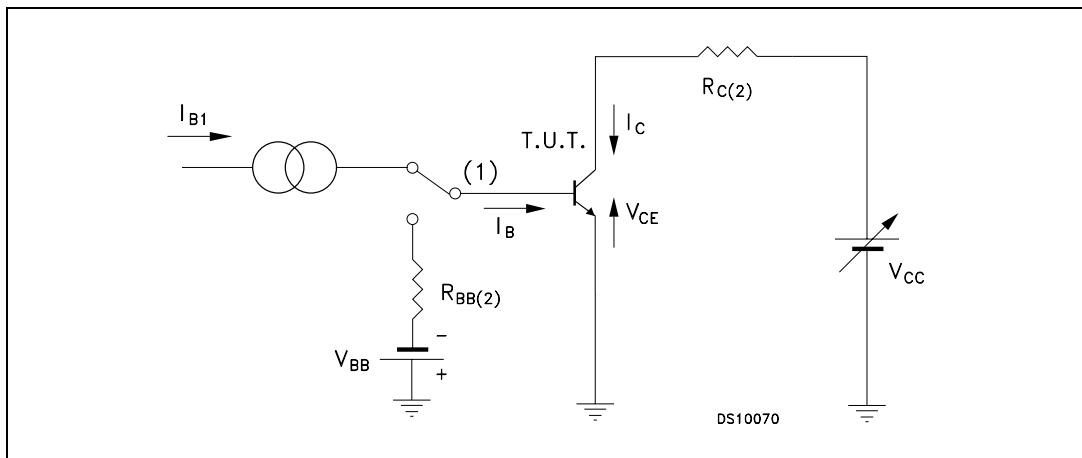


Figure 12. Reverse biased SOA



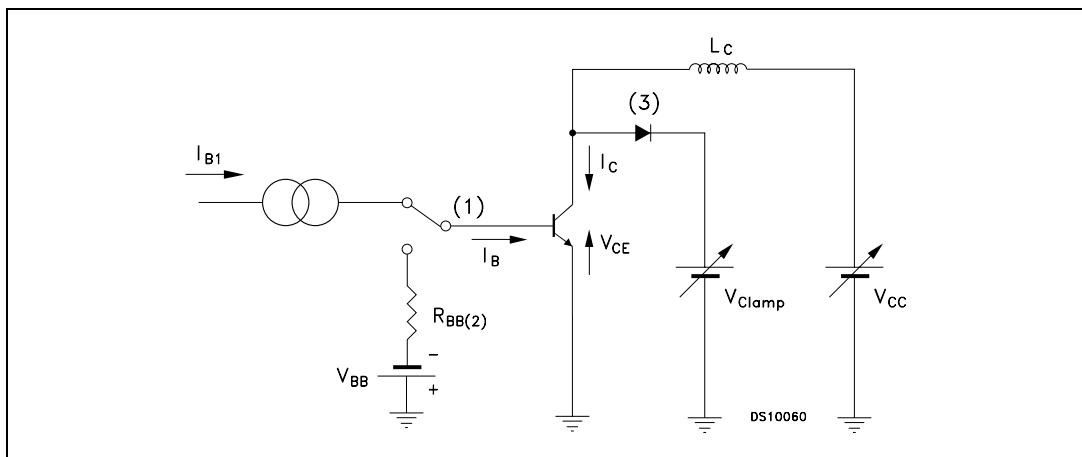
## 2.2 Test circuits

Figure 13. Resistive load switching test circuit



1. Fast electronic switch
2. Non-inductive resistor

Figure 14. Inductive load switching test circuit



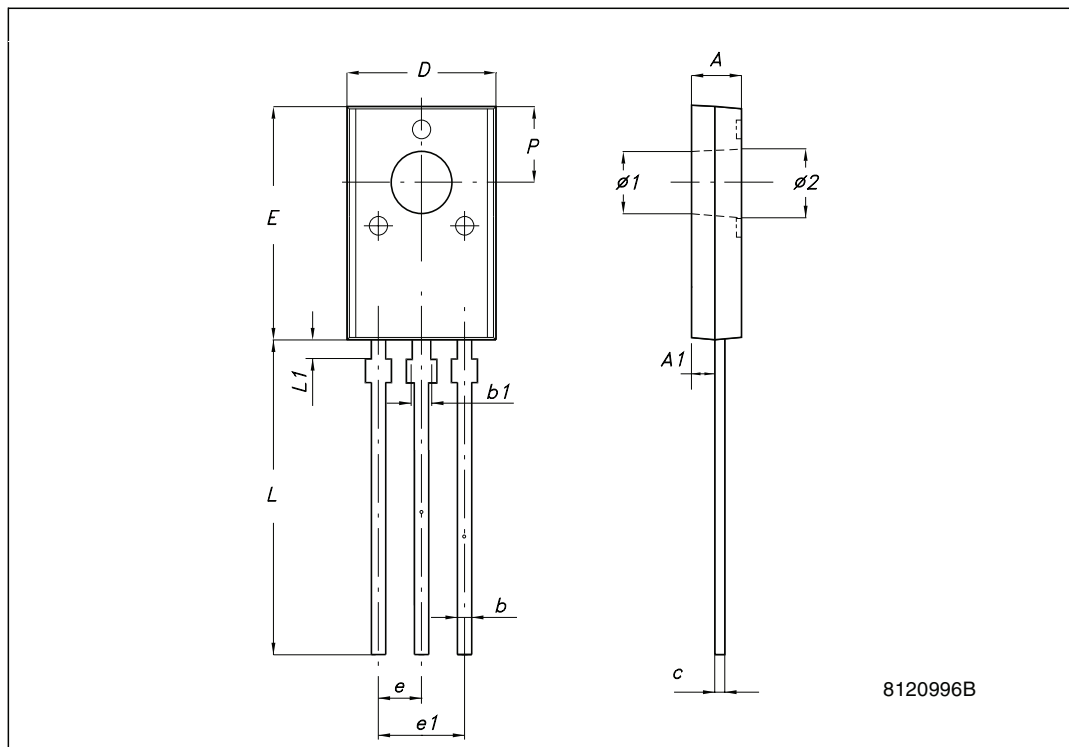
1. Fast electronic switch
2. Non-inductive resistor
3. Fast recovery rectifier

### 3 Package mechanical data

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**SOT-32FP mechanical data**

DIM.	mm.		
	MIN.	TYP	MAX.
A	3.00		3.40
A1	1.80		2.20
b	0.66		0.86
b1	1.17		1.37
c	0.45		0.60
D	7.80		8.20
E	10.80		11.20
e		2.28	
e1	4.46		4.66
L	15.30		15.70
L1	1.30		1.50
P	4.04		4.24
ø1	2.90		3.10
ø2	3.10		3.30





## 4 Revision history

**Table 4. Document revision history**

Date	Revision	Changes
06-May-2009	1	Initial release

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