

## Fixed Frequency Crystal Oscillator

- TTL and CMOS compatible output
- Tight symmetry 45 to 55% available
- Typical 5.0 x 3.2 x 1.2mm ceramic SMD package
- Complies with Directive 2002/95/EC (RoHS Compliant)

# PO321

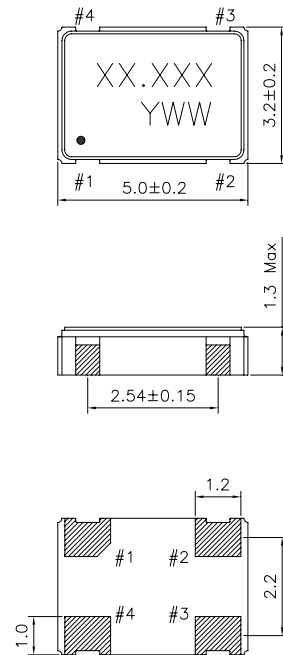
### Specifications

<b>Frequency Range:</b>	0.3456MHz ~ 125.00MHz		
<b>Operating Temperature:</b>	-10°C ~ +60°C	- B	
	-20°C ~ +70°C	- C	
	-40°C ~ +85°C	- L	
<b>Storage Temperature:</b>	-55°C ~ +125°C		
<b>Frequency Stability:</b>	±50 ppm	- H	
	±25 ppm	- E	
	±20 ppm	- C	
<b>Supply Voltage:</b>	5.0V <sub>DD</sub>	- C	
	3.3V <sub>DD</sub>	- E	
	2.5V <sub>DD</sub>	- H	
<b>Supply Current:</b>	5.0	3.3	2.5 (V <sub>DD</sub> )
	0.3456MHz ~ 1.5MHz	7	5 (mA)
	1.5MHz ~ 20MHz	10	7 (mA)
	20MHz ~ 50MHz	30	15 (mA)
	50MHz ~ 75MHz	40	20 (mA)
> 75MHz	N/A	30	25 (mA)
<b>Output Level:</b>	Output High (Logic "1")	90% V <sub>DD</sub>	
	Output Low (Logic "0")	10% V <sub>DD</sub>	
<b>Output Waveform:</b>	TTL	- B	
	TTL/50pF	- R	
	CMOS/15pF	- K	
	CMOS/50pF	- G	
<b>Transition Times:</b>	5.0	3.3	2.5 (V <sub>DC</sub> )
	0.3456MHz ~ 20MHz	8	10 (nSec)
	20MHz ~ 50MHz	5	6 (nSec)
	50MHz ~ 75MHz	2	3 (nSec)
> 75MHz	N/A	3	3 (nSec)
<b>Symmetry or Duty Cycle (%):</b>	45 ~ 50		
<b>Start-Up Time:</b>	8 mSec max.		
<b>Tri-State Function:</b>	With Tri-State	- T	
(Input to Pin #1)			
<b>Absolute Clock Period Jitter:</b>	40 pSec		

### Note:

1. Other frequencies, stabilities, and operating temperature ranges available. Consult VTC Support for specific requirements.
2. Not all combinations of the above, stabilities, and temperature ranges are available. Consult VTC Support if your requirement is not standard.
3. All specifications subject to change without notice.

### Outline Drawing



Marking: Line 1: Frequency in MHz  
Line 2: Date Code

Pin	Connection
#1	NC or E/D
#2	Ground
#3	Output
#4	Supply V <sub>DD</sub>

All dimensions are in mm

### Ordering Information

Product Code + Operating Temperature Code + Stability Code + Supply Voltage Code + Output Waveform Code + Tri-State Code or None + Frequency.

i.e. PO321CHEKT-40.0MHz