

# Dual P-channel MOSFET

## ELM34803AA-N

### ■General description

ELM34803AA-N uses advanced trench technology to provide excellent  $R_{ds(on)}$ , low gate charge and low gate resistance.

### ■Features

- $V_{ds} = -30V$
- $I_d = -8A$
- $R_{ds(on)} < 22m\Omega$  ( $V_{gs} = -10V$ )
- $R_{ds(on)} < 34m\Omega$  ( $V_{gs} = -4.5V$ )

### ■Maximum absolute ratings

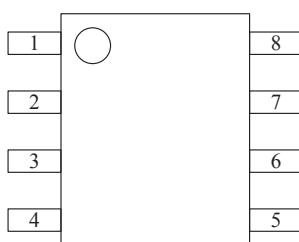
Parameter	Symbol	Limit	Unit	Note
Drain-source voltage	$V_{ds}$	-30	V	
Gate-source voltage	$V_{gs}$	$\pm 25$	V	
Continuous drain current Ta=25°C	$I_d$	-8	A	3
Ta=70°C	$I_d$	-6		
Pulsed drain current	$I_{dm}$	-40	A	
Avalanche current	$I_{as}$	-30	A	
Avalanche energy	$E_{as}$	45	mJ	
Power dissipation Ta=25°C	$P_d$	2.00	W	3
Ta=70°C	$P_d$	1.28		
Junction and storage temperature range	$T_j, T_{stg}$	-55 to 150	°C	

### ■Thermal characteristics

Parameter	Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	$R_{\theta ja}$		62.5	°C/W	

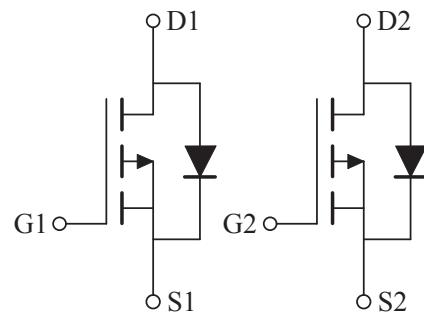
### ■Pin configuration

SOP-8(TOP VIEW)



Pin No.	Pin name
1	SOURCE1
2	GATE1
3	SOURCE2
4	GATE2
5	DRAIN2
6	DRAIN2
7	DRAIN1
8	DRAIN1

### ■Circuit



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### ■Electrical characteristics

T<sub>a</sub>=25°C

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
<b>STATIC PARAMETERS</b>							
Drain-source breakdown voltage	BV <sub>dss</sub>	Id=-250μA, V <sub>gs</sub> =0V	-30			V	
Zero gate voltage drain current	Id <sub>ss</sub>	V <sub>ds</sub> =-24V, V <sub>gs</sub> =0V			-1	μA	
		V <sub>ds</sub> =-20V, V <sub>gs</sub> =0V, T <sub>j</sub> =125°C			-10		
Gate-body leakage current	I <sub>gss</sub>	V <sub>ds</sub> =0V, V <sub>gs</sub> =±25V			±100	nA	
Gate threshold voltage	V <sub>gs(th)</sub>	V <sub>ds</sub> =V <sub>gs</sub> , Id=-250μA	-1.0	-1.5	-3.0	V	
Static drain-source on-resistance	R <sub>ds(on)</sub>	V <sub>gs</sub> =-10V, Id=-9A		20	22	mΩ	1
		V <sub>gs</sub> =-4.5V, Id=-7A		29	34		
Forward transconductance	G <sub>fs</sub>	V <sub>ds</sub> =-5V, Id=-9A		20		S	1
Diode forward voltage	V <sub>sd</sub>	I <sub>f</sub> =-9A, V <sub>gs</sub> =0V			-1	V	1
Max. body-diode continuous current	I <sub>s</sub>				-2	A	
<b>DYNAMIC PARAMETERS</b>							
Input capacitance	C <sub>iss</sub>	V <sub>gs</sub> =0V, V <sub>ds</sub> =-15V, f=1MHz		1480		pF	
Output capacitance	C <sub>oss</sub>			334		pF	
Reverse transfer capacitance	C <sub>rss</sub>			231		pF	
Gate resistance	R <sub>g</sub>	V <sub>gs</sub> =0V, V <sub>ds</sub> =0V, f=1MHz		2.9		Ω	
<b>SWITCHING PARAMETERS</b>							
Total gate charge (10V)	Q <sub>g</sub>	V <sub>gs</sub> =-10V, V <sub>ds</sub> =-15V Id=-9A		30		nC	2
Total gate charge (4.5V)	Q <sub>g</sub>			15		nC	2
Gate-source charge	Q <sub>gs</sub>			5		nC	2
Gate-drain charge	Q <sub>gd</sub>			6		nC	2
Turn-on delay time	t <sub>d(on)</sub>	V <sub>gs</sub> =-10V, V <sub>ds</sub> =-15V Id≈-9A, R <sub>gen</sub> =6Ω		13		ns	2
Turn-on rise time	t <sub>r</sub>			8		ns	2
Turn-off delay time	t <sub>d(off)</sub>			16		ns	2
Turn-off fall time	t <sub>f</sub>			12		ns	2
Body diode reverse recovery time	t <sub>rr</sub>	I <sub>f</sub> =-9A, dI/dt=100A/μs		40		ns	
Body diode reverse recovery charge	Q <sub>rr</sub>	I <sub>f</sub> =-9A, dI/dt=100A/μs		26		nC	

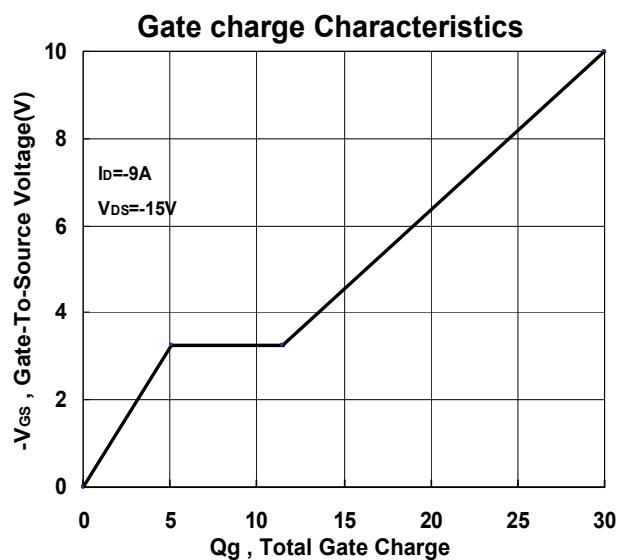
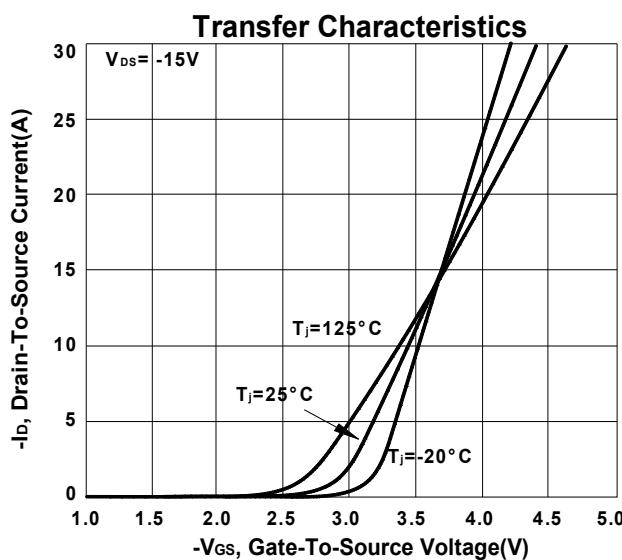
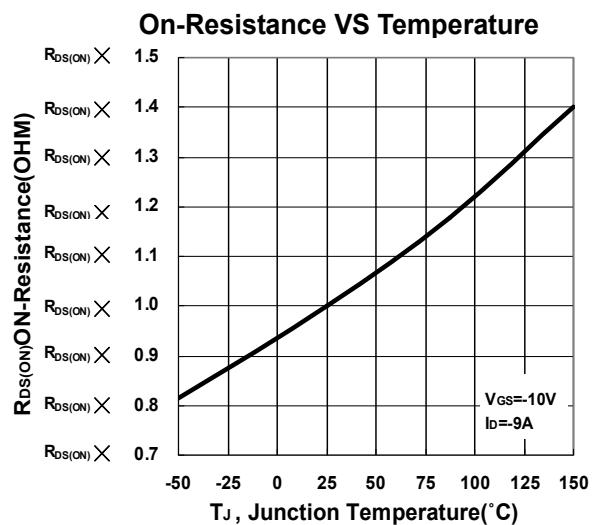
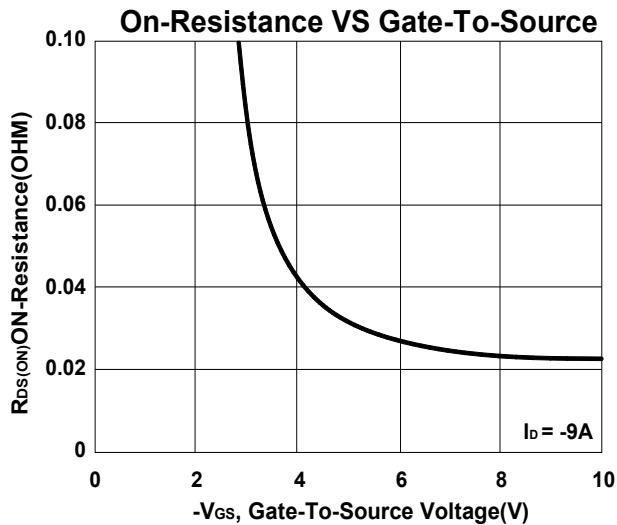
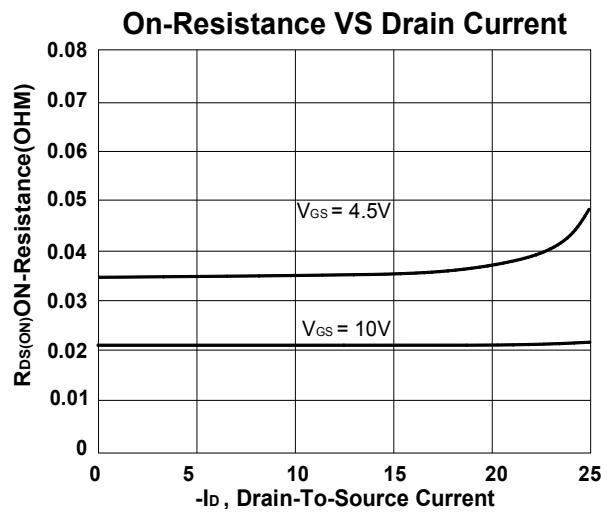
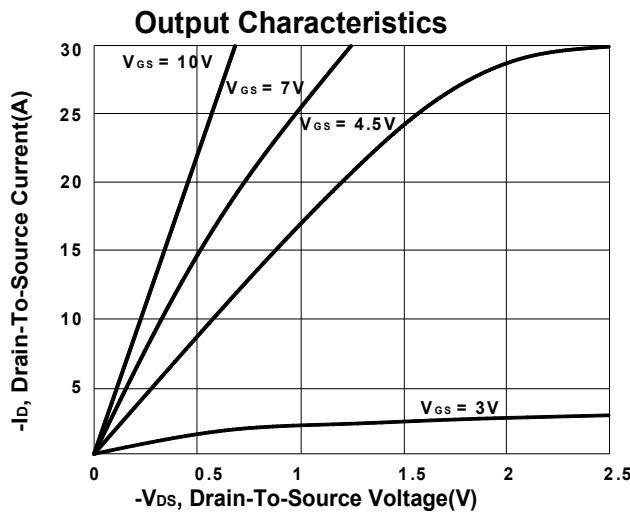
### NOTE :

1. Pulsed width≤300μsec and Duty cycle≤2%.
2. Independent of operating temperature.
3. Pulsed width limited by maximum junction temperature.
4. Duty cycle ≤ 1%.

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### ■ Typical electrical and thermal characteristics



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