

STEVAL-IKR001V5

Sub-GHz transceiver development kit based on the SPIRIT1 (915 MHz band)

Data brief

Features

- 2 SPIRIT1 low power, sub-GHz RF transceiver daughterboards tuned for the 915 MHz band (FCC title 47 part 15) and 920 MHz band (ARIB STD-T108)
- 2 STM32L microcontroller-based motherboards
- Associated SPIRIT1 software development kit with documentation, STM32L firmware and GUI
- Debug connector
- USB interface
- Modulation schemes: 2-FSK, GFSK, MSK, GMSK, OOK, and ASK
- Air data rate from 1 to 500 kbps
- Very low power consumption (9 mA RX and 21 mA TX at +11 dBm)
- Excellent performance of receiver sensitivity (up to -118 dBm)
- Low duty cycle RX/TX operation mode
- Automatic acknowledgement, retransmission, and timeout protocol engine
- AES 128-bit encryption co-processor
- SPI interface for microcontroller
- RoHS complian

Description

The STEVAL-IKR001V5 development kit includes 2 RF daughterboards based on the SPIRIT1 low power, sub-GHz low data rate transceiver suitable for ISM bands and wireless M-BUS, and 2 motherboards based on the STM32L low power microcontroller to control the daughterboards.

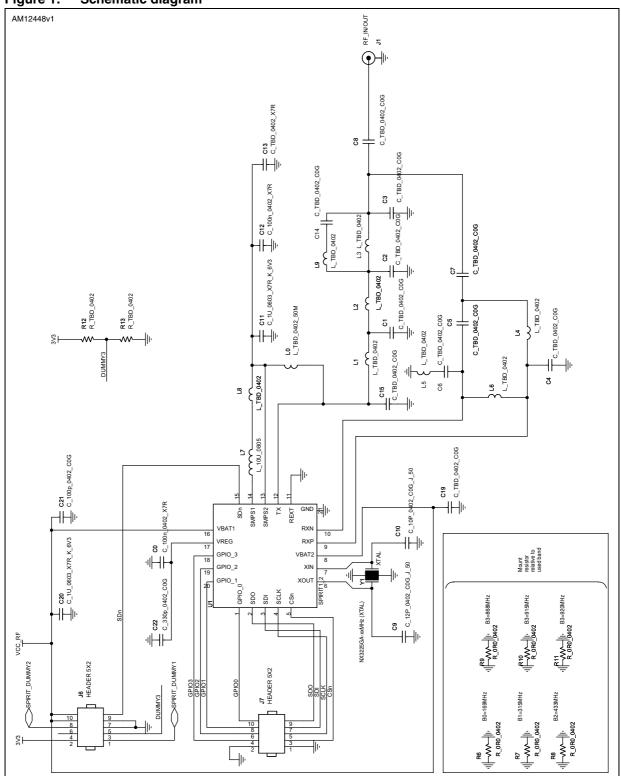
Each motherboard features a USB connector for PC GUI interaction and firmware updates. A JTAG connector allows the development of specific firmware on the microcontroller.



Schematic diagram STEVAL-IKR001V5

1 Schematic diagram

Figure 1. Schematic diagram



STEVAL-IKR001V5 Revision history

2 Revision history

Table 1. Document revision history

Date	Revision	Changes
04-Jul-2012	1	Initial release.
02-Aug-2012	2	Updated the Features and Description on the cover page
25-Oct-2012	3	Updated the Features on the cover page

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