



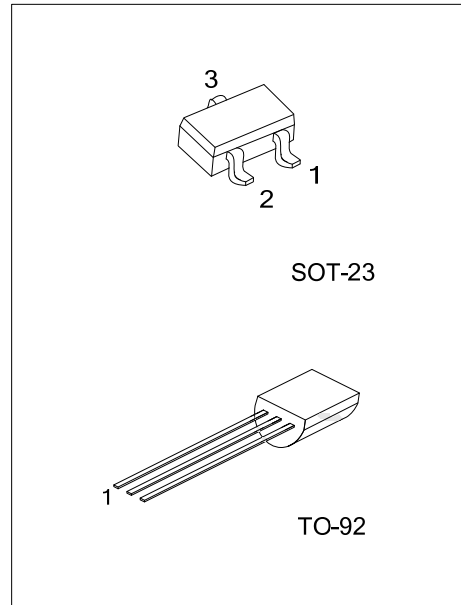
XL/ML1225

SCR

MEDIUM POWER LOW VOLTAGE TRANSISTOR

DESCRIPTION

The **XL1225/ML1225** silicon controlled rectifiers are high performance planar diffused PNP devices. These parts are intended for low cost high volume applications.



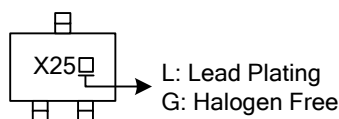
ORDERING INFORMATION

| Ordering Number | | | Package | Pin Assignment | | | Packing |
|-----------------|-----------------|-----------------|---------|----------------|---|---|-----------|
| Normal | Lead Free | Halogen Free | | 1 | 2 | 3 | |
| XL1225-x-AE3-R | XL1225L-x-AE3-R | XL1225G-x-AE3-R | SOT-23 | G | K | A | Tape Reel |
| XL1225-x-T92-B | XL1225L-x-T92-B | XL1225G-x-T92-B | TO-92 | K | G | A | Tape Box |
| XL1225-x-T92-K | XL1225L-x-T92-K | XL1225G-x-T92-K | TO-92 | K | G | A | Bulk |
| XL1225-x-T92-R | XL1225L-x-T92-R | XL1225G-x-T92-R | TO-92 | K | G | A | Tape Reel |
| ML1225-x-AE3-R | ML1225L-x-AE3-R | ML1225G-x-AE3-R | SOT-23 | G | K | A | Tape Reel |
| ML1225-x-T92-B | ML1225L-x-T92-B | ML1225G-x-T92-B | TO-92 | K | G | A | Tape Box |
| ML1225-x-T92-K | ML1225L-x-T92-K | ML1225G-x-T92-K | TO-92 | K | G | A | Bulk |
| ML1225-x-T92-R | ML1225L-x-T92-R | ML1225G-x-T92-R | TO-92 | K | G | A | Tape Reel |

Note: G: Gate, K: Cathode, A: Anode

| | |
|---|--|
| <p>XL1225L-xx-T92-K</p> <p>(1) Packing Type</p> <p>(2) Package Type</p> <p>(3) Rank</p> <p>(4) Lead Free</p> <p>(5) XL / ML</p> | <p>(1) B: Tape Box, K: Bulk, R: Tape Reel</p> <p>(2) T92: TO-92, AE3: SOT-23</p> <p>(3) Refer to CLASSIFICATION OF I_{GT}</p> <p>(4) G: Halogen Free, L: Lead Free, Blank: Pb/Sn</p> <p>(5) XL : V_{DRM}· 400V , ML: V_{DRM}· 300V</p> |
|---|--|

MARKING



(SOT-23)

■ ABSOLUTE MAXIMUM RATINGS (Ta= 25°C, unless otherwise specified)

| PARAMETER | SYMBOL | RATINGS | UNIT |
|---|---------------------|------------|------|
| Repetitive Peak Off-State Voltage (T _J =40 ~ 125°C, R _{GK} =1kΩ) | XL1225 | 400 | V |
| | ML1225 | 300 | |
| On-State Current (T _c =40°C) | I _{T(RMS)} | 0.8 | A |
| Average On-State Current (Half Cycle=180,T _c =40°C) | I _{T(AV)} | 0.5 | A |
| Peak Reverse Gate Voltage (I _{GR} =10μA) | V _{GRM} | 1 | V |
| Peak Gate Current (10μs Max.) | I _{GM} | 0.1 | A |
| Gate Dissipation (20ms Max.) | P _{G(AV)} | 150 | mW |
| Junction Temperature | T _J | +125 | °C |
| Storage Temperature | T _{STG} | -40 ~ +150 | °C |

Note 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
 2. The device is guaranteed to meet performance specification within 0°C~70°C operating temperature range and assured by design from -20°C ~85°C.

■ ELECTRICAL CHARACTERISTICS (Ta= 25°C, unless otherwise specified.)

| PARAMETER | SYMBOL | TEST CONDITIONS | MIN | TYP | MAX | UNIT |
|-------------------------------|--------------------|---|-----|-----|------|------|
| Off State Leakage Current | I _{DRM} | V _{DRM} (R _{GK} =1KΩ), T _J =125°C | | | 0.1 | mA |
| Off State Leakage Current | I _{DRM} | V _{DRM} (R _{GK} =1KΩ), T _J =25°C | | | 1.0 | μA |
| On State Voltage | V _T | AT I _T =0.4A | | | 1.4 | V |
| | | AT I _T =0.8A | | | 2.2 | |
| On State Threshold Voltage | V _{T(TO)} | T _J =125°C | | | 0.95 | V |
| On State Slops Resistance | R _t | T _J =125°C | | | 600 | m |
| Gate Trigger Current | I _{GT} | V _D =7V | | | 200 | μA |
| Gate Trigger Voltage | V _{GT} | V _D =7V | | | 0.8 | V |
| Holding Current | I _H | R _{GK} =1KΩ | | | 5 | mA |
| Latching Current | I _L | R _{GK} =1KΩ | | | 6 | mA |
| Critical Rate of Voltage Rise | DV/DT | V _D =0.67*V _{DRM} (R _{GK} =1KΩ),T _J =125°C | | | | V/μs |
| Critical Rate of Current Rise | DV/DT | I _G =10mA, dl _G /dt=0.1A/μs,T _J =125°C | | | | A/μs |
| Gate Controlled Delay Time | T _{GD} | I _G =10mA, dl _G /dt=0.1A/μs | | | 2.2 | μs |
| Commutated Turn-off Time | TG | T _J =85°C, V _D =0.67*V _{DRM} , V _R =35V, I _T =I _{T(AV)} | | | 200 | μs |

■ CLASSIFICATION OF I_{GT}

| RANK | B | C | AA | AB | AC | AD |
|-------|--------|---------|------|-------|-------|-------|
| RANGE | 50-100 | 100-200 | 8-15 | 15-20 | 20-25 | 25-50 |

■ TYPICAL CHARACTERISTICS

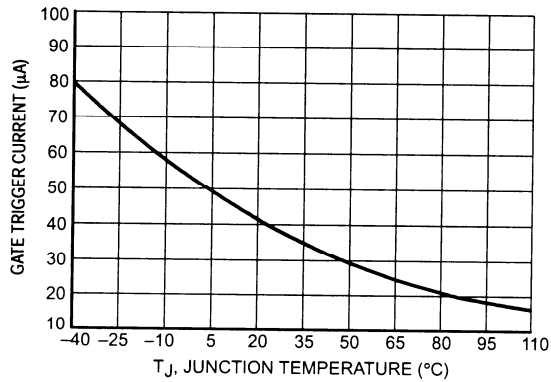


Figure 1. Typical Gate Trigger Current versus Junction Temperature

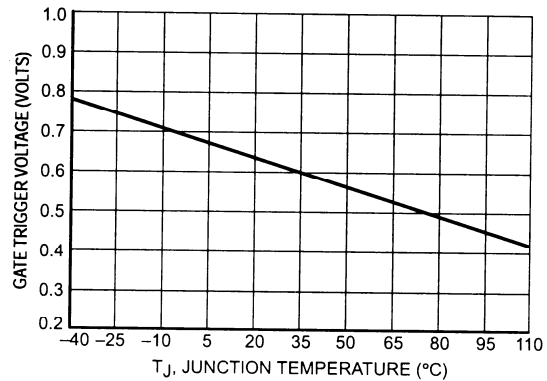


Figure 2. Typical Gate Trigger Voltage versus Junction Temperature

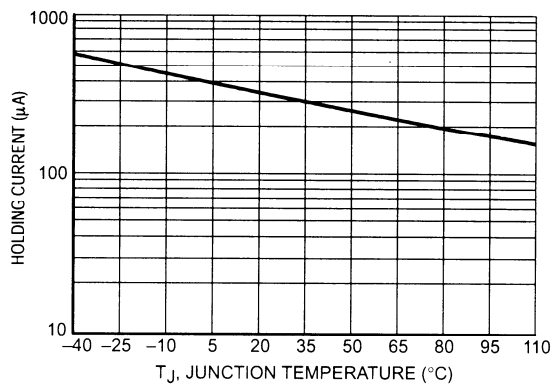


Figure 3. Typical Holding Current versus Junction Temperature

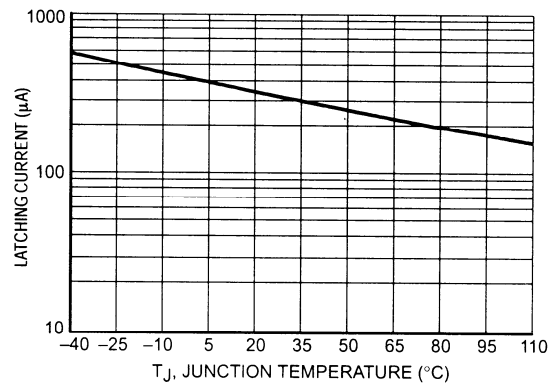


Figure 4. Typical Latching Current versus Junction Temperature

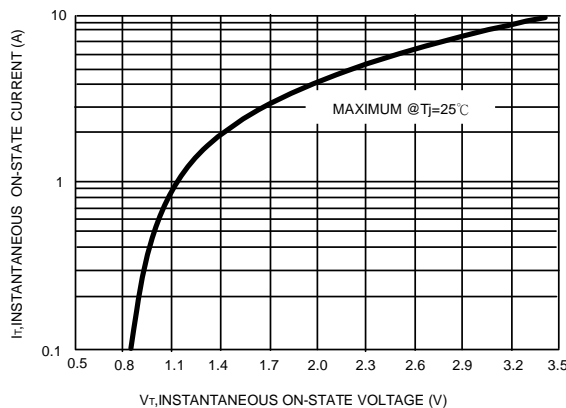


Figure 5. Typical On-State Characteristics

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