## XP0NG8A

## Silicon PNP epitaxial planar type (Tr) Silicon epitaxial planar type (SBD)

#### For digital circuits

#### ■ Features

- Two elements incorporated into one package (Tr + SBD)
- Costs can be reduced through downsizing of the equipment and reduction of the number of parts
- SMini type package allowing easy automatic insertion through tape packing and magazine packing

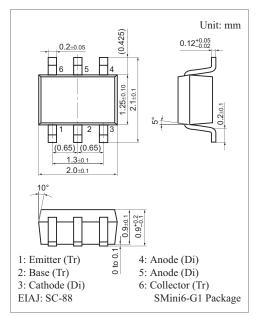
#### ■ Basic Part Number

■ UNR211L + MA152WK

### ■ Absolute Maximum Ratings $T_a = 25$ °C

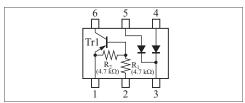
Parameter		Symbol	Rating	Unit	
Tr	Collector-base voltage (Emitter open)	V <sub>CBO</sub>	-50	V	
	Collector-emitter voltage (Base open)	V <sub>CEO</sub>	-50	V	
	Collector current	$I_{C}$	-100	mA	
SBD	Forward current	$I_F$	100	mA	
	Peak forward current	$I_{FM}$	225	mA	
	Non-repetitive peak forward surge current *	$I_{FSM}$	500	mA	
	Reverse voltage	$V_R$	80	V	
	Maximum peak reverse voltage	V <sub>RM</sub>	80	V	
Overall	Total power dissipation	P <sub>T</sub>	150	mW	
	Junction temperature	T <sub>j</sub>	150	°C	
	Storage temperature	T <sub>stg</sub>	-55 to +150	°C	

Note) \*: t = 1 s



#### Marking Symbol: 4D

#### Internal Connection



XP0NG8A Panasonic

### ■ Electrical Characteristics $T_a = 25$ °C±3°C

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Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	$V_{CBO}$	$I_{\rm C} = -10 \mu\text{A},  I_{\rm E} = 0$	-50			V
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	$I_{\rm CI} = -2 \text{ mA}, I_{\rm B} = 0$	-50			V
Collector-base cutoff current (Emitter open)	$I_{CBO}$	$V_{CB} = -50 \text{ V}, I_{E} = 0$			-0.1	μΑ
Collector-emitter cutoff current (Base open)	$I_{CEO}$	$V_{CH} = -50 \text{ V}, I_B = 0$			-0.5	μΑ
Emitter-base cutoff current (Collector open)	I <sub>EBO</sub>	$V_{EB} = -6 \text{ V}, I_{C} = 0$			-2.0	mA
Forward current transfer ratio	h <sub>FE</sub>	$V_{CH} = -10 \text{ V}, I_{C} = -5 \text{ mA}$	20			_
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	$I_{\rm CI} = -10 \text{ mA}, I_{\rm B} = -0.3 \text{ mA}$			-0.25	V
Output voltage high-level	V <sub>OH</sub>	$V_{CCI} = -5 \text{ V}, V_B = -0.5 \text{ V}, R_{LI} = 1 \text{ k}\Omega$	-4.9			V
Output voltage low-level	V <sub>OL</sub>	$V_{CCI} = -5 \text{ V}, V_B = -2.5 \text{ V}, R_{IJ} = 1 \text{ k}\Omega$			-0.2	V
Input resistance	$R_1$		-30%	4.7	+30%	kΩ
Resistance ratio	$R_1/R_2$		0.8	1.0	1.2	_
Transition frequency	$f_T$	$V_{CB} = -10 \text{ V}, I_{E} = 1 \text{ mA}, f = 200 \text{ MHz}$		80		MHz

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

#### • SBD

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Forward voltage	V <sub>F</sub>	$I_F = 100 \text{ mA}$			1.2	V
Reverse voltage	V <sub>R</sub>	$I_{R} = 100 \ \mu A$	80			V
Reverse current	$I_R$	$V_{R^l} = 75 V$			0.1	μΑ
Terminal capacitance	C <sub>t</sub>	$V_{RJ} = 0 V, f = 1 MHz$			2.0	pF
Reverse recovery time	t <sub>rr</sub>	$I_F = 10 \text{ mA}, V_{RI} = 6 \text{ V}, R_{LI} = 100 \Omega,$ $I_{rr} = 0.1 I_R$			3.0	ns

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 measuring methods for diodes.

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