

# **CONVERTER CARD PI-002**

# PARALLEL TO ANALOGUE YPBPR/RGB 10 BIT LOW COST

**INPUT** Parallel multiplexed component signals to CCIR Rec 656

SMPTE RP-125 coded.

Note. To ease interfacing into equipment data is latched by the rising edge of the clock.

Data rate 27 MHz

Data Set up time 3 nS min to positive edge of clock
Data hold time 2.5 nS min from positive edge of clock

Line standard 625/50 or 525/60 Auto detection

#### **Analogue Output Characteristics**

 $\begin{array}{ll} \text{Output format} & \text{YPbPr / RGB} \\ \text{Output levels} & 700 \text{ mV } \pm 1\% \\ \text{Output Impedance} & 75 \text{ Ohms } \pm 1\% \\ \end{array}$ 

Output return loss > 40 dB to 5.5 MHz

Resolution 10 bit

Freq. Response Luminance < 0.2 dB to 5.5 MHz

Chrominance < 0.2 dB to 2.2 MHz

K-ratings kp, kpb, kbar <1%

Noise >70 dB rms to 5.5 MHz

**Power**  $+5V \pm 0.25V (300 \text{mA max})$ 

#### **Mechanical Details**

The following formats are available:

Format A STD configuration with low profile pins and BNC connectors for outputs.

Format B LOW profile pins and MCX connectors for outputs. Format C STAND-OFF configuration with BNC outputs.

## **Connector Pinout**

1	2	3	4	5	6	7	8	9	10	11	12
+5V	CLK	D0	D1	D2	D3	D4	D5	D6	D7	D8	D9
13	14	15	16	17	18	19	20	21	22	23	24
+5V	+5V	NC	NC	NC	NC	RESET	NC	NC	GND	GND	GND
25	26	27	28	29	30	31	32	33	34	35	36
NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	S/ON	RGB
37	38	39	40	41	42	43	44	45	46	47	48
NC	NC	NC	NC	NC	NC	NC	NC	NC	ID0	ID1	ID2

D0 to D9 Parallel Data Bus D0 LSB, D9 MSB

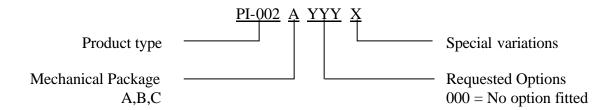
CLK Parallel Data clock

RGB +5v=RGB output 0v=YPbPr output SON +5v=Sync on RGB/Y 0v= No sync ID0,ID1,ID2 Card type identification code.

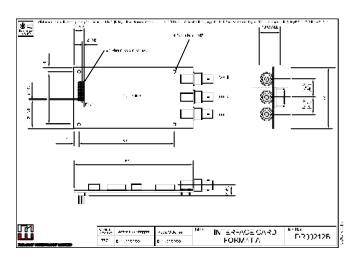
### Card Type Identification code.

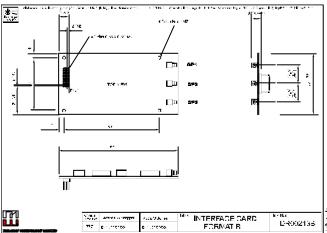
CARD TYPE	ID2	ID1	ID0
PI-002	0v	+5v	0V

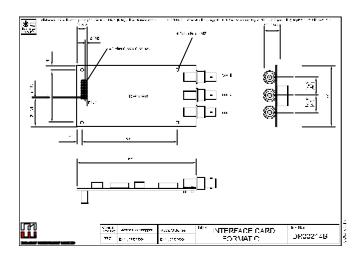
# **Ordering codes**



Note: This item is  ${\bf NOT}$  aqueous washable.







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Aug-05(C)