



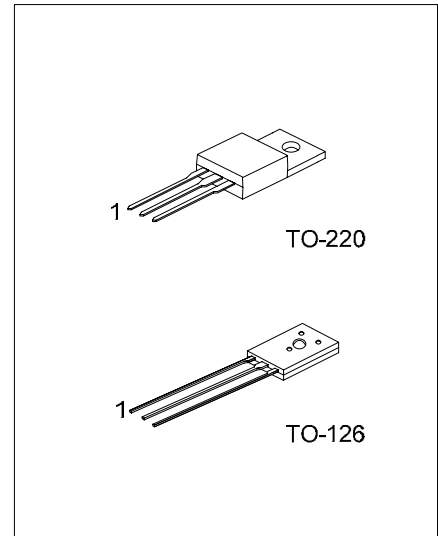
TIP122

NPN SILICON TRANSISTOR

NPN EPITAXIAL TRANSISTOR

DESCRIPTION

The UTC **TIP122** is a NPN epitaxial transistor, designed for use in general purpose amplifier low-speed switching applications.



*Pb-free plating product number: TIP122L

ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Normal	Lead Free Plating		1	2	3	
TIP122-T60-K	TIP122L-T60-K	TO-126	E	C	B	Bulk
TIP122-TA3-T	TIP122L-TA3-T	TO-220	B	C	E	Tube

<p>TIP122L-T60-K</p> <p>(1) Packing Type</p> <p>(2) Package Type</p> <p>(3) Lead Plating</p>	<p>(1) K: Bulk, T: Tube</p> <p>(2) T60: TO-126, TA3: TO-220</p> <p>(3) L: Lead Free Plating, Blank: Pb/Sn</p>
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■ ABSOLUTE MAXIMUM RATINGS (Ta=25)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector to Base Voltage	V_{CBO}	100	V
Collector to Emitter Voltage	V_{CEO}	100	V
Emitter to Base Voltage	V_{EBO}	5	V
IC Collector Current	I_C	5	A
Power Dissipation (T _C =25)	TO-220	65	W
	TO-126	40	W
Junction Temperature	T_J	+150	
Storage Temperature	T_{STG}	-55 ~ +150	

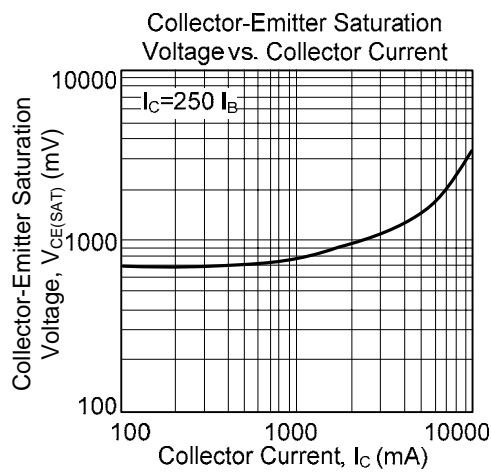
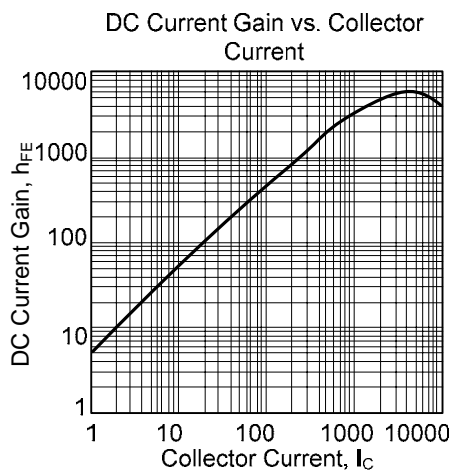
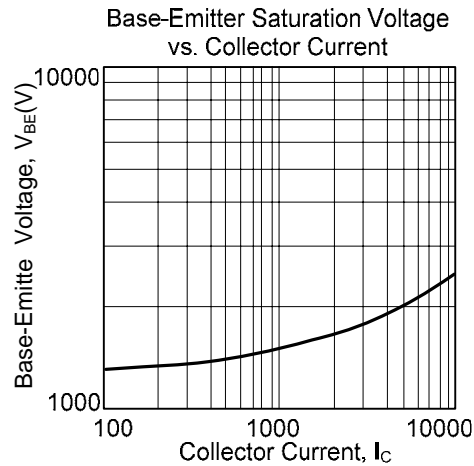
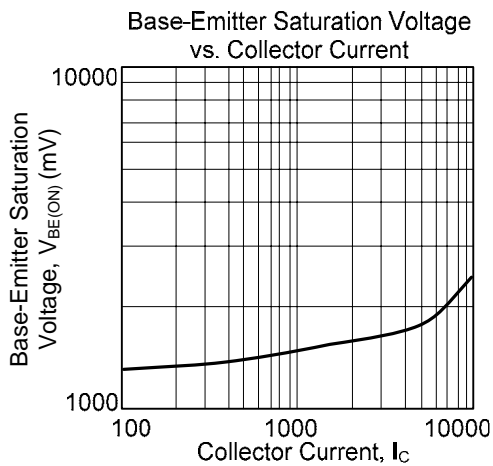
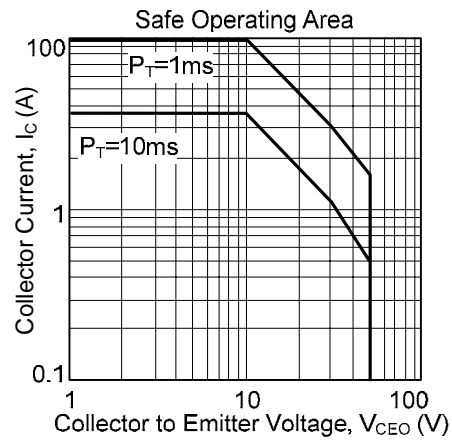
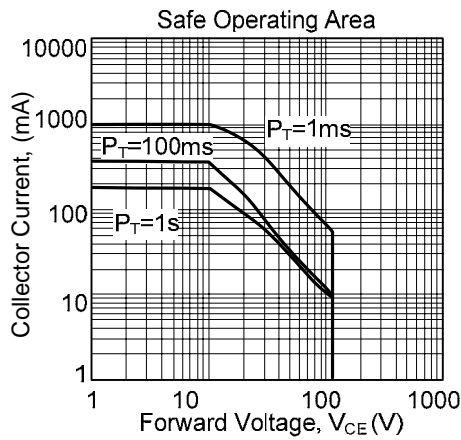
Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

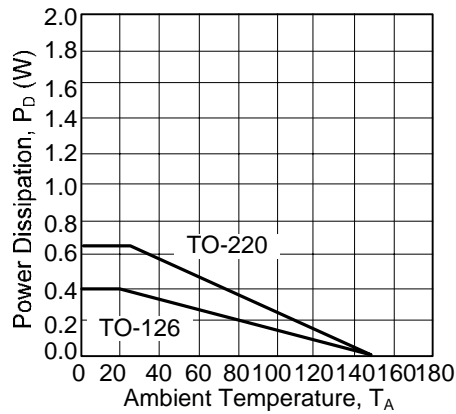
■ ELECTRICAL CHARACTERISTICS (Ta=25)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Emitter Breakdown Voltage	BV_{CEO}	$I_C=100mA$	100			V
Collector-Emitter Saturation Voltage	$V_{CE(SAT)1}$	$I_C=3A, I_B=12mA$			2	V
Collector-Emitter Saturation Voltage	$V_{CE(SAT)2}$	$I_C=5A, I_B=20mA$			4	V
Base-Emitter Saturation Voltage	$V_{BE(ON)}$	$V_{CE}=3V, I_C=3A$			2.5	V
Collector Cut-Off Current	I_{CBO}	$V_{CB}=100V$			200	uA
Collector-Cut-Off Current	I_{CEO}	$V_{CE}=50V$			500	uA
Emitter Cut-Off Current	I_{EBO}	$V_{EB}=5V$			2	mA
DC Current Gain	h_{FE}	$I_C=500mA, V_{CE}=3V$	1000			
		$I_C=3A, V_{CE}=3V$	1000			

■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS



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