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STANDARI MILITAI DRAWII	?Y	D		CHE	PARE COL COL COL COL COL COL COL COL COL COL	D BY	<u>3.</u> M					DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444 MICROCIRCUIT, LINEAR, SWITCHING REGULATOR SUBSYSTEM, MONOLITHIC SILICON				OR								
THIS DRAWING IS FOR USE BY ALL DI AND AGENCIES DEPARTMENT OF	AVAILA EPARTI S OF TH	MENT IE	าร	7	DRAWING APPROVAL DATE 13 JANUARY 1989 REVISION LEVEL				+	SIZE A		(672	CODE	:	Т		62	-88	370	51 —			

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DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

1. SCOPE		and noments for s	lass B microcircuits	in accordance
ith 1.2.1 of Mil-311	drawing describes device r D-883, "Provisions for the			l
1 0 Dawt number	The complete part number	shall be as show	n in the following ex	ample:
	01	Ε		X
5962-887	$\frac{61}{1}$		-	
į		į		!
Drawing	number Device typ (1.2.1)	Case o (1.2	uci 1110	finish per M-38510
1.2.1 Device type	. The device type shall	identify the circ	cuit function as follo	: swc
Device type	Coneric number	<u>Circ</u>	uit function	
	- 785 4 0	Switching	regulator subsystem	
01 1.2.2 <u>Case outli</u>	ne. The case outline shal	ll be as designat	ed in appendix C of M	IL-M-38510, and as
follows:			Case outline	Ì
Outli	ne letter	(16 load 840"	x .310" x .200"), dua	l-in-line package
	E D-2	(10-1eau, .040		
1.3 Absolute max	cimum ratings. age (VCC)			
Input voltage Differentia Short circu Current fro Voltage fro Voltage fro Voltage fro Reverse pow Current the Power dissisted the Storage teles of the Curage teles of the Storage teles of the Curage teles of the Storage	age (VCC)	itter	Indefinite 10 mA 40 V 40 V 40 V 40 V 40 V 1.5 A 1.5 A 400 mW See MIL-M-38510, 6 175°C -65°C to 150°C	appendix C
1.4 Recommende	d operating conditions.	(T)	55°C to +125°C	
Ambient op	erating temperature range	(IA)	-	
	tial input voltage shall n	ot exceed the sup		
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DEECNSE FLEC	TY DRAWING TRONICS SUPPLY CENTER TON, OHIO 45444		REVISION LEVEL	SHEET 2
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2. APPLICABLE DOCUMENTS

2.1 Government specification and standard. Unless otherwise specified, the following specification and standard, of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

(Copies of the specification and standard required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

- 3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.
- 3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.
 - 3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.
 - 3.2.2 Block diagram. The block diagram shall be as specified on figure 2.
 - 3.2.3 Case outline. The case outline shall be in accordance with 1.2.2 herein.
- 3.3 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table I and apply over the full ambient operating temperature range.
- 3.4 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in 6.4 herein.
- 3.5 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in 6.4. The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall state that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.
- 3.6 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.
- 3.7 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).

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DESC FORM 193A SEP 87 Electrical performance characteristics. TABLE I. Limits Conditions 1/ -55°C \leq $T_{\text{A}} \leq$ +125°C Unit Group A Test Symbol | subgroups Min Max GENERAL CHARACTERISTICS 3.5 mΑ $v_{CC} = 5 V$ 1, 2, 3 LCC Supply current (op amp disconnected) $V_{CC} = 40 \text{ V}$ 5.0 4.0 $v_{CC} = 5 v$ 1, 2, 3 ICC Supply current (op amp connected) 5.5 $V_{CC} = 40 V$ REFERENCE SECTION 1.31 V 1, 2, 3 1.18 $I_{REF} = 1 \text{ mA}$ Reference voltage VREF 0.2 | mV/V $\begin{array}{l} \text{3.0 V} \leq \text{V}_{\text{CC}} \leq \text{40 V,} \\ \text{I}_{\text{REF}} = \text{1 mA,} \text{T}_{\text{A}} = \text{+25}^{\circ}\text{C} \end{array}$ 1 V_{RLINE} Line regulation $1.0 \text{ mA} \leq I_{REF} \leq 10 \text{ mA},$ $T_A = 25 \text{ C}$ 1 0.5 |mV/mA Load regulation v_{RLOAD} OSCILLATOR SECTION TA = +25°C | VCC = 5 V 20 50 μΑ 1 CHG Charging current 70 $V_{CC} = 40 \text{ V}$ 20 250 150 μA $T_A = +25^{\circ}C | V_{CC} = 5 V$ IDIS Discharge current 350 $V_{CC} = 40 V$ 150 CURRENT LIMIT SECTION 350 m٧ $I_{CT} = 200 \mu A$, $T_{A} = +25^{\circ}C$ 250 **VCLS** Sense voltage See footnotes at end of table. SIZE **STANDARDIZED** Α 5962-88761 **MILITARY DRAWING REVISION LEVEL** SHEET DEFENSE ELECTRONICS SUPPLY CENTER 4 DAYTON, OHIO 45444

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T	 Symbol		Condition	ns 1/	Group A	Limits		Unit
Test	 	i - I I	Condition 55°C <u><</u> T _A <u><</u>	+125°C	subgroups 	Min	Max	
OUTPUT SWITCH SECTION								
Output saturation voltage 1	V _{SAT1}	I _{SW} =	1 A	4/	1, 2, 3] 	1.3	٧
Output saturation voltage 2	V _{SAT2}	! ! !		5/			0.7	
Output leakage current	IL	V ₀ = 4	40 V, T _A = +	25°C	1	 	10	μА
POWER DIODE		··				T		,
Forward voltage drop	ιν _{FD}	I _D = 3	1 A		1, 2, 3	i 1	1.5	V
Diode leakage current	ILD	\ \V_D =	40 V, T _A = +	25°C	1	 	10	 μ A
COMPARATOR					_	T	7	r
Input offset voltage	V _{IO}	V _{CM} = V _{REF}			1, 2, 3		15	mV
Input bias current	IIIB	V _{CM} = V _{REF}			1, 2, 3	 	200	l nA
Input offset current 2/	1110	V _{CM} = V _{REF}			1, 2, 3		75	! nA
Input voltage range	VIR	T _A =	+25°C		1	0	V _{CC} -2	 V
Power supply rejection 2/ ratio	PSRR	3.0 V	/ < OP AMP SU +25°C	JPPLY < 40 V,	4	1	316	μ ۷ /۱
See footnotes at end of tab	ole.							
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		cal performance characteristics -	TI	Limit	llmid	
Test	Symbol	Conditions 1/ -55°C <u><</u> T _A <u><</u> +125°C	Group A subgroups 	Min	Max	Uni
OUTPUT OPERATIONAL AMPLIFIER	R					
Input offset voltage	ν _{IO}	V _{CM} = 2.5 V	1, 2, 3		15	mV
Input bias current	IIB	V _{CM} = 2.5 V	1, 2, 3	! 	200	nA
Input offset current	110	V _{CM} = 2.5 V	1, 2, 3		75	nA
Large signal voltage gain+	Avs+	1.0 V $<$ V ₀ $<$ 2.5 V, T _A = +25°C R _L = 2.0 k Ω to GND	4	25		V/n
Large signal voltage gain-	Ays-	1.0 V < V ₀ < 2.5 V, T _A = +25°C R _L = 2.0 kΩ to OP AMP SUPPLY	4	25		ν/τ
Common mode rejection ratio	CMRR	1 0 V < V _{CM} < 3.0 V, T _A = +25°C	4	76	 	dB
Input voltage range 3/	VIR	T _A = +25°C	1	0	3.0 	٧
Power supply rejection ratio	PSRR	3.0 V < 0P AMP SUPPLY < 40 V,	4		158	μV.
Output source current	I SOU	T _A = +25°C	1	 	 -75 	mA
Output sink current	ISIN	T _A = +25°C	1	 10] 	 mA
Output voltage LOW	V _{OL}	I _{OL} = 5 mA, T _A = +25°C	1]]	1.0	 V
Output voltage HIGH	Т V _{OH}	I I _{OH} = -50 mA, T _A = +25°C	1	2.0		V

^{1/} V_{CC} = 5.0 V, OP AMP SUPPLY = 5.0 V, unless otherwise specified.

 $\frac{5}{115} = 100 \text{ mA}.$

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 $[\]underline{2}/$ Guaranteed, if not tested, to the limits specified.

 $[\]underline{3}/~\text{V}_{\text{IR}}$ is guaranteed by the CMRR test.

 $[\]underline{4}$ / Pins 15 and 16 tied together, $I_{16} = 1$ A.

 Device type 	01
 Case outline 	E
 Terminal number 	Terminal symbol
1	DIODE CATHODE
2	DIODE ANODE
3	SWITCH EMITTER
4	OP AMP OUT
5	OP AMP SUPPLY
6	OP AMP +IN
7	OP AMP - IN
8	V _{REF}
9	COMPTR +IN
10	COMPTR -IN
11	GND
12	C _T
13	Vcc
14	IPK SEN
15	DRIVER COLL
16	SWITCH COLL

FIGURE 1. Terminal connections.

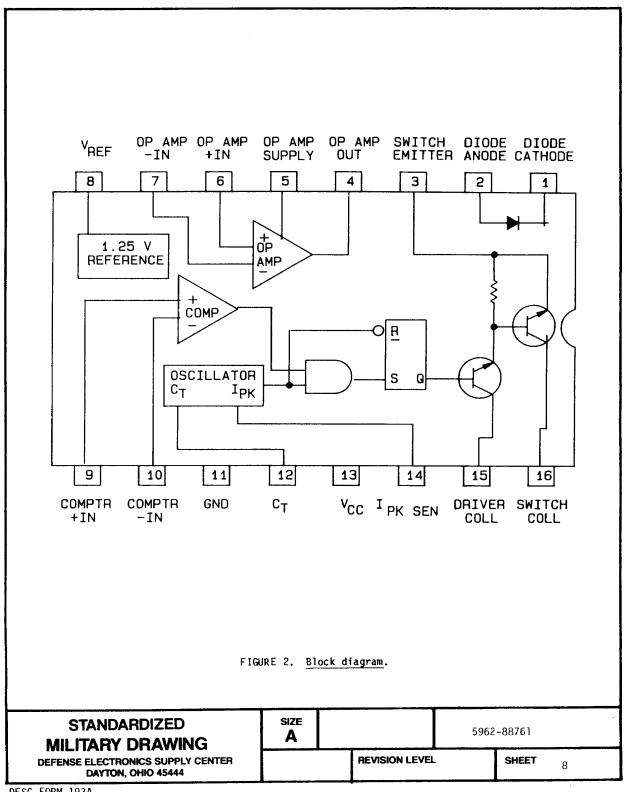
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- 3.8 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.
 - 4. QUALITY ASSURANCE PROVISIONS
- 4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).
- 4.2 <u>Screening</u>. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:
 - a. Burn-in test, method 1015 of MIL-STD-883.
 - Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_A = +125^{\circ}C$, minimum.
 - b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.
- 4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.
 - 4.3.1 Group A inspection.
 - a. Tests shall be as specified in table II herein.
 - b. Subgroups 5, 6, 7, 8, 9, 10, and 11 in table I, method 5005 of MIL-STD-883 shall be omitted.
 - 4.3.2 Groups C and D inspections.
 - a. End-point electrical parameters shall be as specified in table II herein.
 - b. Steady-state life test conditions, method 1005 of MIL-STD-883.
 - Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_A = +125^{\circ}C$, minimum.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements 	Subgroups (per method 5005, table I)
Interim electrical parameters (method 5004)	1
 Final electrical test parameters (method 5004)	1*,2,3,4
 Group A test requirements (method 5005)	1,2,3,4
 Groups C and D end-point electrical parameters (method 5005)	1

^{*} PDA applies to subgroup 1.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

- 6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.
- 6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.
- 6.3 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, OH 45444, or telephone 513-296-5375.

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6.4 Approved source of supply. An approved source of supply is listed herein. Additional sources will be added as they become available. The vendor listed herein has agreed to this drawing and a certificate of compliance (see 3.5 herein) has been submitted to DESC-ECS.

	Vendor	Vendor	Replacement
Military drawing	CAGE	similar part	military specification
part number	number	number <u>1</u> /	part number
5962-8876101EX	27014	μ A78S40DMQB	

1/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

> Vendor CAGE number

> > 27014

Vendor name and address

National Semiconductor 2900 Semiconductor Drive P.O. Box 58090 Santa Clara, CA 95052-8090

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