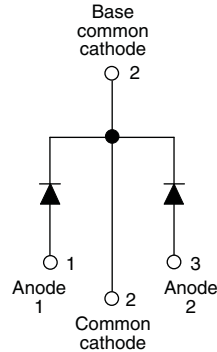


## HEXFRED®

### Ultrafast Soft Recovery Diode, 2 x 6 A


**TO-247AC**

**FEATURES**

- Ultrafast and ultrasoft recovery
- Very low  $I_{RRM}$  and  $Q_{rr}$
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified for industrial level


**RoHS**  
COMPLIANT

**BENEFITS**

- Reduced RFI and EMI
- Reduced power loss in diode and switching transistor
- Higher frequency operation
- Reduced snubbing
- Reduced parts count

**DESCRIPTION**

VS-HFA12PA120CPbF is a state of the art center tap ultrafast recovery diode. Employing the latest in epitaxial construction and advanced processing techniques it features a superb combination of characteristics which result in performance which is unsurpassed by any rectifier previously available. The VS-HFA12PA120CPbF has basic ratings of 1200 V and 6 A per leg continuous current. In addition to ultrafast recovery time, the HEXFRED® product line features extremely low values of peak recovery current ( $I_{RRM}$ ) and does not exhibit any tendency to “snap-off” during the  $t_b$  portion of recovery. The HEXFRED features combine to offer designers a rectifier with lower noise and significantly lower switching losses in both the diode and the switching transistor. These HEXFRED advantages can help to significantly reduce snubbing, component count and heatsink sizes. The HEXFRED VS-HFA12PA120CPbF is ideally suited for applications in power supplies and power conversion systems (such as inverters, converters, UPS systems, and power factor correction circuits), motor drives, and many other similar applications where high speed, high efficiency is needed.

**PRODUCT SUMMARY**

Package	TO-247AC
$I_{F(AV)}$	2 x 6 A
$V_R$	1200 V
$V_F$ at $I_F$	3.0 V
$t_{rr}$ (typ.)	26 ns
$T_J$ max.	150 °C
Diode variation	Single die

**ABSOLUTE MAXIMUM RATINGS**

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Cathode to anode voltage	$V_R$		1200	V
Maximum continuous forward current	$I_F$	$T_C = 100\text{ °C}$	6	A
			12	
Single pulse forward current	$I_{FSM}$		80	
Maximum repetitive forward current	$I_{FRM}$		24	
Maximum power dissipation	$P_D$	$T_C = 25\text{ °C}$	62.5	W
		$T_C = 100\text{ °C}$	25	
Operating junction and storage temperature range	$T_J, T_{Stg}$		- 55 to + 150	°C

ELECTRICAL SPECIFICATIONS (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V <sub>BR</sub>	I <sub>R</sub> = 100 μA	1200	-	-	V
Maximum forward voltage	V <sub>FM</sub>	I <sub>F</sub> = 6 A	-	2.7	3.0	
		I <sub>F</sub> = 12 A	-	3.5	3.9	
		I <sub>F</sub> = 6 A, T <sub>J</sub> = 125 °C	-	2.4	2.8	
Maximum reverse leakage current	I <sub>RM</sub>	V <sub>R</sub> = V <sub>R</sub> rated	-	0.26	5.0	μA
		T <sub>J</sub> = 125 °C, V <sub>R</sub> = 0.8 x V <sub>R</sub> rated	-	110	500	
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 200 V	-	9.0	14	pF
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8.0	-	nH

DYNAMIC RECOVERY CHARACTERISTICS (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> = 1.0 A, di <sub>F</sub> /dt = 200 A/μs, V <sub>R</sub> = 30 V	-	26	-	ns
	t <sub>rr1</sub>	T <sub>J</sub> = 25 °C	-	53	80	
	t <sub>rr2</sub>	T <sub>J</sub> = 125 °C	-	87	130	
Peak recovery current	I <sub>RRM1</sub>	T <sub>J</sub> = 25 °C	-	4.4	8.0	A
	I <sub>RRM2</sub>	T <sub>J</sub> = 125 °C	-	5.0	9.0	
Reverse recovery charge	Q <sub>rr1</sub>	T <sub>J</sub> = 25 °C	-	116	320	nC
	Q <sub>rr2</sub>	T <sub>J</sub> = 125 °C	-	233	585	
Peak rate of fall of recovery current during t <sub>b</sub>	di <sub>(rec)M</sub> /dt1	T <sub>J</sub> = 25 °C	-	180	-	A/μs
	di <sub>(rec)M</sub> /dt2	T <sub>J</sub> = 125 °C	-	100	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Lead temperature	T <sub>lead</sub>	0.063" from case (1.6 mm) for 10 s	-	-	300	°C
Thermal resistance, junction to case	R <sub>thJC</sub>		-	-	2.0	K/W
Thermal resistance, junction to ambient	R <sub>thJA</sub>	Typical socket mount	-	-	80	
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.50	-	
Weight			-	2.0	-	g
			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-247AC (JEDEC)	HFA12PA120C			

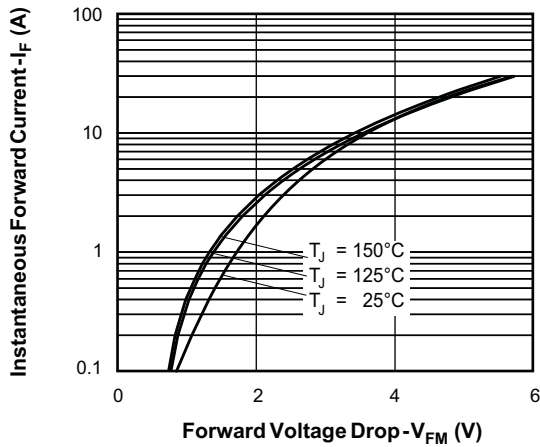


Fig. 1 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current

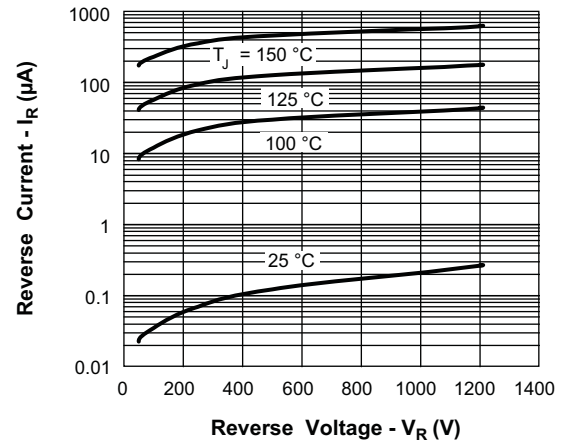


Fig. 2 - Typical Reverse Current vs. Reverse Voltage

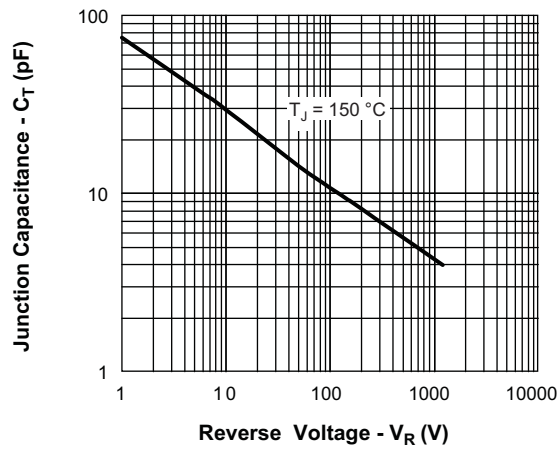


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

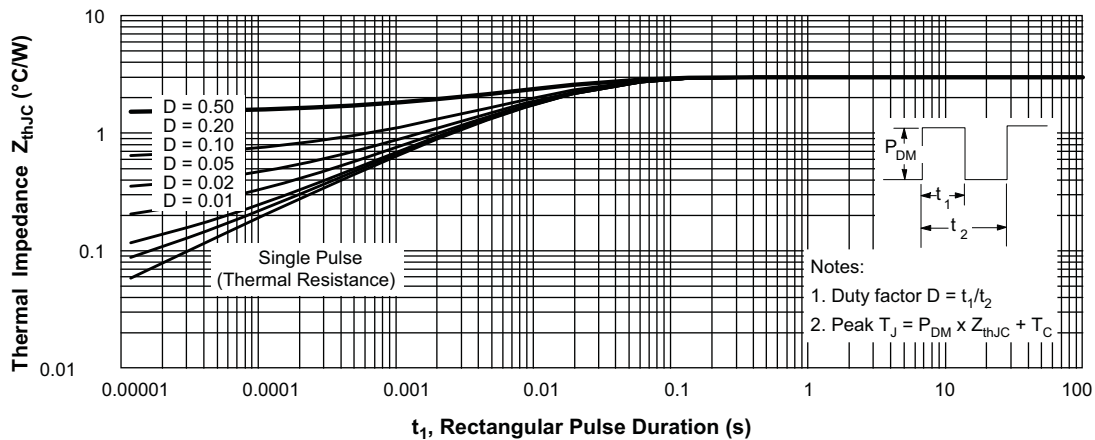


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

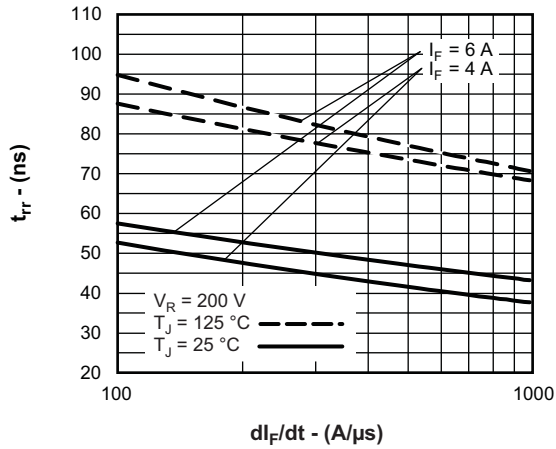


Fig. 5 - Typical Reverse Recovery Time vs.  $di_F/dt$

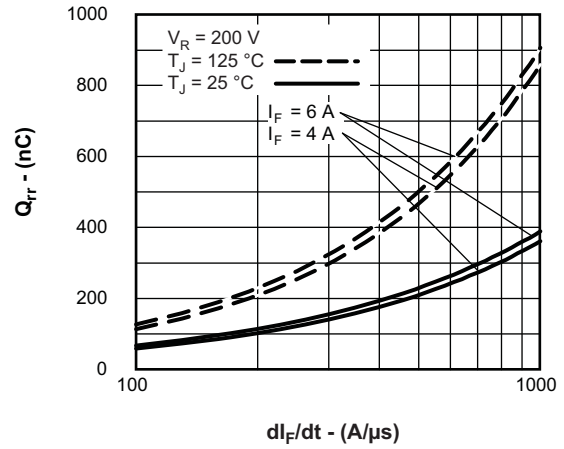


Fig. 7 - Typical Stored Charge vs.  $di_F/dt$

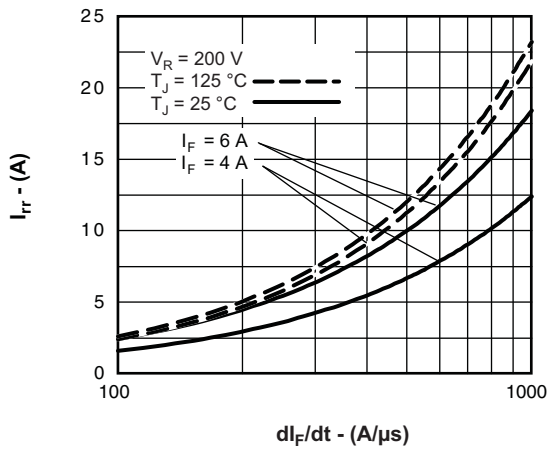


Fig. 6 - Typical Recovery Current vs.  $di_F/dt$

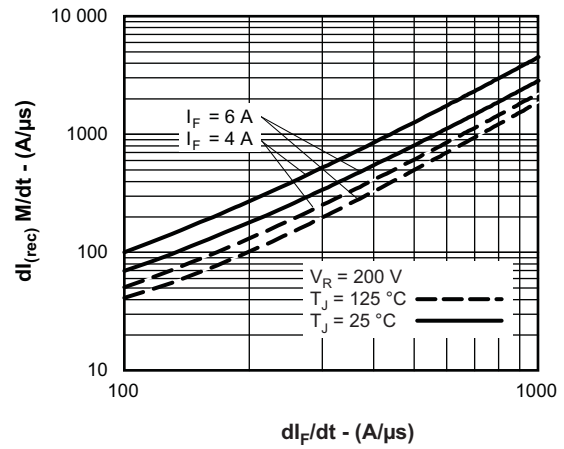


Fig. 8 - Typical  $dI_{(rec)M}/dt$  vs.  $di_F/dt$

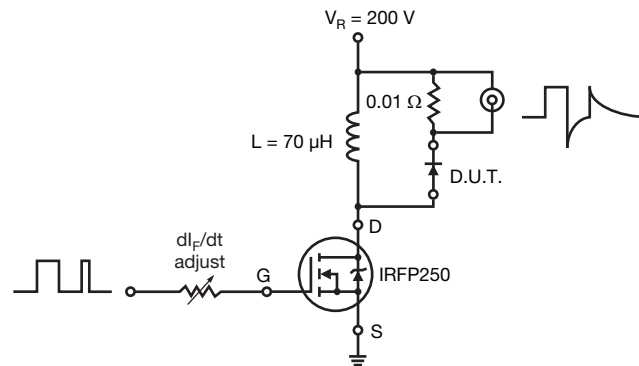
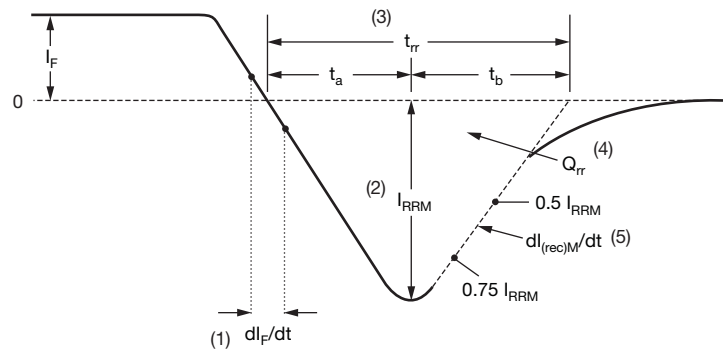
**HEXFRED<sup>®</sup>**  
 Ultrafast Soft Recovery Diode, 2 x 6 A


Fig. 9 - Reverse Recovery Parameter Test Circuit


 (1)  $di_f/dt$  - rate of change of current through zero crossing

 (2)  $I_{RRM}$  - peak reverse recovery current

 (3)  $t_{rr}$  - reverse recovery time measured from zero crossing point of negative going  $I_F$  to point where a line passing through  $0.75 I_{RRM}$  and  $0.50 I_{RRM}$  extrapolated to zero current.

 (4)  $Q_{rr}$  - area under curve defined by  $t_{rr}$  and  $I_{RRM}$ 

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

 (5)  $di_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

Fig. 10 - Reverse Recovery Waveform and Definitions

# VS-HFA12PA120CPbF

Vishay Semiconductors

HEXFRED®  
Ultrafast Soft Recovery Diode, 2 x 6 A



## ORDERING INFORMATION TABLE

Device code	VS-	HF	A	12	PA	120	C	PbF
	1	2	3	4	5	6	7	8

- 1** - Vishay Semiconductors product
- 2** - HEXFRED® family
- 3** - Electron irradiated
- 4** - Current rating (12 = 12 A)
- 5** - PA = TO-247AC
- 6** - Voltage rating: (120 = 1200 V)
- 7** - Circuit configuration  
C = Common cathode
- 8** - PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS	
Dimensions	<a href="http://www.vishay.com/doc?95223">www.vishay.com/doc?95223</a>
Part marking information	<a href="http://www.vishay.com/doc?95226">www.vishay.com/doc?95226</a>



### DIMENSIONS in millimeters and inches



#### Lead assignments

- Diodes**  
 1. - Anode/open  
 2. - Cathode  
 3. - Anode

SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	
A	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.37	0.065	0.094	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
c	0.38	0.86	0.015	0.034	
c1	0.38	0.76	0.015	0.030	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.72	-	0.540	-	
e	5.46 BSC		0.215 BSC		
FK	2.54		0.010		
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
N	7.62 BSC		0.3		
$\phi P$	3.56	3.66	0.14	0.144	
$\phi P1$	-	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	1.78	0.216	
S	5.51 BSC		0.217 BSC		

#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6)  $\phi P$  to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC outline TO-247 with exception of dimension c



## Disclaimer

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