



TELECOM EQUIPMENT PROTECTION: TRISIL™

FEATURES

- Bidirectional crowbar protection
- Voltage range from 62V to 270V
- Low capacitance from 15pF to 30pF typ. @ 50V
- Low leakage current: $I_R = 2\mu\text{A}$ max.
- Holding current: $I_H = 150$ mA Min.
- Repetitive peak pulse current:
 $I_{PP} = 50$ A (10/1000 μs)

MAIN APPLICATIONS

Telecommunication equipment such as:

- Analog and digital line cards (xDSL, T1/E1, ISDN...).
- Terminals (phone, fax, modem...) and central office equipment.

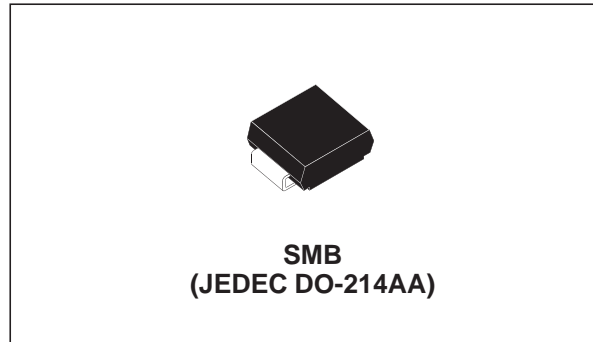
DESCRIPTION

The SMTPAxxx series has been designed to protect telecommunication equipment against lightning and transient induced by AC power lines.

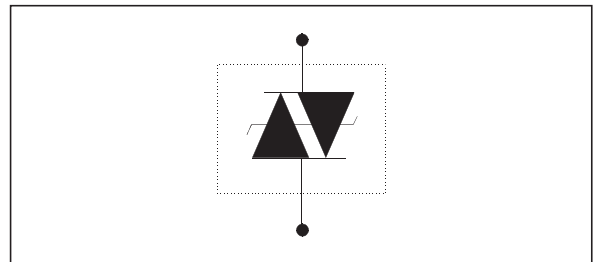
The package / die size ratio has been optimized by using the SMB package.

BENEFITS

Trisils are not subject to ageing and provide a fail safe mode in short circuit for a better protection. Trisils are used to help equipment to meet various standards such as UL1950, IEC950 / CSA C22.2, UL1459 and FCC part 68. Trisils have UL94 V0 resin approved. SMB package is JEDEC registered. (Trisils are UL 497B approved - file: E136224).



SCHEMATIC DIAGRAM



SMTPAxxx

IN COMPLIANCES WITH THE FOLLOWING STANDARDS

Standard	Peak Surge Voltage (V)	Voltage Waveform (μ s)	Required peak current (A)	Current Waveform (μ s)	Minimum serial resistor to meet standard (Ω)
GR-1089 Core First level	2500	2/10	500	2/10	12
	1000	10/1000	100	10/1000	10
GR-1089 Core Second level	5000	2/10	500	2/10	24
GR-1089 Core Intra-building	1500	2/10	100	2/10	0
ITU-T-K20 / K21	6000	10/700	150	5/310	53
	1500		37.5		0
ITU-T-K20 (IEC61000-4-2)	6000	1/60 ns	ESD contact discharge		0
	8000		ESD air discharge		0
VDE0433	4000	10/700	100	5/310	21.5
	2000		50		0
VDE0878	4000	1.2/50	100	1/20	0
	2000		50		0
IEC61000-4-5	4000	10/700	100	5/310	21.5
	4000	1.2/50	100	8/20	0
FCC Part 68, lightning surge type A	1500	10/160	200	10/160	12.5
	800	10/560	100	10/560	6.5
FCC Part 68, lightning surge type B	1000	9/720	25	5/320	0

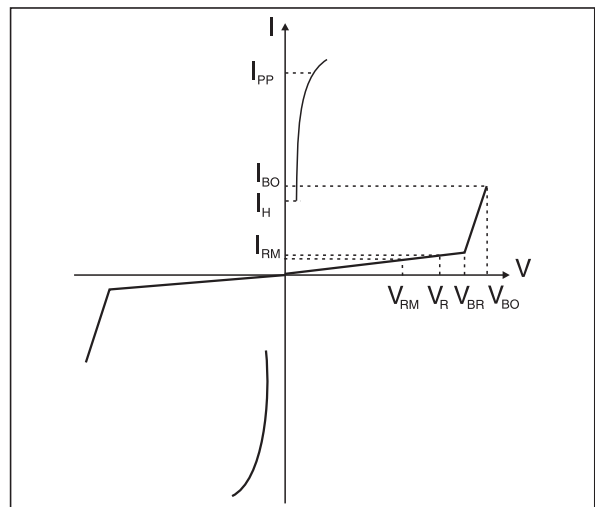
THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-a)}$	Junction to ambient with recommended footprint	100	$^{\circ}C/W$
$R_{th(j-l)}$	Junction to leads	20	$^{\circ}C/W$

ELECTRICAL CHARACTERISTICS

($T_{amb} = 25^{\circ}C$)

Symbol	Parameter
V_{RM}	Stand-off voltage
I_{RM}	Leakage current at V_{RM}
V_R	Continuous reverse voltage
V_{BR}	Breakdown voltage
V_{BO}	Breakover voltage
I_H	Holding current
I_{BO}	Breakover current
I_{PP}	Peak pulse current
C	Capacitance



ABSOLUTE RATINGS ($T_{amb} = 25^{\circ}\text{C}$)

Symbol	Parameter		Value	Unit
I _{PP}	Repetitive peak pulse current:	10/1000 μs	50	A
		8/20 μs	100	
		10/560 μs	55	
		5/310 μs	65	
		10/160 μs	75	
		1/20 μs	100	
		2/10 μs	150	
I _{FS}	Fail safe mode: maximum current	8/20 μs	2.5	kA
I _{TSM}	Non repetitive surge peak on-state current (Sinusoidal)	t = 20ms	30	A
		t = 16.6ms	32	
		t = 0.2s	17	
		t = 2s	9	
I ² t	I ² t value for fusing	t = 16.6ms	8.5	A ² s
		t = 20ms	9	
T _L	Maximum lead temperature for soldering during 10 s.		260	$^{\circ}\text{C}$
T _{stg} T _j	Storage temperature range		- 55 to + 150	$^{\circ}\text{C}$
	Maximum junction temperature		150	$^{\circ}\text{C}$

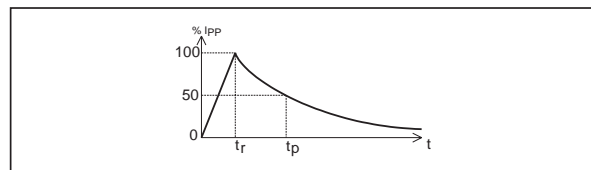
Repetitive peak pulse current

tr: rise time (μs)

tp: pulse duration time (μs)

ex.: Pulse waveform 10/1000 μs

tr = 10 μs tp = 1000 μs



SMTPAxxx

ELECTRICAL PARAMETERS (Tamb = 25°C)

Type	IRM @ VRM max		IR @ VR MAX		DYNAMIC VBO @ IBO max		STATIC VBO @ IBO max		IH min	C typ.	C typ.
	µA	V	µA	V	V	mA	V	mA	mA	pF	pF
SMTPA62	2	56	50	62	85	800	82	800	150	30	50
SMTPA68		61		68	93		90		150	30	45
SMTPA100		90		100	135		133		150	20	40
SMTPA120		108		120	160		160		150	20	40
SMTPA130		117		130	173		173		150	20	35
SMTPA180		162		180	235		240		150	15	30
SMTPA200		180		200	262		267		150	15	30
SMTPA220		198		220	285		293		150	15	30
SMTPA240		216		240	300		320		150	15	30
SMTPA270		243		270	350		360		150	15	30

Note 1: IR measured at VR guarantee VBRmin ≥ VR

Note 2: See functional breakover voltage test circuit 1.

Note 3: See test circuit 2.

Note 4: See functional holding current test circuit 3.

Note 5: VR = 50V bias, VRMS = 1V, F = 1MHz.

Note 6: VR = 2V bias, VRMS = 1V, F = 1MHz

Fig. 1: Non repetitive surge peak on-state current versus overload duration (Tj initial = 25°C)

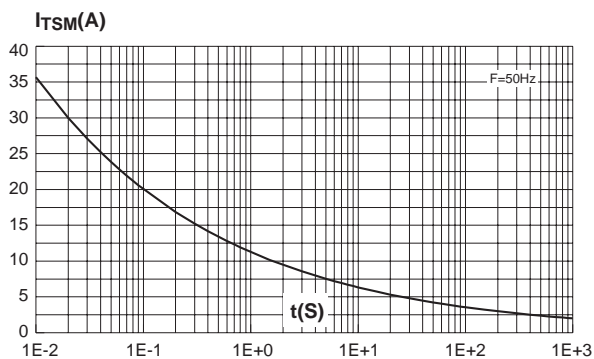


Fig. 2: On-state voltage versus on-state current (typical values).

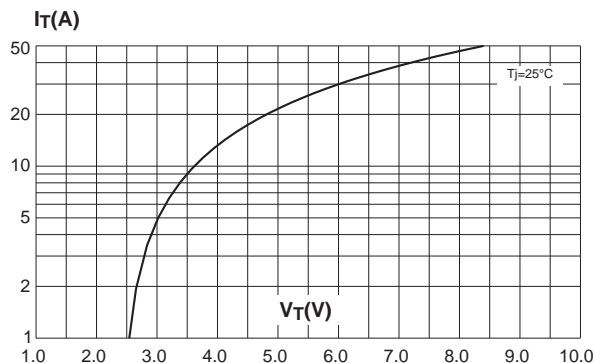


Fig. 3: Relative variation of holding current versus junction temperature.

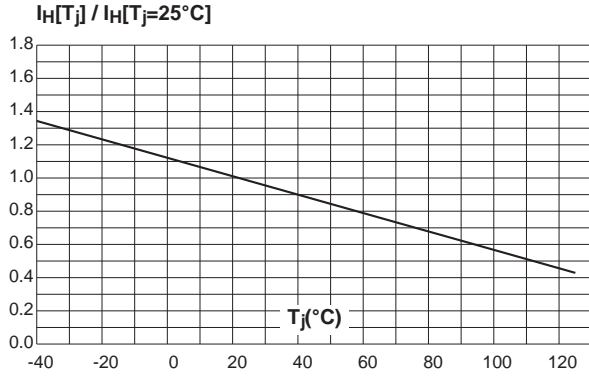


Fig. 4: Relative variation of breakover voltage versus junction temperature.

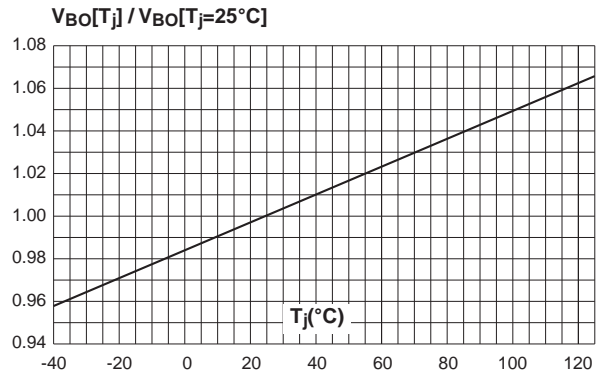


Fig. 5: Relative variation of leakage current versus junction temperature (typical values).

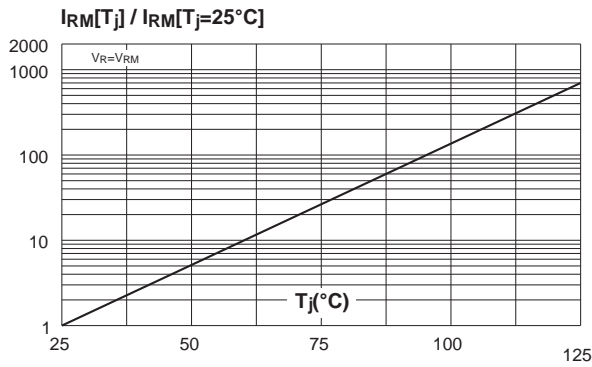


Fig. 6: Relative variation of thermal impedance versus pulse duration.

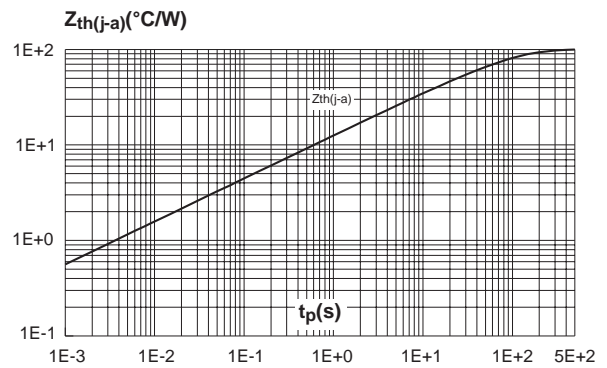
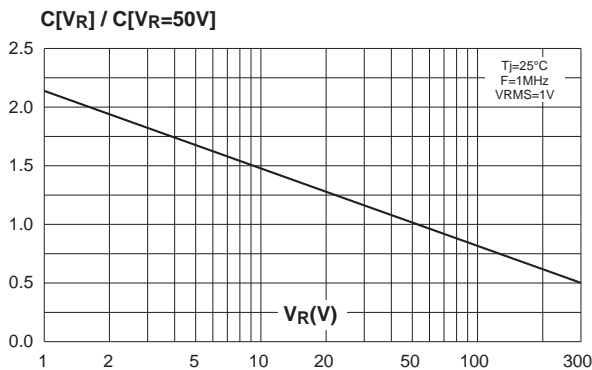
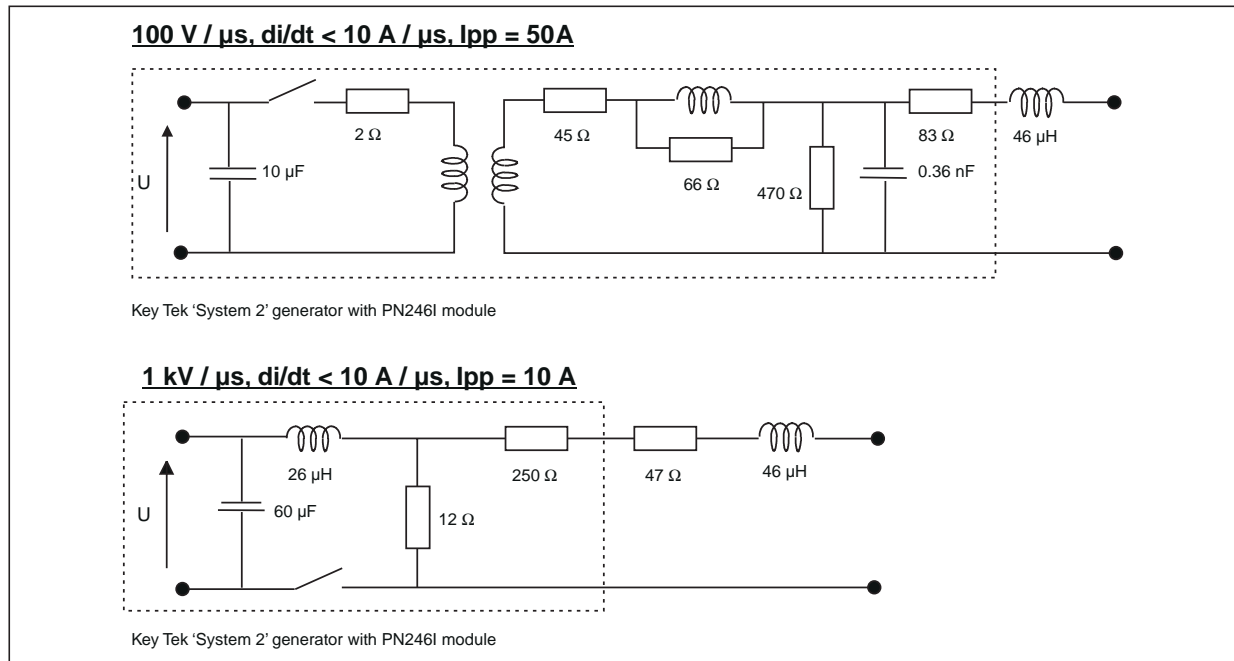


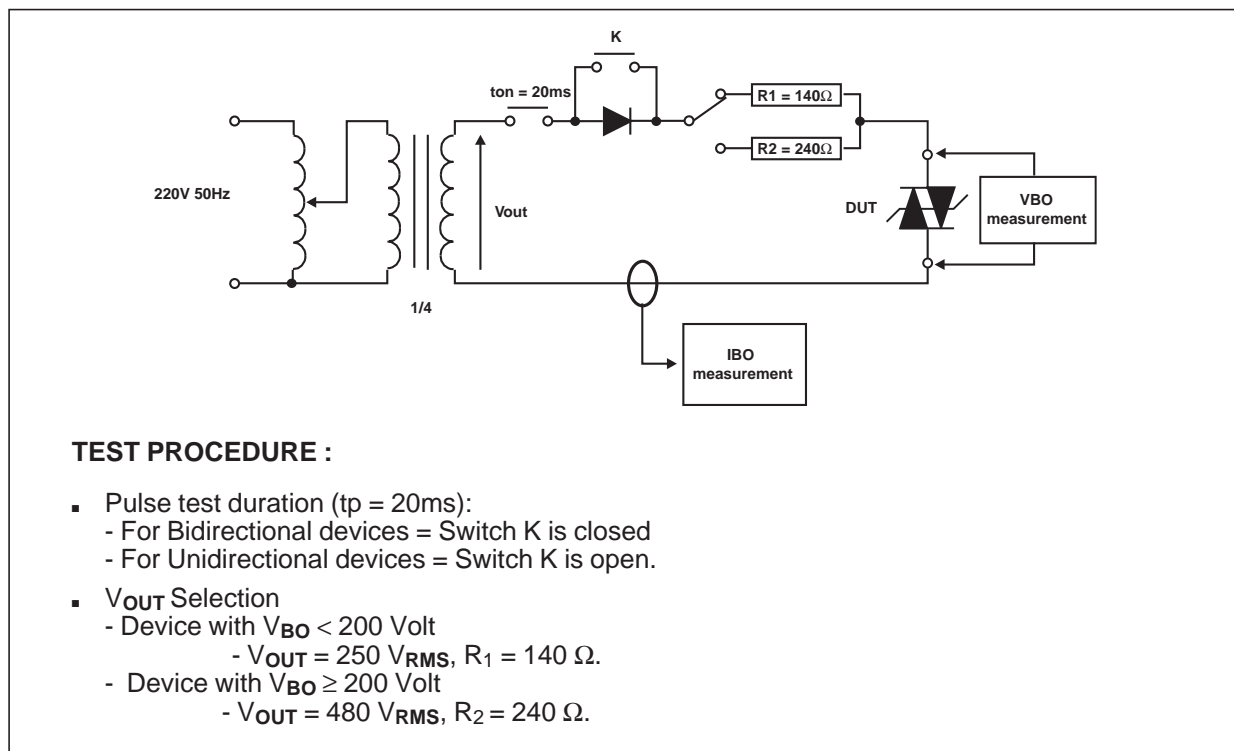
Fig. 7: Relative variation of junction capacitance versus reverse voltage applied (typical values).



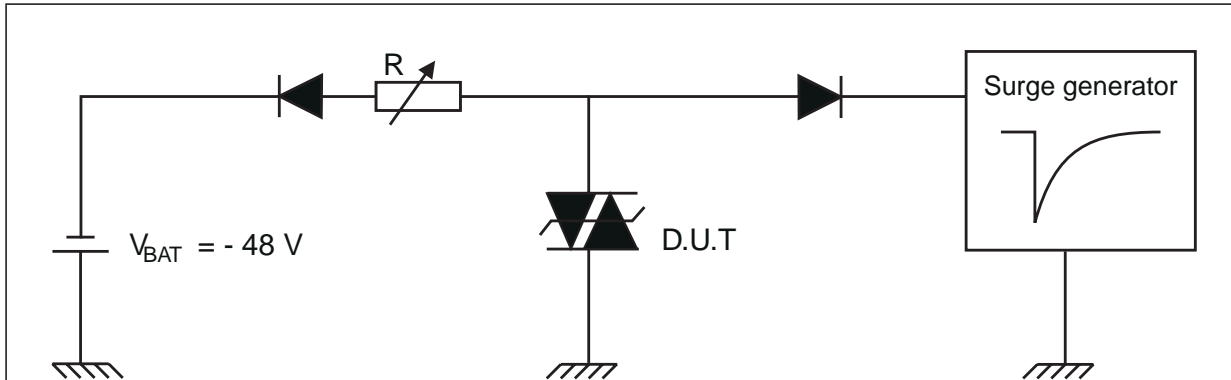
TEST CIRCUIT 1 FOR DYNAMIC I_{BO} and V_{BO} PARAMETERS



TEST CIRCUIT 2 for I_{BO} AND V_{BO} PARAMETERS.



TEST CIRCUIT 3 for I_H PARAMETERS.



This is a GO-NO GO test which allows to confirm the holding current (I_H) level in a functional test circuit.

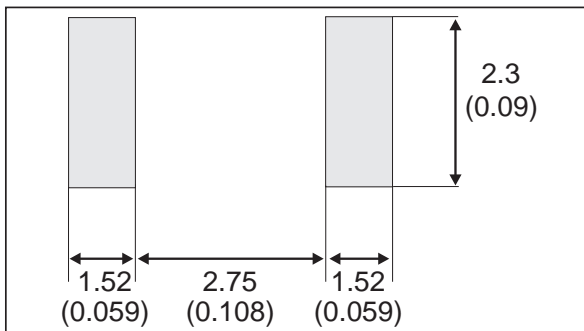
TEST PROCEDURE :

- Adjust the current level at the I_H value by short circuiting the D.U.T.
- Fire the D.U.T. with a surge current : $I_{pp} = 10A, 10/1000 \mu s$.
- The D.U.T. will come back to the off-state within 50 ms max.

PACKAGE MECHANICAL DATA
SMB (JEDEC DO-214AA)

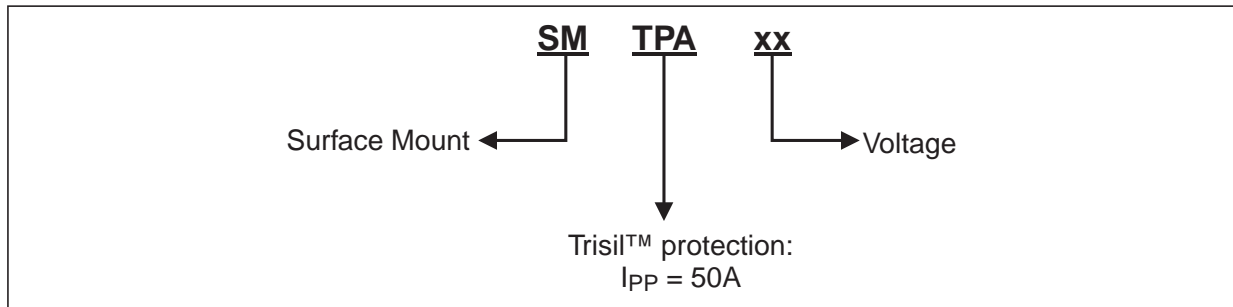
REF.	DIMENSIONS			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A1	1.90	2.45	0.075	0.096
A2	0.05	0.20	0.002	0.008
b	1.95	2.20	0.077	0.087
c	0.15	0.41	0.006	0.016
E	5.10	5.60	0.201	0.220
E1	4.05	4.60	0.159	0.181
D	3.30	3.95	0.130	0.156
L	0.75	1.60	0.030	0.063

FOOT PRINT in millimeters (inches)



SMTPAxxx

ORDER CODE



ORDERING INFORMATION

Part number	Marking	Package	Weight	Base qty	Delivery mode
SMTPA62	U01	SMB	0.11 g	5000	Tape & reel
SMTPA68	U05				
SMTPA100	U13				
SMTPA120	U17				
SMTPA130	U19				
SMTPA180	U25				
SMTPA200	U27				
SMTPA220	U31				
SMTPA240	U35				
SMTPA270	U39				

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