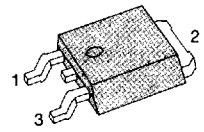
FEATURES

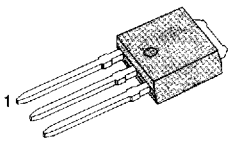
- **Lower $R_{DS(ON)}$**
- **Excellent voltage stability**
- **Fast switching speeds**
- **Rugged polysilicon gate cell structure**
- **Lower input capacitance**
- **Extended safe operating area**
- **Improved high temperature reliability**

D-PAK



1.Gate 2.Drain 3.Source
IRLR014/010

I-PAK



1.Gate 2.Drain 3.Source
IRLU014/010

PRODUCT SUMMARY

Part Number	V_{DS}	$R_{DS(on)}$	I_D
IRLR014/U014	60V	0.30Ω	6.7A
IRLR010/U010	50V	0.30Ω	6.7A

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ABSOLUTE MAXIMUM RATINGS

Characteristic	Symbol	IRLR014 IRLU014	IRLR010 IRLU010	Unit
Drain-Source Voltage (1)	V_{DSS}	60	50	Vdc
Drain-Gate Voltage (R_{GS}=1MΩ)(1)	V_{DGR}	60	50	Vdc
Gate-Source Voltage	V_{GS}	±15		Adc
Continuous Drain Current T_C=25 °C	I_D	6.7		Adc
Continuous Drain Current T_C=100 °C	I_D	4.2		Adc
Drain Current - Pulsed (3)	I_{DM}	27		Adc
Total Power Dissipation @ T_C=25 °C	P_D	25		Watts
Derate Above 25 °C		0.20		W/ °C
Operating and Storage Junction Temperature Range	T_J, T_{STG}	-55 to +150		°C
Maximum Lead Temp. for Soldering Purposes, 1/8" from case for 5 seconds	T_L	300		°C

Notes : (1) T_J=25°C to 150°C
(2) Pulse test : Pulse width ≤ 300μs, Duty Cycle ≤ 2%
(3) Repetitive rating : Pulse width limited by junction temperature

ELECTRICAL CHARACTERISTICS (T_C=25°C unless otherwise specified)

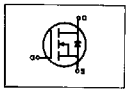
Symbol	Characteristic	Min	Typ	Max	Units	Test Conditions
BV_{DSS}	Drain-Source Breakdown Voltage					
	IRLR014/U014	60	-	-	V	V_{GS}=0V, I_D=250μA
	IRLR010/U010	50	-	-	V	
$V_{GS(th)}$	Gate Threshold Voltage	1.0	-	2.0	V	V_{DS}=V_{GS}, I_D=1mA
I_{GSS}	Gate-Source Leakage Forward	-	-	100	nA	V_{GS}=15V
I_{GSS}	Gate-Source Leakage Reverse	-	-	-100	nA	V_{GS}=-15V
I_{DSS}	Zero Gate Voltage	-	-	250	μA	V_{DS}=Max. Rating, V_{GS}=0V
	Drain Current	-	-	1000	μA	V_{DS}=0.8 Max. Rating, V_{GS}=0V, T_C=125°C
$R_{DS(on)}$	Static Drain-Source On Resistance(2)	-	-	0.30	Ω	V_{GS}=5.0V, I_D=3.4A
g_{fs}	Forward Transconductance (2)	4.0	-	-	℧	V_{DS}≥15V, I_D=3.4A
C_{iss}	Input Capacitance	-	400	-	pF	V_{GS}=0V, V_{DS}=25V, f=1.0MHz
C_{oss}	Output Capacitance	-	150	-	pF	
C_{rss}	Reverse Transfer Capacitance	-	50	-	pF	
$t_{d(on)}$	Turn-On Delay Time	-	12	20	ns	V_{DD}=0.5 BV_{DSS}, I_D=6.7A, Z_O=24Ω
t_r	Rise Time	-	35	50	ns	(MOSFET switching times are essentially
$t_{d(off)}$	Turn-Off Delay Time	-	35	50	ns	independent of operating temperature)
t_f	Fall Time	-	30	40	ns	
Q_g	Total Gate Charge	-	-	13	nC	V_{GS}=5V, I_D=6.7A, V_{DS}=0.8 Max. Rating
	(Gate-Source Plus Gate-Drain)					(Gate charge is essentially independent of
Q_{gs}	Gate-Source Charge	-	3.8	-	nC	operating temperature)
Q_{gd}	Gate-Drain Charge	-	4.8	-	nC	

THERMAL RESISTANCE

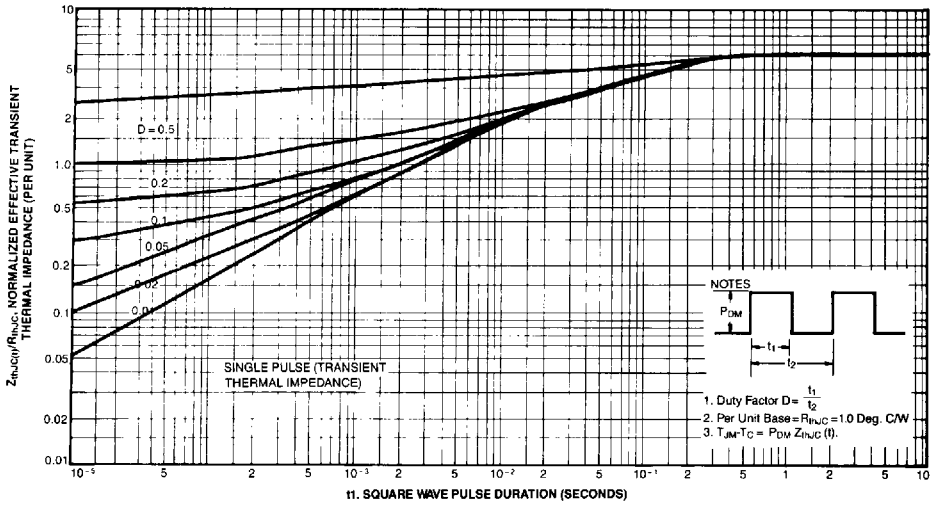
Symbol	Characteristics		All	Units	Remark
R_{thJC}	Junction-to-Case	MAX	5.0	K/W	
R_{thCS}	Case-to-Sink	TYP	1.7	K/W	Mounting surface flat, smooth, and greased
R_{thJA}	Junction-to-Ambient	MAX	110	K/W	Free Air Operation

Notes : (1) T_J=25°C to 150°C
(2) Pulse test : Pulse width≤300μs, Duty Cycle≤2%
(3) Repetitive rating : Pulse width limited by max. junction temperature

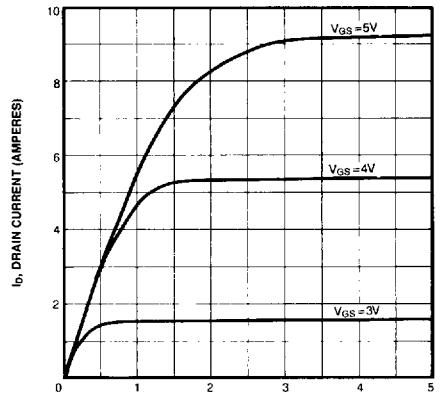
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Symbol	Characteristic	Min	Typ	Max	Units	Test Condition
I_S	Continuous Source Current (Body Diode)	—	—	6.7	A	Modified MOSFET symbol showing the integral reverse P-N junction rectifier 
I_{SM}	Pulse Source Current (Body Diode) (3)	—	—	27	A	
V_{SD}	Diode Forward Voltage (2)	—	—	1.8	V	T_J=25°C, I_S=6.7A, V_{GS}=0V
t_{rr}	Reverse Recovery Time	—	—	200	ns	T_J=25°C, I_F=6.7A, dI_F/dt=100A/μS

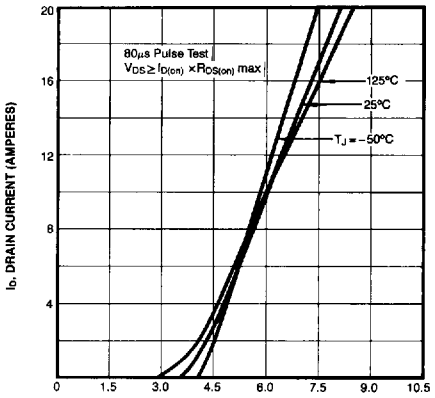
Notes : (1) T_J=25°C to 150°C
(2) Pulse test : Pulse width ≤ 300μs, Duty Cycle ≤ 2%
(3) Repetitive rating : Pulse width limited by max. junction temperature



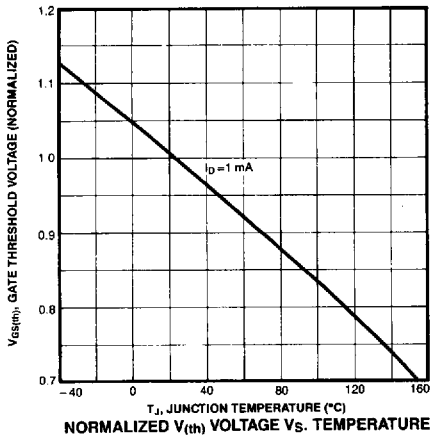
MAXIMUM EFFECTIVE TRANSIENT THERMAL IMPEDANCE JUNCTION-TO-CASE V_S. PULSE DURATION



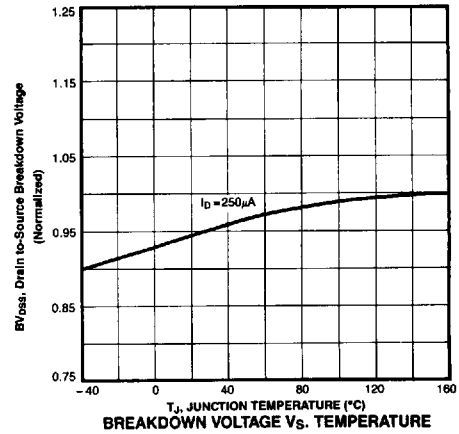
TYPICAL SATURATION CHARACTERISTICS



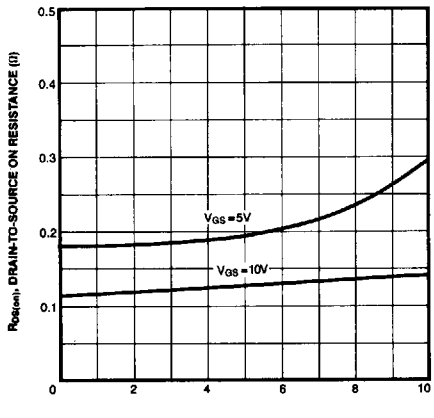
TYPICAL TRANSFER CHARACTERISTICS



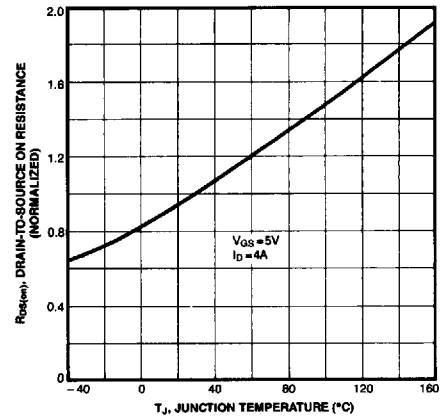
NORMALIZED $V_{(th)}$ VOLTAGE V_S. TEMPERATURE



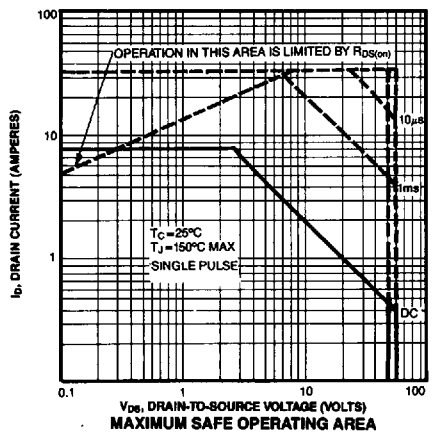
BREAKDOWN VOLTAGE V_S. TEMPERATURE



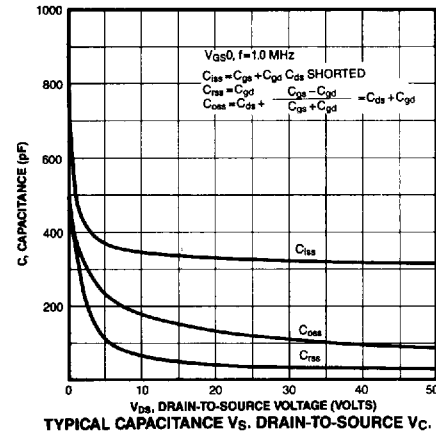
TYPICAL ON-RESISTANCE V_S. DRAIN CURRENT



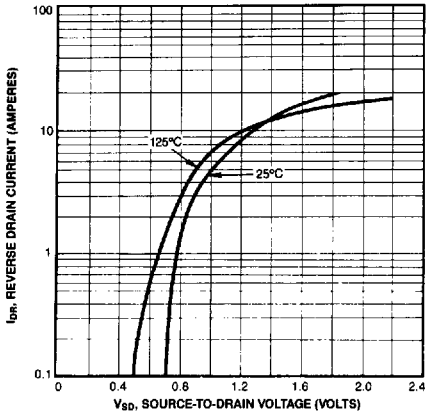
NORMALIZED ON-RESISTANCE V_S. TEMPERATURE



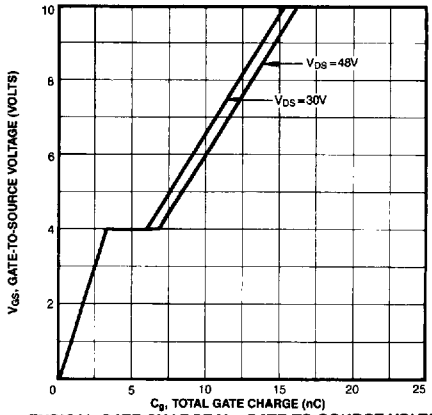
MAXIMUM SAFE OPERATING AREA



TYPICAL CAPACITANCE V_S. DRAIN-TO-SOURCE V_C. TAGE



TYPICAL SOURCE — DRAIN DIODE FORWARD VOLTAGE



TYPICAL GATE CHARGE V_S. GATE-TO-SOURCE VOLTAGE

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