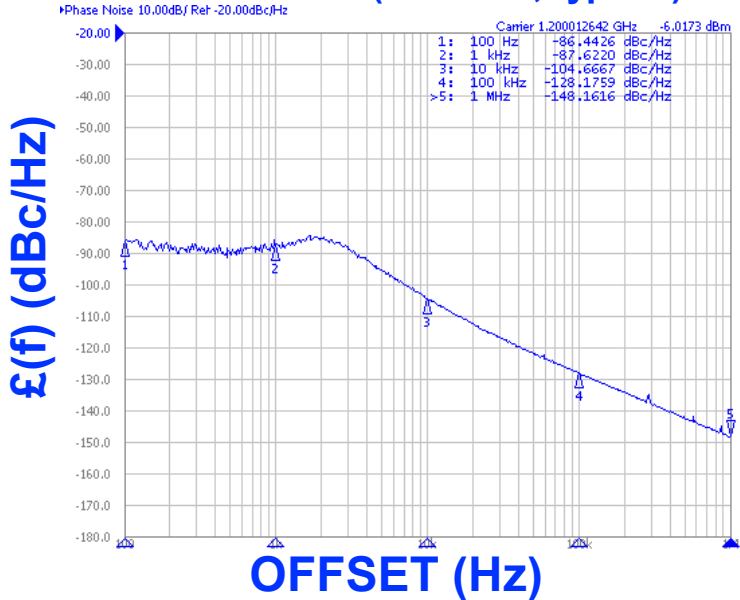


PHASE NOISE (1 Hz BW, typical)



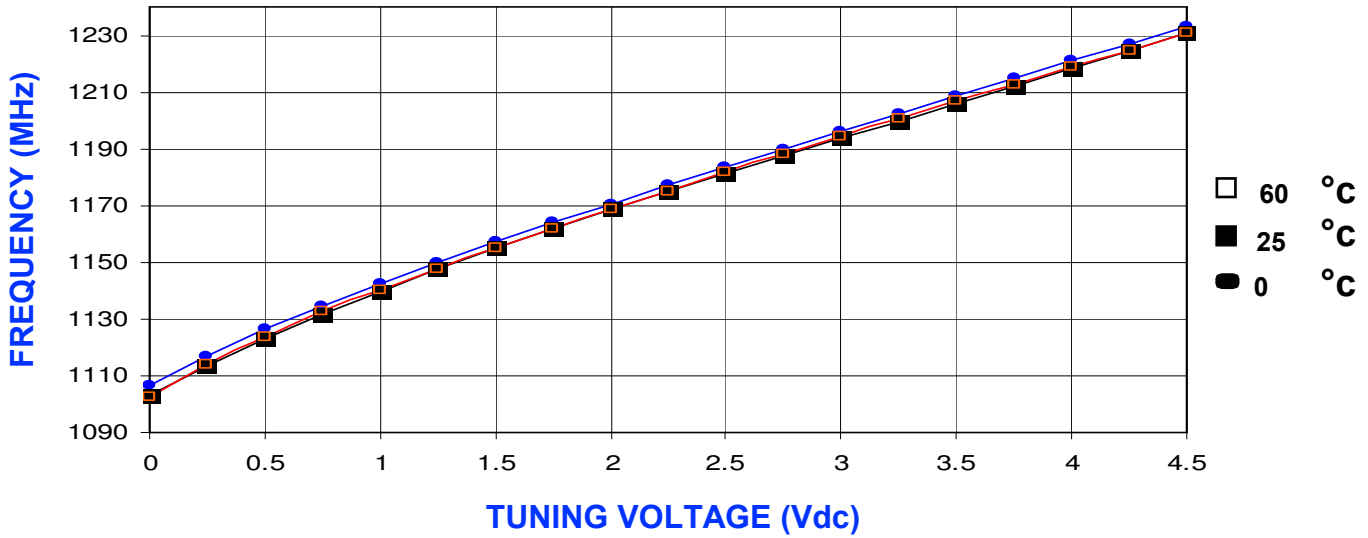
FEATURES
• Frequency Range: 1200 - 1200 MHz
• Tuning Voltage: Vdc
• PLL-24 - Style Package
APPLICATIONS
• Basestations
• Satellite Communications
•

PERFORMANCE SPECIFICATIONS	VALUE	UNITS
Oscillation Frequency Range	1200 - 1200	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-103	dBc/Hz
Harmonic Suppression (2nd, typ.)	-15	dBc
Tuning Voltage		Vdc
Tuning Sensitivity (avg.)	5000	MHz/V
Power Output	0±3	dBm
Load Impedance	50	Ω
Input Capacitance (max.)	1000	pF
Pushing	2	MHz/V
Pulling (14dB Return Loss, Any Phase)	2	MHz
Operating Temperature Range	0 to 60	°C
Package Style	PLL-24	
POWER SUPPLY REQUIREMENTS		
Supply Voltage (Vcc, nom.)	5	Vdc
Supply Current (Icc, typ.)	33	mA

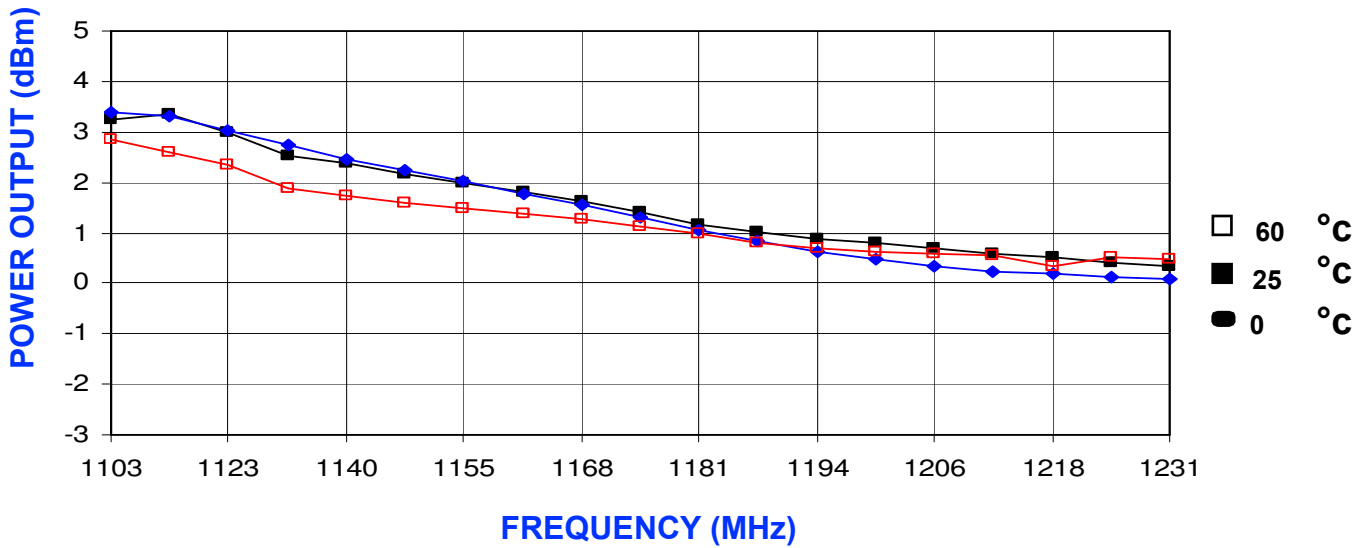
All specifications are typical unless otherwise noted and subject to change without notice.

APPLICATION NOTES
• AN-100/1 : Mounting and Grounding of VCOs
• AN-102 : Proper Output Loading of VCOs
• AN-107 : How to Solder Z-COMM VCOs
NOTES:
Reference Oscillator Signal: 5 MHz f_{osc} <math>< 100</math> MHz
Frequency Synthesizer: Analog Devices - ADF4106

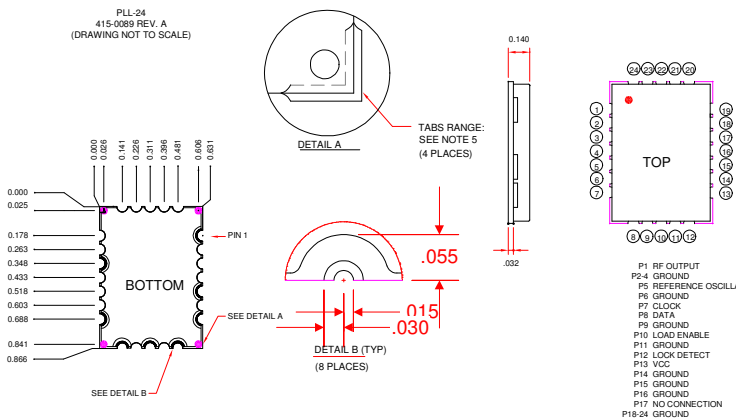
TUNING CURVE, typ.



POWER CURVE, typ.



PHYSICAL DIMENSIONS



1. The inside radius of all 24 half holes at the perimeter of the board are plated to provide a surface for the attachment of the PLL Module to the PCB. 16 pads are for grounding, 8 pads are for signal interfaces.
2. The surface of the shield is tin-plated and may be soldered to. The shield's base metal is cold rolled steel.
3. The ground plane on the bottom side is ground and switches to a ground track on the top side of the board as well as to the shield.
4. Unless otherwise noted all dimensions are in inches.
5. Unless otherwise noted all tolerances are as follows:
xxx = ± .010

- P1 RF OUTPUT
- P2-4 GROUND
- P5 REFERENCE OSCILLATOR INPUT
- P6 GROUND
- P7 CLOCK
- P8 DATA
- P9 GROUND
- P10 LOAD ENABLE
- P11 GROUND
- P12 LOCK DETECT
- P13 VCC
- P14 GROUND
- P15 GROUND
- P16 GROUND
- P17 NO CONNECTION
- P18-24 GROUND