Low voltage variable capacitance diode

Rev. 2 — 8 September 2011

Product data sheet

1. Product profile

1.1 General description

The BB208-02 is a planar technology variable capacitance diode in a SOD523 (SC-79) ultra small SMD plastic package.

The BB208-03 is a planar technology variable capacitance diode in a SOD323 (SC-76) very small SMD plastic package.

1.2 Features and benefits

- Very small SMD plastic packages
- Very low series resistance
- Excellent CV linearity
- C_{d(1V)}: 21.5 pF; C_{d(7.5V)}: 4.9 pF
- High ratio.

1.3 Applications

- Voltage Controlled Oscillators (VCO)
- Voltage Controlled Crystal Oscillators/Temperature Controlled Crystal Oscillators (VCXO/TCXO).

2. Pinning information

Table 1.	Discrete pinning: SOD523		
Pin	Description	Simplified outline	Symbol
1	cathode		Ш
2	anode	12	sym008
Table 2.	Discrete pinning: SOD323		
Pin	Description	Simplified outline	Symbol
1	cathode		Ш
2	anode		\ ↓ ↓



svm008

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3. Ordering information

Table 3. Ordering information					
Type number Package					
Name	Description	Version			
-	plastic surface mounted package; 2 leads	SOD523			
-	plastic surface mounted package; 2 leads	SOD323			
	Package Name	Package Name Description - plastic surface mounted package; 2 leads			

4. Marking

Table 4. Marking	
Type number	Marking code
BB208-02	A1
BB208-03	A2

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Мах	Unit
V _R	continuous reverse voltage		-	10	V
I _F	continuous forward current		-	20	mA
T _{stg}	storage temperature		-55	+150	°C
Tj	operating junction temperature		-55	+125	°C

6. Characteristics

Table 6. Electrical characteristics

 $T_i = 25 \ ^{\circ}C$ unless otherwise specified.

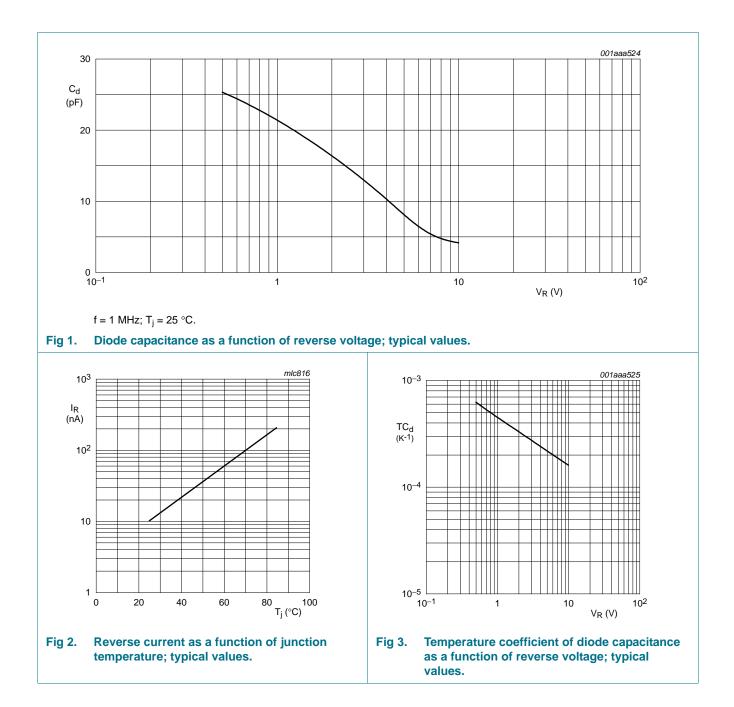
J						
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
I _R	reverse current	$V_R = 10 V$; see <u>Figure 2</u>	-	-	10	nA
		$V_R = 10 \text{ V}; \text{ T}_j = 85 \text{ °C}; \text{ see } \frac{\text{Figure 2}}{100000000000000000000000000000000000$	-	-	200	nA
r _s	diode series resistance	f = 100 MHz; V _R = 3 V	-	0.35	0.5	Ω
C _d diode capacitance		f = 1 MHz; see Figure 1 and Figure 3				
		$V_R = 1 V$	19.9	-	23.2	pF
		$V_R = 4 V$	-	10.1	-	pF
		V _R = 7.5 V	4.5	-	5.4	pF
$\frac{C_{d(1V)}}{C_{d(4V)}}$	capacitance ratio	f = 1 MHz	2.0	-	-	
$\frac{C_{d(1V)}}{C_{d(7.5V)}}$	capacitance ratio	f = 1 MHz	3.7	-	5.2	

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7. Package outline

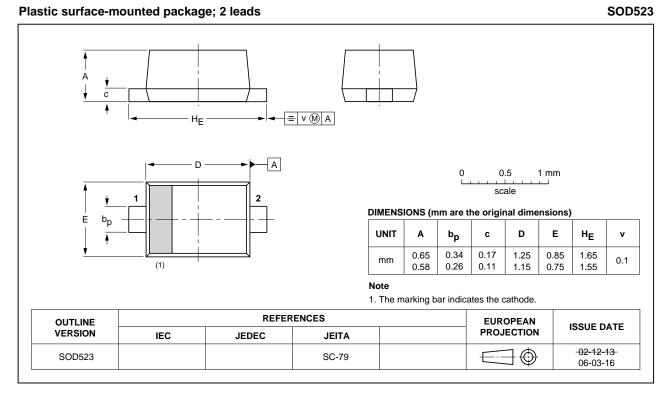


Fig 4. Package outline (BB208-02).

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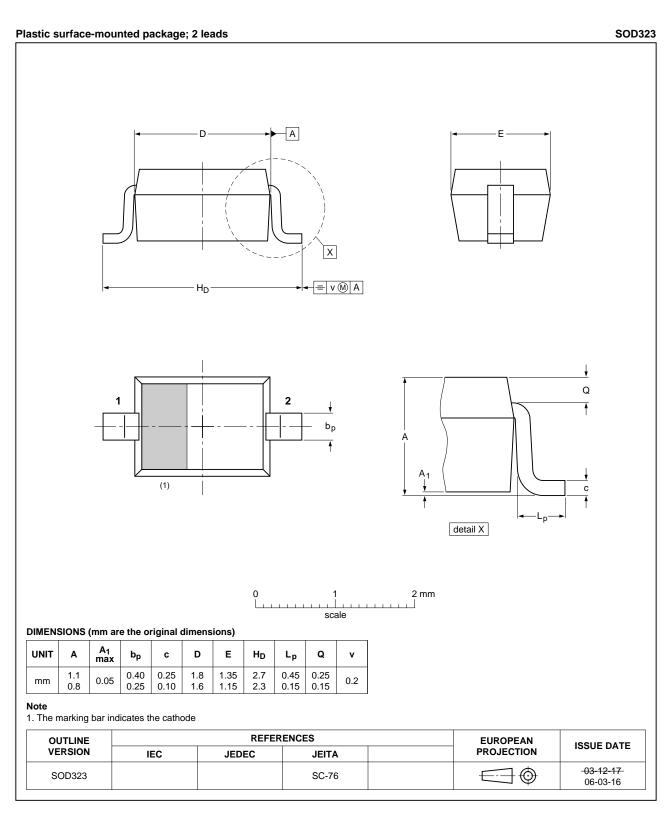


Fig 5. Package outline (BB208-03).

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8. Revision history

Table 7. Revision histe	ory			
Document ID	Release date	Data sheet status	Change notice	Supersedes
BB208-02_BB208-03 v.2	20110908	Product data sheet	-	BB208-02_BB208-03 v.1
Modifications:		of this data sheet has been NXP Semiconductors.	n redesigned to comply w	ith the new identity
	 Legal texts h 	ave been adapted to the	new company name whe	re appropriate.
	 Package out 	line drawings have been	updated to the latest vers	ion.
BB208-02_BB208-03 v.1 (9397 750 12696)	20040407	Product data	-	-

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9. Legal information

9.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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Date of release: 8 September 2011 Document identifier: BB208-02; BB208-03