



A Product Line of Diodes Incorporated



DRDC3105

INTEGRATED RELAY, INDUCTIVE LOAD DRIVER

Description and Applications

The DRDC3105 is an integrated solid-state DC relay driver that can switch inductive loads. It provides a robust driver interface by acting as a buffer stage between sensitive logic circuits and that of 3 to 6V DC inductive relay coils. With a low input drive current requirement, the DRDC3105 only has slight loading on the input circuitry and it will provide good transient isolation between output and input channels. The output switch is guaranteed by design to go open-circuit and fall into the off-state condition when input drive is lost or disconnected.

In the industry standard SOT23 and SOT26, the DRDC3105 comes as a single or dual die which can replace three to six individual discrete components within a single integrated package, including a Zener across the output. The Zener will clamp at 6.6V to sink inductive currents to ground which will reduce EMI noise in the system. By integrating the Zener, the DRDC3105 eliminates the need for an external free-wheeling diode and allows the driving of inductive loads such as relays, solenoids, incandescent lamps, and small DC motors in:

- Automotive: 5.0 V Driven Relays, Lamp Drivers, Motor Controls
- Telecom equipment: Modems, DSL, Cable (EMTA), Line Cards, IP-PBX, Analog Terminal Adaptors, End User Telecom Equipment
- Desktop Computers, Printers, Photocopiers
- LCD & Plasma TVs, Set Top Boxes
- Consumer Appliances, White Goods, Automated Door Control
- Industrial equipment inc. Process Control, ATE equipment
- Solar inverters

Features and Benefits

- Inductive load driver capable of driving 3 to 6V DC coils
- Optimized to switch inductive loads from supply of 3 to 5V with the capability to drive coils up to 2.5W from a 5V rail
- Fully integrated into a single SOT23 or dual SOT26 package to minimize footprint area and reduce number of components
 Includes Zener across output to reduce EMI noise
- Internal low saturation BJT to reduce power dissipation in driving high currents into the coil
- Output guaranteed to be in off-state condition during no input
- Near Zero quiescent supply current in off-state condition with minimal leakage
- Rugged design and inherently robust by using solid-state BJT technology integrated into a single die
- ESD Protected up to 1kV on Human Body Model (HBM)
- "Lead-Free", RoHS Compliant (Note 1)
- Halogen and Antimony Free "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT23 & SOT26
- Case material: "Green" plastic molding compound (Note 2)
- UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish
- Weight: SOT23 = 0.008 grams (Approximate) SOT26 = 0.018 grams (Approximate)

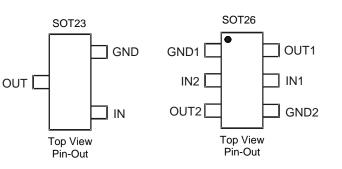




Top View



Top View



Ordering Information (Note 3)

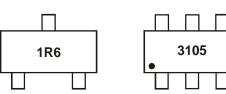
Product	Package	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DRDC3105F-7	SOT23	1R6	7	8	3,000
DRDC3105E6-7	SOT26	3105	7	8	3,000

Notes: 1. No purposefully added lead.

2. Diodes Inc's "Green" policy can be found on our website at http://www.diodes.com

3. For packaging details, go to our website at http://www.diodes.com

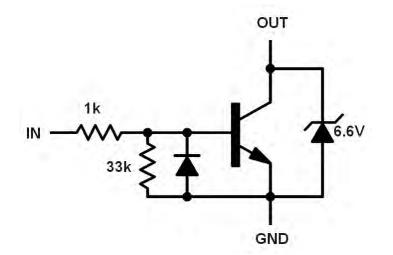
Marking Information



1R6 = SOT23, Product type Marking Code 3105 = SOT26, Product type Marking Code



Internal Device Schematic



Maximum Ratings @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	Limit	Unit	
Supply Voltage	V _{CC}	6.0	V	
Input Voltage (Forward)	V _{in(fwd)}	6.0	V	
Input Voltage (Reverse)		V _{in(rev)}	-0.5	V
Output Sink Continuous Current	(Note 4)	lo	500	mA
Repetitive Pulse Zener Energy Limit (Duty Cycle 0.01%)		E _{zpk}	50	mJ

Thermal Characteristics for DRDC3105F (SOT23) @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit	
Power Dissipation Linear Derating Factor	(Note 4)	P _D	330 2.64	mW mW/°C
Thermal Resistance, Junction to Ambient	(Note 4)	R _{θJA}	379	°C/W
Thermal Resistance, Junction to Case (Note 5)		$R_{\theta JC}$	306	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C	

Notes: 4. For a device surface mounted on 25mm X 25mm X 1.6mm FR4 PCB with high coverage of single sided 1 oz copper, in still air condition; the device is measured when operating in a steady-state condition.

5. Thermal resistance from junction to case.

Thermal Characteristics for DRDC3105E6 (SOT26) @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit		
	(Note 6 & 9)		0.9 7.2	W mW/°C	
Power Dissipation Linear Derating Factor	(Note 6 & 10)	PD	1.1 8.8		
	(Note 7 & 9)		1.7 13.6		
	(Note 6 & 9)		139	°C/W	
Thermal Resistance, Junction to Ambient	(Note 6 & 10)	R _{0JA}	113		
	(Note 7 & 9)	0011	73		
Thermal Resistance, Junction to Leads		R _{θJL}	100	°C/W	
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C	

Notes: 6. For a device surface mounted on 25mm X 25mm FR4 PCB with high coverage of single sided 1 oz copper, in still air conditions

7. For a device surface mounted on FR4 PCB measured at < 5sec

8. Repetitive rating - pulse width limited by maximum junction temperature. Refer to transient thermal impedance graph

9. For a device with one active die

10. For a device with two die running at equal power

11. Thermal resistance from junction to solder-point (at the end of the "OUT" lead).





Electrical Characteristics @T _A = 25°C unless otherwise specified						
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS						·
Output Zener Breakdown Voltage	BV _(out) BV _(-out)	6.2 -	6.6 0.67	7.0 -	V	@ IT = 10 mA Pulse
Output Leakage Current @ 0 Input Voltage	I _{OO}	-	-	0.1 30	μA	V _O = 5.5 V, T _A = 25°C V _O = 5.5 V, T _A = 85°C
"ON" state input voltage (Note 12)	V _{in(on)}		0.99	1.5	V	I _O =100 mA, V _O = 150mV
"OFF" State Input Voltage (Note 13)	V _{in(off)}	400	540	-	mV	I _O =100 μA, V _O = 4.9V
ON CHARACTERISTICS						·
Input Bias Current (H _{FE} Limited)	l _{in}	-	0.7	1.6	mA	$I_0 = 250 \text{ mA}, V_0 = 0.25 \text{ V}$
Output Saturation Voltage	V _{O(sat)}	-	125	160	mV	l _O = 250 mA, l _{in} = 1.5 mA
Output Sink Current – Continuous	I _{O(on)}	250	430	-	mA	V _{CE} = 0.25 V, I _{in} = 1.5 mA
SWITCHING CHARACTERISTICS (Refer to Figure 7	1)					
Propagation Delay Times:	-					
High to Low Propagation Delay; (5.0V 74HC04) Low to High Propagation Delay; (5.0V 74HC04)	t _{PHL} t _{PLH}	-	20.4 1.43	-	ns µs	-
High to Low Propagation Delay; 13 (3.0V 74HC04) Low to High Propagation Delay; 13 (3.0V 74HC04)	t _{PHL} t _{PLH}	-	32.2 760	-	ns ns	-
High to Low Propagation Delay; 14 (5.0V 74LS04) Low to High Propagation Delay; 14 (5.0V 74LS04)	t _{PHL} t _{PLH}	-	25.3 2.57	-	ns µs	-
Transition Times:	-					
Fall Time; (5.0V 74HC04) Rise Time; (5.0V 74HC04)	t _f tr	-	12.5 411	-	ns ns	-
Fall Time; 13 (3.0V 74HC04) Rise Time; 13 (3.0V 74HC04)	t _f t _r	-	21.1 220	-	ns ns	-
Fall Time; 14 (5.0V 74LS04) Rise Time; 14 (5.0V 74LS04)	t _f t _r	-	15.1 849	-	ns ns	-

12. The device is guaranteed to be in "ON" state with $V_{in(on)}$ above 1.5V 13. The device is guaranteed to be in "OFF" state with $V_{in(off)}$ below 400mV Notes:

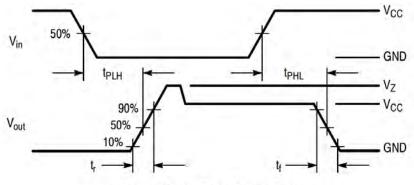


Figure 1. Switching Waveforms



Typical Electrical Characteristics

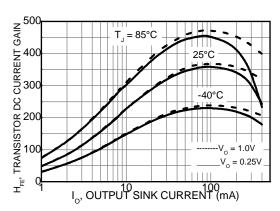


Figure 2. Transistor DC Current Gain

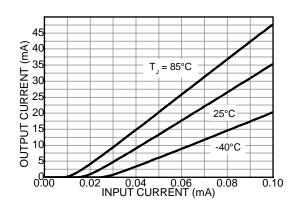
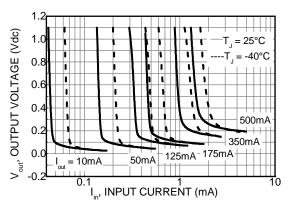
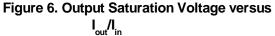


Figure 4. Threshold Effects





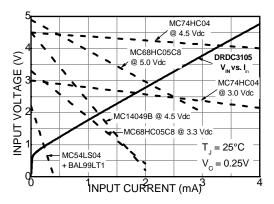


Figure 3. Input V-I Requirement Compared to Possible Source Logic Outputs

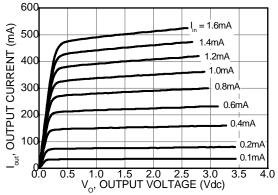


Figure 5. Transistor Output V-I Characteristic

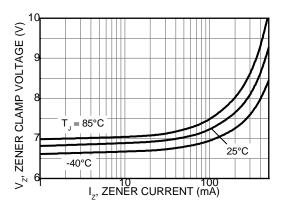
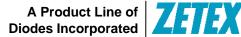


Figure 7. Zener Clamp Voltage versus Zener Current





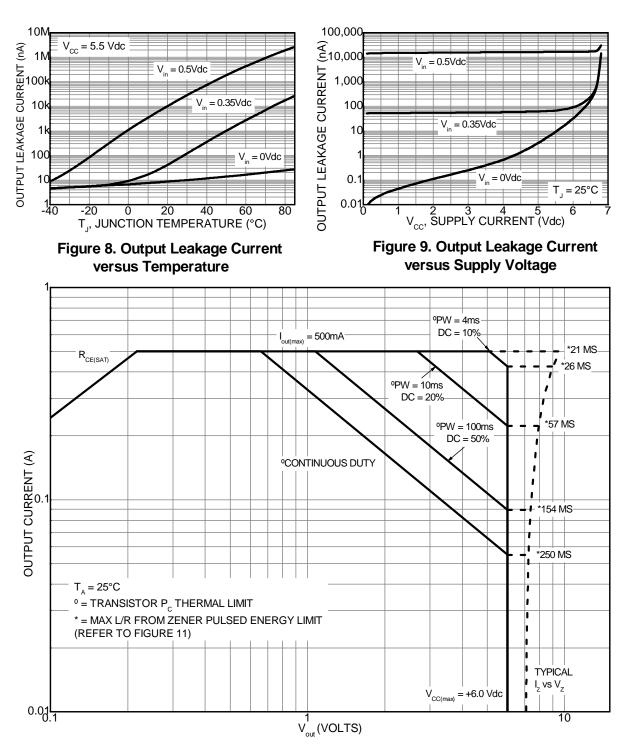
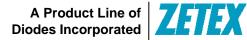


Figure 10. Safe Operating Area





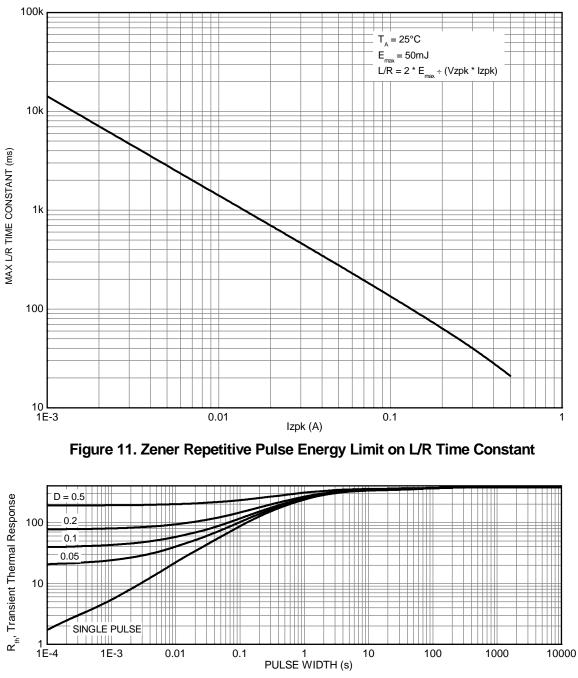
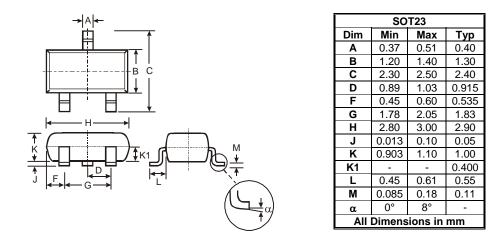


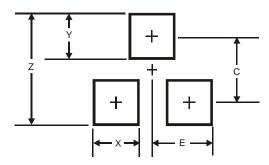
Figure 12. Transient Thermal Response



Package Outline Dimensions for SOT23



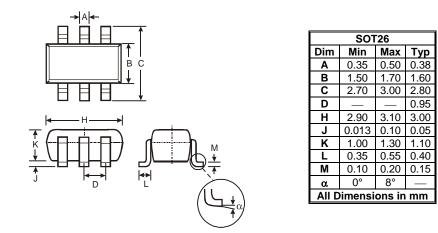
Suggested Pad Layout for SOT23



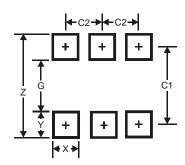
Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35



Package Outline Dimensions SOT26



Suggested Pad Layout for SOT26



Dimensions	Value (in mm)
Z	3.20
G	1.60
Х	0.55
Y	0.80
C1	2.40
C2	0.95



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