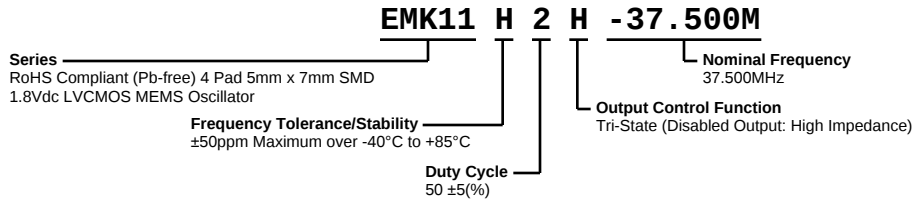


EMK11H2H-37.500M



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ELECTRICAL SPECIFICATIONS

Nominal Frequency	37.500MHz
Frequency Tolerance/Stability	±50ppm Maximum over -40°C to +85°C (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25°C, 260°C Reflow, Shock, and Vibration)
Aging at 25°C	±1ppm Maximum First Year
Operating Temperature Range	-40°C to +85°C
Supply Voltage	1.8Vdc ±5%
Input Current	18mA Maximum
Output Voltage Logic High (Voh)	90% of Vdd Minimum (IOH=-8mA)
Output Voltage Logic Low (Vol)	10% of Vdd Maximum (IOL=+8mA)
Rise/Fall Time	2nSec Maximum (Measured from 20% to 80% of waveform)
Duty Cycle	50 ±5(%) (Measured at 50% of waveform)
Load Drive Capability	15pF Maximum
Output Logic Type	CMOS
Output Control Function	Tri-State (Disabled Output: High Impedance)
Output Control Input Voltage	+0.7Vdd Minimum or No Connect to Enable Output, +0.3Vdd Maximum to Disable Output
Peak to Peak Jitter (tPK)	250pSec Maximum, 100pSec Typical
Start Up Time	50mSec Maximum
Storage Temperature Range	-55°C to +125°C

ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

ESD Susceptibility	MIL-STD-883, Method 3015, Class 2, HBM 2000V
Flammability	UL94-V0
Mechanical Shock	MIL-STD-883, Method 2002, Condition G, 30,000G
Moisture Resistance	MIL-STD-883, Method 1004
Moisture Sensitivity Level	J-STD-020, MSL 1
Resistance to Soldering Heat	MIL-STD-202, Method 210, Condition K
Resistance to Solvents	MIL-STD-202, Method 215
Solderability	MIL-STD-883, Method 2003 (Four I/O Pads on bottom of package only)
Temperature Cycling	MIL-STD-883, Method 1010, Condition B
Thermal Shock	MIL-STD-883, Method 1011, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A, 20G

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Technical drawing of a rectangular plate with the following dimensions and features:

- Overall width: 5.00 ± 0.15
- Overall height: 7.00 ± 0.15
- Thickness: 0.85 ± 0.15
- Top edge chamfer: 0.08 MAX
- Bottom edge chamfer: 0.08 MAX
- Four corner holes (1, 2, 3, 4) with radius $R0.70 \pm 0.10$
- Central hole (A) with diameter $\varnothing 0.25 \pm 0.10$
- Distance from bottom edge to center of hole A: 5.08 ± 0.10
- Distance from left edge to center of hole A: 2.60 ± 0.15
- Distance from right edge to center of hole A: 2.1
- Distance from top edge to center of hole A: 1.70
- Distance from bottom edge to center of hole A: $1.40 \pm 0.10 \text{ (x4)}$

PIN	CONNECTION
1	Tri-State (High Impedance)
2	Ground
3	Output
4	Supply Voltage

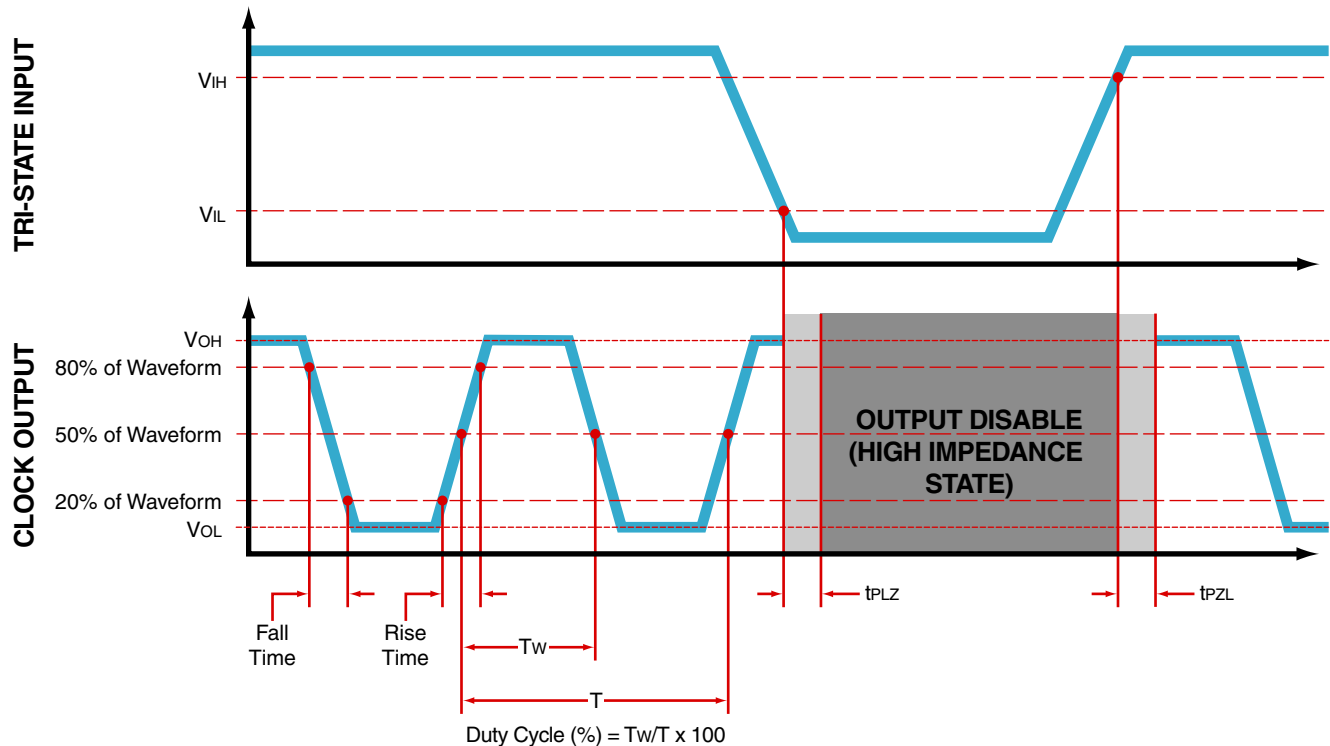
LINE	MARKING
1	XXXX or XXXXX XXXX or XXXXX=Ecliptek Manufacturing Lot Code

Figure 1: Dimensions of the test board. The diagram shows a 2x2 grid of blue square components. Dimensions are given in inches. Top horizontal spacing: 2.0 (X4). Top vertical spacing: 2.2 (X4). Left vertical spacing: 2.88. Bottom horizontal spacing: 1.81. A label 'Solder Land (X4)' points to the top-right component.

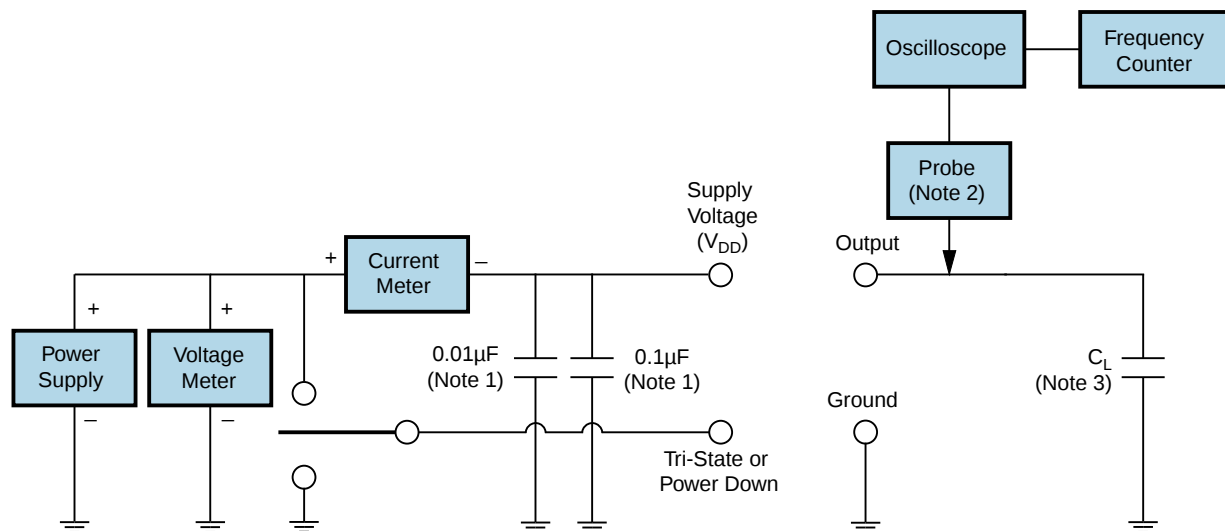
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OUTPUT WAVEFORM & TIMING DIAGRAM



Test Circuit for CMOS Output



Note 1: An external 0.1 μF low frequency tantalum bypass capacitor in parallel with a 0.01 μF high frequency ceramic bypass capacitor close to the package ground and V_{DD} pin is required.

Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>300MHz) passive probe is recommended.

Note 3: Capacitance value C_L includes sum of all probe and fixture capacitance.

Recommended Solder Reflow Methods



High Temperature Infrared/Convection

$T_s \text{ MAX to } T_L$ (Ramp-up Rate)	3°C/second Maximum
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Preheat

- Temperature Minimum ($T_s \text{ MIN}$)	150°C
- Temperature Typical ($T_s \text{ TYP}$)	175°C
- Temperature Maximum ($T_s \text{ MAX}$)	200°C
- Time ($t_s \text{ MIN}$)	60 - 180 Seconds

Ramp-up Rate (T_L to T_p)	3°C/second Maximum
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Time Maintained Above:

- Temperature (T_L)	217°C
- Time (t_L)	60 - 150 Seconds

Peak Temperature (T_p)	260°C Maximum for 10 Seconds Maximum
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Target Peak Temperature ($T_p \text{ Target}$)	250°C +0/-5°C
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Time within 5°C of actual peak (t_p)	20 - 40 seconds
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Ramp-down Rate	6°C/second Maximum
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Time 25°C to Peak Temperature (t)	8 minutes Maximum
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Moisture Sensitivity Level	Level 1
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Recommended Solder Reflow Methods



Low Temperature Infrared/Convection 240°C

T_S MAX to T_L (Ramp-up Rate) 5°C/second Maximum

Preheat

- Temperature Minimum (T_S MIN) N/A
- Temperature Typical (T_S TYP) 150°C
- Temperature Maximum (T_S MAX) N/A
- Time (t_s MIN) 60 - 120 Seconds

Ramp-up Rate (T_L to T_P) 5°C/second Maximum

Time Maintained Above:

- Temperature (T_L) 150°C
- Time (t_L) 200 Seconds Maximum

Peak Temperature (T_P) 240°C Maximum

Target Peak Temperature (T_P Target) 240°C Maximum 1 Time / 230°C Maximum 2 Times

Time within 5°C of actual peak (t_p) 10 seconds Maximum 2 Times / 80 seconds Maximum 1 Time

Ramp-down Rate 5°C/second Maximum

Time 25°C to Peak Temperature (t) N/A

Moisture Sensitivity Level Level 1

Low Temperature Manual Soldering

185°C Maximum for 10 seconds Maximum, 2 times Maximum.

High Temperature Manual Soldering

260°C Maximum for 5 seconds Maximum, 2 times Maximum.