

PRODUCT SUMMARY				
V _{DS} (V)	r _{DS(on)} (Ω)	I _D (A) ^a		
20	0.0033 @ V _{GS} = 10 V	39		
	0.0053 @ V _{GS} = 4.5 V	31		



FEATURES

- TrenchFET[®] Power MOSFET
- 175°C Junction Temperature
- PWM Optimized for High-Efficiency
- 100% R_g Tested

APPLICATIONS

- Synchronous Buck Converter - Low-Side
 - Secondary Synchronous Rectifier

Ordering Information: SUD70N02-03P

N-Channel MOSFET

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ABSOLUTE MAXIMUM RATINGS (T _A = 25°C UNLESS OTHERWISE NOTED)						
Parameter		Symbol	Limit	Unit		
Drain-Source Voltage		V _{DS}	20			
Gate-Source Voltage		V _{GS}	±20	V		
	$T_A = 25^{\circ}C$		39 ^a			
Continuous Drain Current ^a	$T_{C}=25^{\circ}C$		70 ^b			
Pulsed Drain Current		I _{DM}	100	A		
Continuous Source Current (Diode Conduction) ^a		۱ _S	37			
	$T_A = 25^{\circ}C$	_	8.3 ^a			
Maximum Power Dissipation	$T_{\rm C} = 25^{\circ}{\rm C}$	P _D	100	w		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 175	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
	$t \le 10 \text{ sec}$	R _{thJA}	15	18	°C/W	
Maximum Junction-to-Ambient ^a	Steady State		40	50		
Maximum Junction-to-Case		R _{thJC}	1.2	1.5		

Notes

a. Surface Mounted on FR4 Board, $t \le 10$ sec.

b. Limited by package



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Parameter	Symbol	Test Condition	Min	Typ ^a	Max	Unit	
Static	- 1 1 1					1	
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0 V, I_D = 250 μ A	20			v	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	0.8		3.0		
Gate-Body Leakage	IGSS	V _{DS} = 0 V, V _{GS} = ±20 V			±100	nA	
	I _{DSS}	$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ	
Zero Gate Voltage Drain Current		V_{DS} = 16 V, V_{GS} = 0 V, T_{J} = 125 °C			50		
On-State Drain Current ^b	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 10 V	V _{DS} = 5 V, V _{GS} = 10 V 50			Α	
		V _{GS} = 10 V, I _D = 20 A		0.0026	0.0033	<u> </u>	
Drain-Source On-State Resistance ^b	r _{DS(on)}	V_{GS} = 10 V, I_{D} = 20 A, T_{J} = 125 $^{\circ}$ C			0.0047	Ω	
		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 20 \text{ A}$		0.0042	0.0053	1	
Forward Transconductance ^b	9 _{fs}	V _{DS} = 15 V, I _D = 20 A	15			S	
Dynamic ^a	· ·					•	
Input Capacitance	C _{iss}			5100		pF	
Output Capacitance	C _{oss}	V_{GS} = 0 V, V_{DS} = 10 V, f = 1 MHz		1650			
Reverse Transfer Capacitance	C _{rss}			800			
Gate Resistance	Rg	f = 1.0 MHz		1.1	1.8	Ω	
Total Gate Charge ^c	Qg			40	60	nC	
Gate-Source Charge ^c	Q _{gs}	V_{DS} = 10 V, V_{GS} = 4.5 V, I_{D} = 50 A		14			
Gate-Drain Charge ^c	Q _{gd}			13			
Turn-On Delay Time ^c	t _{d(on)}			15	25	- ns	
Rise Time ^c	tr	$V_{DD} = 10 \text{ V}, \text{ R}_{\text{I}} = 0.2 \Omega$	-	11	20		
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong 50$ Å, $V_{GEN} = 10$ V, $R_G = 2.5 \Omega$	-	45	70		
Fall Time ^c	t _f		-	15	25		
Source-Drain Diode Ratings ar	d Characteristi	c (T _C = 25°C)				•	
Pulsed Current	I _{SM}				100	Α	
Diode Forward Voltage ^b	V _{SD}	$I_{F} = 50 \text{ A}, V_{GS} = 0 \text{ V}$		1.2	1.5	V	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 50 A, di/dt = 100 A/μs		45	90	ns	

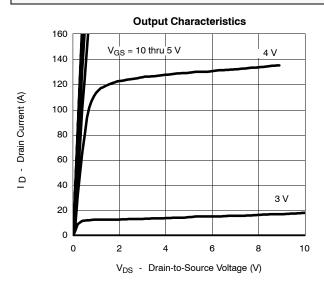
Notes

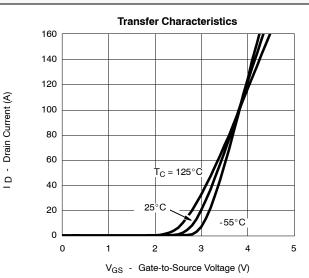
a. Guaranteed by design, not subject to production testing.

b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2%.

c. Independent of operating temperature.

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

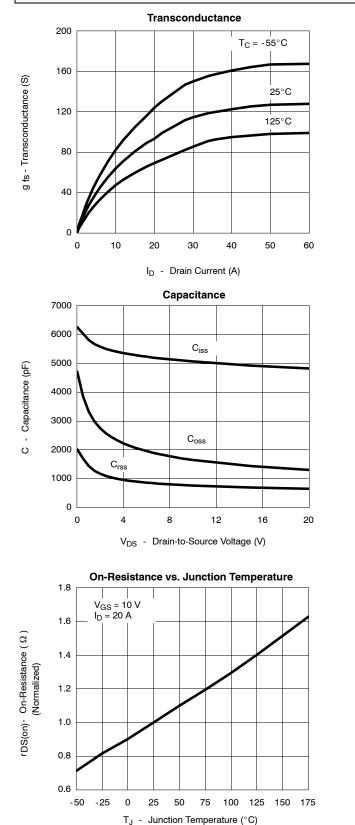


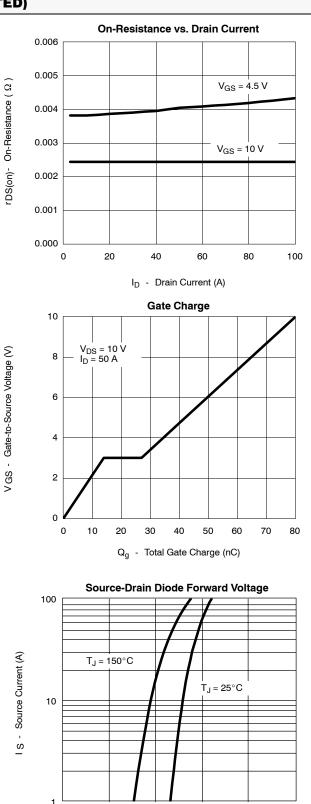




SUD70N02-03P N-Channel 20 V (D-S) 175 °C MOSFET

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)





- Source-to-Drain Voltage (V)

0.9

1.2

1.5

0.6

0

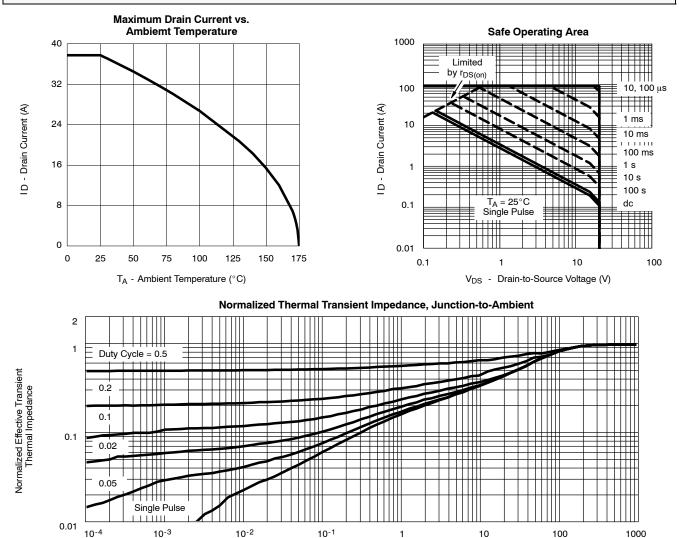
0.3

 V_{SD}



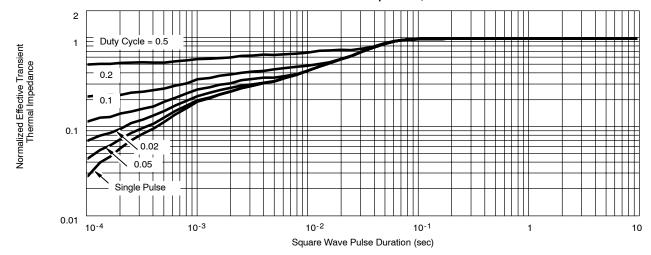
SUD70N02-03P N-Channel 20 V (D-S) 175 °C MOSFET

THERMAL RATINGS



Normalized Thermal Transient Impedance, Junction-to-Case

Square Wave Pulse Duration (sec)





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