Silicon NPN Epitaxial Twin Transistor

HITACHI

ADE-208-1448(Z)

Preliminary Rev. 0 Aug. 2001

Features

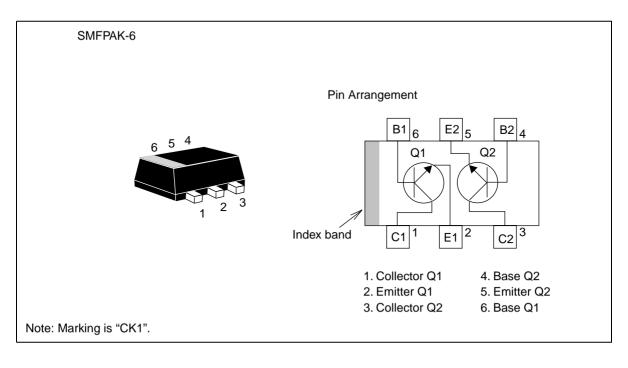
• Include 2 transistors in a small size SMD package: SMFPAK-6 (6 Leads: 1.5 x 1.1 x 0.55 mm)

Q1: Q2:

Equivalent Equivalent Buffer Transistor OSC Transistor

2SC5700 2SC5700

Outline





Absolute Maximum Ratings

 $(Ta = 25 \, ^{\circ}C)$

		Ratings		
Item	Symbol	Q1 and Q2	Unit	
Collector to base voltage	$V_{\scriptscriptstyleCBO}$	15	V	
Collector to emitter voltage	V _{CEO}	4	V	
Emitter to base voltage	V _{EBO}	1.5	V	
Collector current	I _c	50	mA	
Collector power dissipation	P _c	Total 220*	mW	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

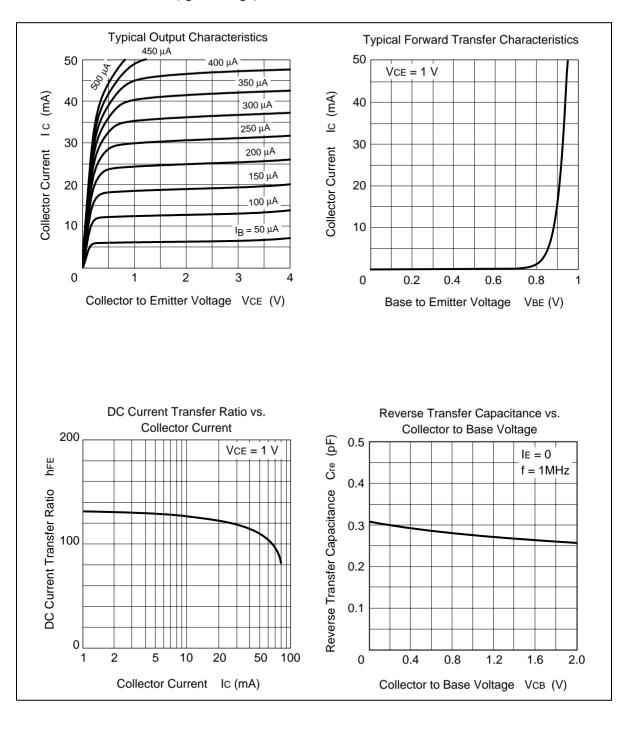
^{*}Value on PCB. (FR-4 (13 x 13 x 0.635 mm))

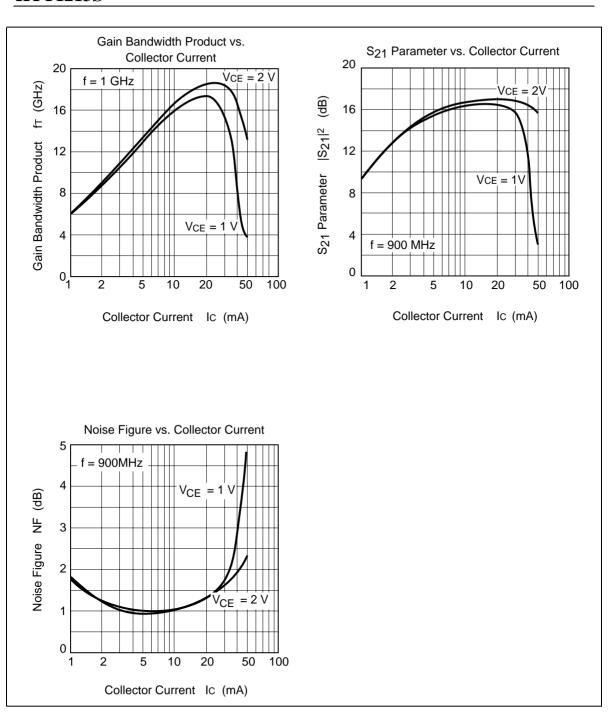
Electrical Characteristics (Q1 and Q2)

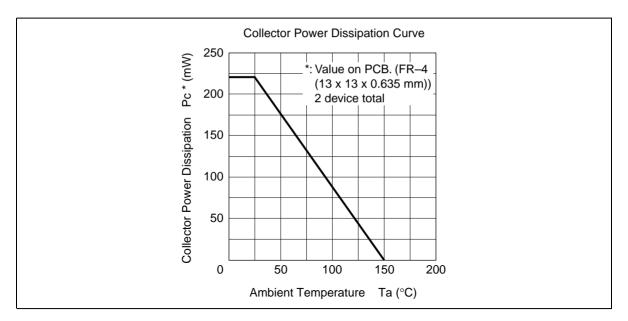
 $(Ta = 25 \, ^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test conditions
Collector to base breakdown voltage	$V_{(BR)CBO}$	15	_	_	V	$I_{c} = 10 \ \mu A, \ I_{e} = 0$
Collector cutoff current	I _{CBO}	_		0.1	mA	$V_{CB} = 15 \text{ V}, I_{E} = 0$
Collector cutoff current	I _{CEO}	_	_	1	mA	$V_{CE} = 4 \text{ V}, R_{BE} = \text{infinite}$
Emitter cutoff current	I _{EBO}	_	_	0.2	mA	$V_{EB} = 0.8 \text{ V}, I_{C} = 0$
DC current transfer ratio	h _{FE}	100	130	170		$V_{CE} = 1 \text{ V}, I_{C} = 5 \text{ mA}$
Reverse transfer capacitance	C_{re}	_	0.30	0.45	pF	V _{CB} = 1 V, f = 1 MHz Emitter ground
Gain bandwidth product	f _T	10	13	_	GHz	$V_{CE} = 1 \text{ V}, I_{C} = 5 \text{ mA}, f = 1 \text{ GHz}$
Forward transfer coefficient	S ₂₁ ²	13	16	_	dB	$V_{CE} = 1 \text{ V}, I_{C} = 5 \text{ mA},$
Noise figure	NF	_	1.0	2.0	dB	Γ f = 900 MHz, $\Gamma_{\rm S} = \Gamma_{\rm L} = 50 \Omega$

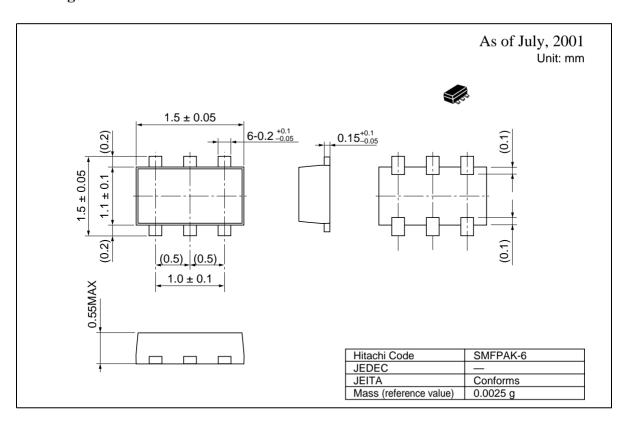
Main Characteristics (Q1 and Q2)







Package Dimensions



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