

# SANYO Semiconductors DATA SHEET

An ON Semiconductor Company

P-Channel Silicon MOSFET

# BMS3004 — General-Purpose Switching Device Applications

#### **Features**

- ON-resistance RDS(on)1=6.5m $\Omega$  (typ.)
- Input capacitance Ciss=13400pF (typ.)
- · 4V drive

## **Specifications**

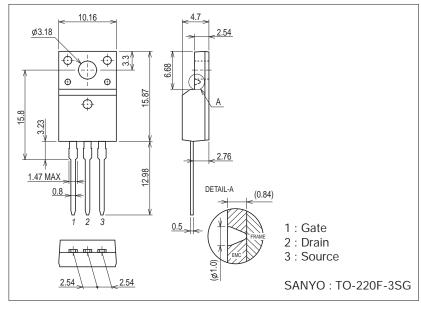
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		-75	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		-68	А
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	-272	А
Allowable Power Dissipation	Do		2.0	W
	PD	Tc=25°C	40	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *1	EAS		380	mJ
Avalanche Current *2	IAV		-54	А

Note :\*1  $V_{DD}$ =-48V, L=100 $\mu H$ ,  $I_{AV}$ =-54A (Fig.1)

#### **Package Dimensions**

unit : mm (typ) 7529-001



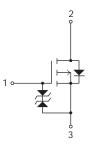
### **Product & Package Information**

Package : TO-220F-3SGJEITA, JEDEC : SC-67

• Minimum Packing Quantity : 50 pcs./magazine

# Marking Electrical Connection





<sup>\*2</sup> L≤100µH, Single pulse

#### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=-1mA, VGS=0V	-75			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =-75V, V <sub>GS</sub> =0V			-10	μΑ
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±16V, V <sub>DS</sub> =0V			±10	μΑ
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1mA	-1.2		-2.6	V
Forward Transfer Admittance	yfs	VDS=-10V, ID=-34A		120		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)1	I <sub>D</sub> =-34A, V <sub>G</sub> S=-10V		6.5	8.5	mΩ
	R <sub>DS</sub> (on)2	I <sub>D</sub> =-34A, V <sub>G</sub> S=-4V		8.3	11.4	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> =-20V, f=1MHz		13400		pF
Output Capacitance	Coss			1000		pF
Reverse Transfer Capacitance	Crss			740		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See Fig.2		70		ns
Rise Time	t <sub>r</sub>			245		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)			1400		ns
Fall Time	tf			650		ns
Total Gate Charge	Qg	V <sub>DS</sub> =-48V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-68A		300		nC
Gate-to-Source Charge	Qgs			30		nC
Gate-to-Drain "Miller" Charge	Qgd			70		nC
Diode Forward Voltage	VSD	IS=-68A, VGS=0V		-0.9	-1.5	V
Reverse Recovery Time	t <sub>rr</sub>	See Fig.3		146		ns
Reverse Recovery Charge	Q <sub>rr</sub>	IS=-68A, VGS=0V, di/dt=-100A/μs		470		nC

Fig.1 Avalanche Resistance Test Circuit

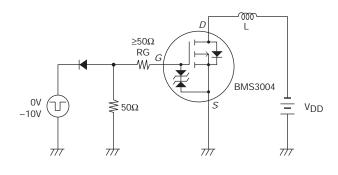


Fig.2 Switching Time Test Circuit

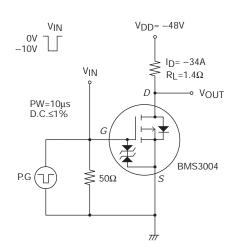
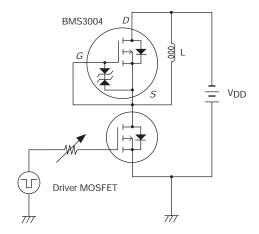
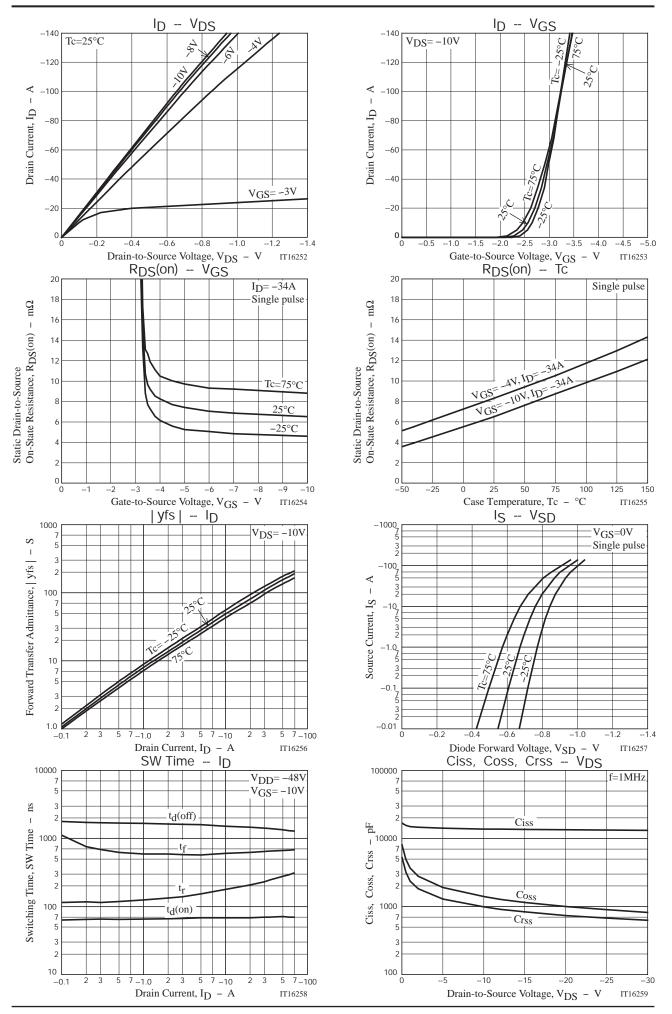
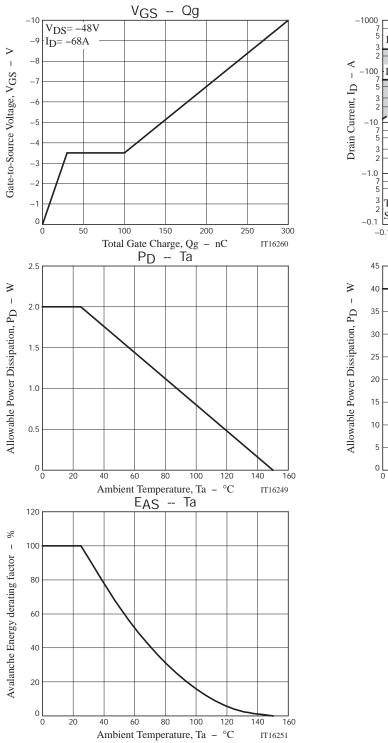
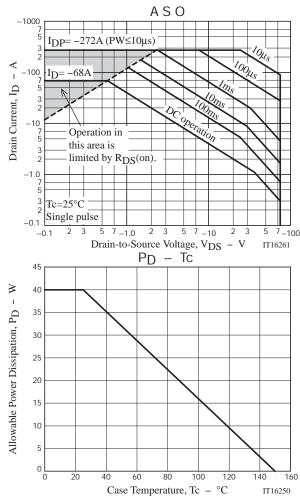


Fig.3 Reverse Recovery Time Test Circuit









Note on usage: Since the BMS3004 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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