TOSHIBA SOLID STATE I/O INTERFACE MODULE

TF1107

DC INPUT MODULE

TOSHIBA TF1107 is DC Line Voltage Input I/O Interface Module and it includes the optical isolator.

Using this Module, you can design high reliability and compact system.

• Recommended Input Voltage: VIN=12~24V

• Input Impedance : $Z_{IN} = 3.1 k\Omega$

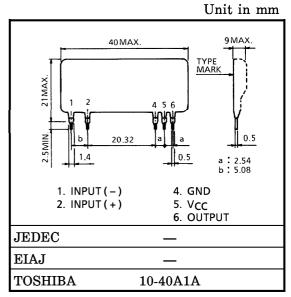
• 1500V AC Optical Isolation

• Wide Supply Voltage : V_{CC}=5~18V

Including Delay Time Circuit

Output is Compatible with TTL and CMOS Logic

• Small Size and Light Weight



Weight: 7g

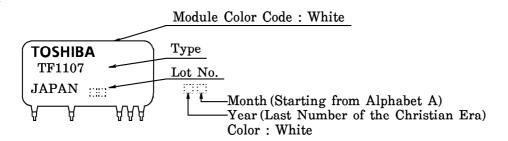
MAXIMUM RATINGS (Ta = 25°C) INPUT (DC LINE VOLTAGE)

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Input Voltage (DC)	v_{IN}	30	V	
Input Current (DC)	$I_{ ext{IN}}$	10	mA	
Operating Frequency Range	f	65	Hz	

OUTPUT (LOGIC CONTROL)

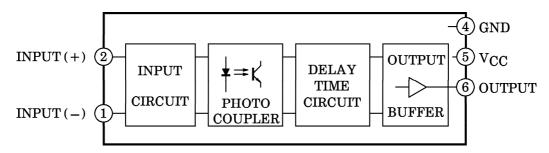
Logic Supply Voltage	V_{CC}	20	V
Output Voltage	V _{OUT}	$-0.5 \sim V_{\text{CC}} + 0.5$	V
Output Current	I _{OUT}	6	mA
Isolation Voltage (Input-Output) (AC)	BVS/AC	1500 (1min)	V
Operating Temperature Range	T_{opr}	-20~80	$^{\circ}\mathrm{C}$
Storage Temperature Range	$T_{ m stg}$	-20~80	$^{\circ}\mathrm{C}$
Lead Soldering Temperature (10s)	T _{sol}	260	$^{\circ}\mathrm{C}$

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BLOCK DIAGRAM



ELECTRICAL CHARACTERISTICS (Ta = 25°C, V_{CC} = 5V) INPUT (DC LINE VOLTAGE)

CHARACTER	RISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Voltage	"H" Level	$ m v_{ILH}$	$ I_{OUT} < 1\mu A$, $V_{OUT} > 4.5V$		7.8	12	V
	"L" Level	$ m v_{IHL}$	$ I_{OUT} < 1\mu A$, $V_{OUT} < 0.5V$	5	7.5	_	
Input Current	"H" Level	${ m I_{ILH}}$	$ I_{ ext{OUT}} < 1\mu A$, $V_{ ext{OUT}} > 4.5 V$	_	1.4	_	mA
	"L" Level	${ m I}_{ m IHL}$	$ I_{OUT} < 1\mu A$, $V_{OUT} < 0.5V$	_	1.3	_	
Input Impedance		$ m z_{IN}$	$V_{IN} = 24V$	_	3.1	_	kΩ

OUTPUT (LOGIC CONTROL)

Output Voltage	"H" Level	v_{OH}	$I_{OUT} = -10 \mu A, V_{IN} = 24 V$	4.5	4.9		V
	"L" Level	v_{OL}	I_{OUT} =2.5mA, V_{IN} =0V		0.3	0.5	
Output Current (sink)		$I_{ m OUT}$	$V_{OL}=1.5V, V_{IN}=0V$	6	16	_	mA
Supply Current	"H" Level	$_{ m ICCH}$	$ I_{OUT} < 1\mu A, V_{IN} = 24V$	_	1.0	5	mA
	"L" Level	$_{ m ICCL}$	$ I_{OUT} < 1\mu A, V_{IN} = 0V$	_	1.4	6	
Propagation	"H" Level	$t_{ m pLH}$	$V_{IN} = 0 \rightarrow 24V$	_	4.2	8	
Delay Time	"L" Level	t_{pHL}	$V_{IN} = 24 \rightarrow 0V$	_	5.5	10	ms
Isolation Resistan	ce	RS	V=1kV, R.H=40~60%	_	1010	_	Ω

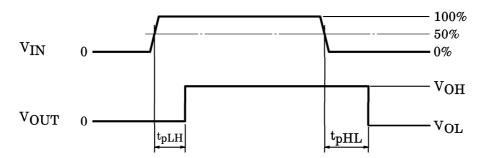


Fig.1 SWITCHING TIME TEST CONDITION

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