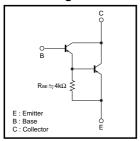
High-gain Amplifier Transistor (32V, 0.3A) 2SD1383K / 2SC1645S

Features

- 1) Darlington connection for high DC current gain.
- 2) Built-in $4k\Omega$ resistor between base and emitter.
- 3) Complements the 2SD852K / 2SA830S.

●Circuit diagram

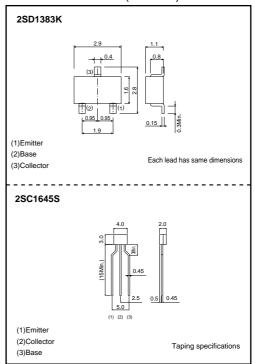


Packaging specifications

Туре	2SD1383K	2SC1645S
Package	SMT3	SPT
hFE	В	В
Marking	W*	-
Code	T146	TP
Basic ordering unit (pieces)	3000	5000

^{*} Denotes hre

●External dimensions (Unit : mm)



● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Collector-base voltage	Vсво	40	V	
Collector-emitter voltage	Vceo	32	V *1	
Emitter-base voltage	Vево	6	V	
Collector current	lc	0.3	A (DC)	
		1.5	A (Pulse) *2	
Collector power dissipation	Pc	0.2	W	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

^{*1} R_{BE}=0Ω *2 Single pulse Pw=10ms

Transistors

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	40	_	-	V	Ic=100μA
Collector-emitter breakdown voltage	BVceo	32	_	-	V	Ic= -1 mA , R _{BE} = 0 Ω
Emitter-base breakdown voltage	ВVево	6	_	_	V	Iε=100μA
Collector cutoff current	Ісво	_	_	1	μΑ	Vcb=24V
Emitter cutoff current	Іево	_	_	1	μΑ	V _{EB} =4.5V
DC current transfer ratio	hfe	5000	-	-	_	Vce=5V, Ic=0.1A
Collector-emitter saturation voltage	VCE(sat)	_	_	1.5	V	Ic=200mA, I _B =0.4mA *1
Transition frequency	f⊤	_	250	_	MHz	Vc=5V, I=-10mA, f=100MHz *2
Output capacitance	Cob	_	5	_	pF	Vcb=10V, Ie=0A, f=1MHz

^{*1} Measured using pulse current. *2 Transition frequency of the device.

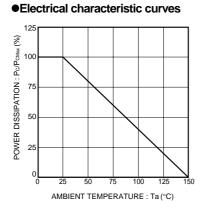


Fig.1 Power dissipation curves

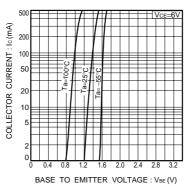


Fig.2 Ground emitter propagation characteristisc

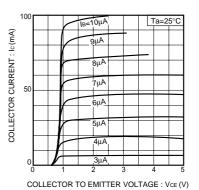


Fig.3 Ground emitter output characteristics

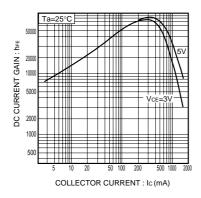


Fig.4 DC current gain vs. collector current (I)

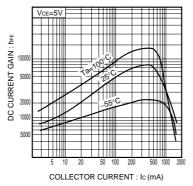


Fig.5 DC current gain vs. collector current (II)

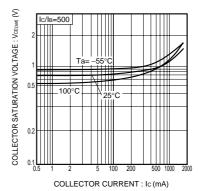


Fig.6 Collector-emitter saturation voltage vs. collector current

2SD1383K / 2SC1645S

Transistors

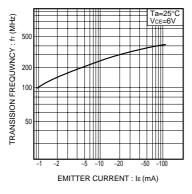


Fig.7 Gain bandwidth product vs. emitter current

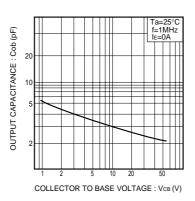


Fig.8 Collector output capacitance vs. collector-base voltage

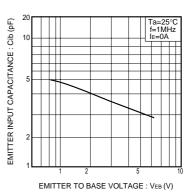


Fig.9 Emitter input capacitance vs. emitter-base voltage

Appendix

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