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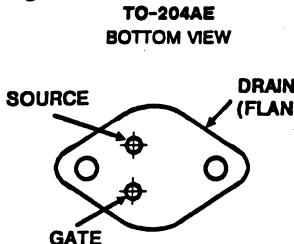
# 2N6764

## N-Channel Enhancement-Mode Power MOS Field-Effect Transistors

### Absolute Maximum Ratings ( $T_C = +25^\circ\text{C}$ ) Unless Otherwise Specified

	2N6764	UNITS	Package
Drain-Source Voltage .....	$V_{DS}$	V	TO-204AE
Drain-Gate Voltage ( $R_{GS} = 20\text{k}\Omega$ ) .....	$V_{DGR}$	V	BOTTOM VIEW
Continuous Drain Current			
$T_C = +25^\circ\text{C}$ .....	$I_D$	A	
$T_C = +100^\circ\text{C}$ .....	$I_D$	A	
Pulsed Drain Current .....	$I_{DM}$	A	
Gate-Source Voltage .....	$V_{GS}$	V	
Maximum Power Dissipation			
$T_C = +25^\circ\text{C}$ (See Figure 11) .....	$P_D$	W	
$T_C = +100^\circ\text{C}$ (See Figure 11) .....	$P_D$	W	
Linear Derating Factor (See Figure 11) .....		$W/\text{^\circ C}$	
Inductive Current, Clamped .....	$I_{LM}$	A	
(See Figures 1 and 2, $L = 100\mu\text{H}$ )			
Operating and Storage Junction Temperature Range.....	$T_J, T_{STG}$	$^\circ\text{C}$	
Maximum Lead Temperature for Soldering .....	$T_L$	$^\circ\text{C}$	
(0.063" (1.6mm) from case for 10s)			
-55 to +150° 300°			

\*JEDEC registered values



### ELECTRICAL CHARACTERISTICS @ $T_c = 25^\circ\text{C}$ (Unless Otherwise Specified)

Parameter	Type	Min.	Typ.	Max.	Units	Test Conditions
$V_{DSS}$ Drain - Source Breakdown Voltage						$V_{GS} = 0$ $I_D = 1.0 \text{ mA}$
	2N6764	100	-	-	V	
$V_{GS(th)}$ Gate Threshold Voltage	ALL	2.0°	-	4.0°	V	$V_{DS} = V_{GS}, I_D = 1 \text{ mA}$
$I_{GSSF}$ Gate - Body Leakage Forward	ALL	-	-	100°	nA	$V_{GS} = 20\text{V}$
$I_{GSR}$ Gate - Body Leakage Reverse	ALL	-	-	100°	nA	$V_{GS} = -20\text{V}$
$I_{DS}$ Zero Gate Voltage Drain Current	ALL	-	0.1	1.0°	mA	$V_{DS} = \text{Max. Rating}, V_{GS} = 0$
		-	0.2	4.0°	mA	$V_{DS} = \text{Max. Rating}, V_{GS} = 0, T_C = 125^\circ\text{C}$
$V_{DS(on)}$ Static Drain-Source On-State Voltage ①	2N6764	-	-	2.0°	V	$V_{GS} = 10\text{V}, I_D = 38\text{A}$
$R_{DS(on)}$ Static Drain-Source On-State Resistance ①	2N6764	-	0.045	0.055°	Ω	$V_{GS} = 10\text{V}, I_D = 24\text{A}$
$R_{DS(on)}$ Static Drain-Source On-State Resistance ①	2N6764	-	-	0.094°	Ω	$V_{GS} = 10\text{V}, I_D = 24\text{A}, T_C = 125^\circ\text{C}$
$G_f$ Forward Transconductance ①	ALL	9.0°	12.5	27°	S (U)	$V_{DS} = 15\text{V}, I_D = 24\text{A}$
$C_{iss}$ Input Capacitance	ALL	1000°	2000	3000°	pF	$V_{GS} = 0, V_{DS} = 25\text{V}, f = 1.0 \text{ MHz}$
$C_{oss}$ Output Capacitance	ALL	500°	1000	1500°	pF	See Fig. 10
$C_{trs}$ Reverse Transfer Capacitance	ALL	150°	350	500°	pF	
$t_d(\text{on})$ Turn-On Delay Time	ALL	-	-	35°	ns	$V_{DD} \geq 24\text{V}, I_D = 24\text{A}, Z_o = 4.7\Omega$
$t_r$ Rise Time	ALL	-	-	100°	ns	(See Figs. 13 and 14)
$t_d(\text{off})$ Turn-Off Delay Time	ALL	-	-	125°	ns	(MOSFET switching times are essentially independent of operating temperature.)
$t_f$ Fall Time	ALL	-	-	100°	ns	

### THERMAL RESISTANCE

$R_{thJC}$ Junction-to-Case	ALL	-	-	0.83°	°C/W	
$R_{thCS}$ Case-to-Sink	ALL	-	0.1	-	°C/W	Mounting surface flat, smooth, and greased.
$R_{thJA}$ Junction-to-Ambient	ALL	-	-	30	°C/W	Free Air Operation

### BODY-DRAIN DIODE RATINGS AND CHARACTERISTICS

$I_S$ Continuous Source Current (Body Diode)	2N6764	-	-	38°	A	Modified MOSFET symbol showing the integral reverse P-N junction rectifier.
$I_{SM}$ Pulsed Source Current (Body Diode)	2N6764	-	-	70	A	
$V_{SD}$ Diode Forward Voltage ①	2N6764	0.90°	-	1.8°	V	$T_C = 25^\circ\text{C}, I_S = 31\text{A}, V_{GS} = 0$
	2N6764	-	-	-	V	$T_C = 25^\circ\text{C}, I_S = 38\text{A}, V_{GS} = 0$
$t_{rr}$ Reverse Recovery Time	ALL	-	500	-	ns	$T_J = 150^\circ\text{C}, I_F = I_{SM}, dI_F/dt = 100 \text{ A}/\mu\text{s}$
$Q_{RR}$ Reverse Recovered Charge	ALL	-	10	-	μC	$T_J = 150^\circ\text{C}, I_F = I_{SM}, dI_F/dt = 100 \text{ A}/\mu\text{s}$

\*JEDEC registered values. ① Pulse Test: Pulse Width  $\leq 300 \mu\text{sec}$ , Duty Cycle  $\leq 2\%$