

10BASE-T NETWORK COMPONENTS

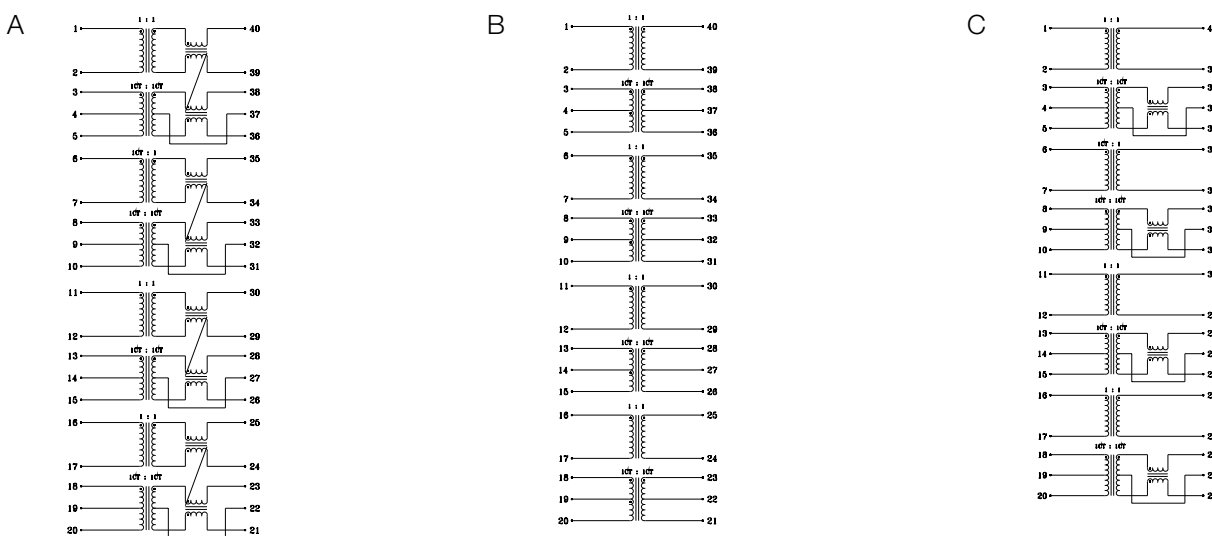
960064A

- Designed for use with AMD's eIMR (79C984A), eIMR+ (79C985) and IMR2 (79C983)/QuiET (79C988) chipsets
- Filterless quad, 4-port designs with common footprints and pinouts offering choice configurations for customized EMI suppression
- Best space and cost efficiency
- Low profile, surface mount packaging, rated to 225°C peak IR reflow temperature
- 2000 Vrms isolation

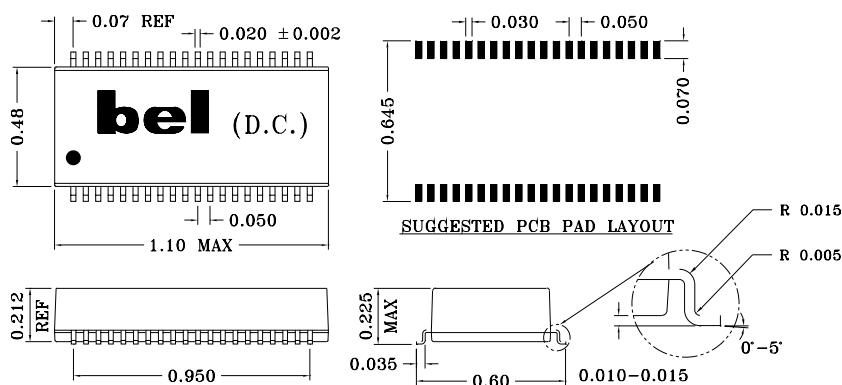
ELECTRICALS AT 25°C

Part No.	Insertion Loss	OCL (μ H) Min	Return Loss	Crosstalk	CM-CM Rej		Schematic
	(dB) Max 1-10MHz		(dB) Min 5MHz-10MHz	(dB) Min 1MHz-10MHz	(dB) Min 10-30MHz	(dB) Min 100MHz	
S553-5999-25	-1.0	100	-18	-40	-40	-30	A
S553-5999-26	-1.0	100	-18	-40	--	--	B
S553-5999-32	-1.0	100	-18	-40	-40	-30	C

SCHEMATICS



MECHANICAL



Specifications subject to change without notice.

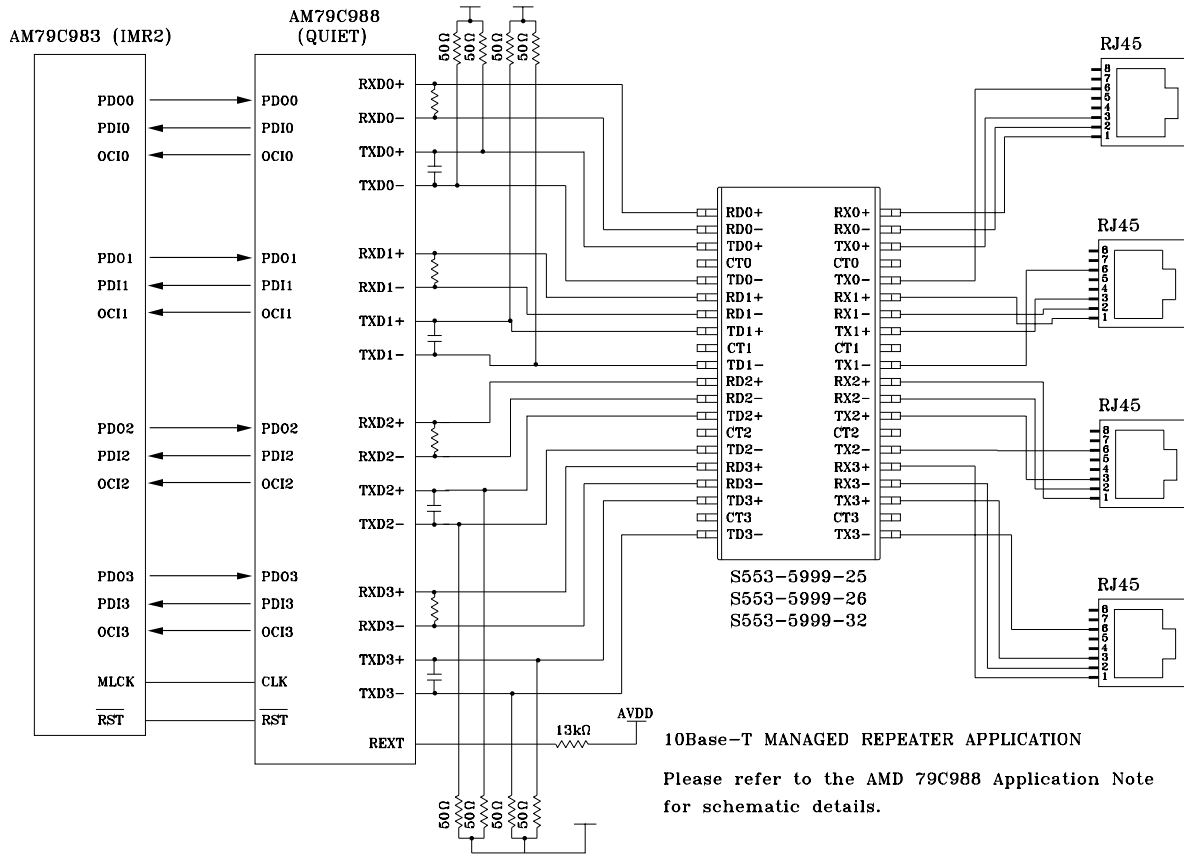


defining a degree of excellence

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APPLICATION CIRCUIT



APPLICATION NOTES

- Bel has developed a variety of quad, 4-port part types for use with AMD's product line of quad, 4-port PHY transceivers that incorporate digital filtering techniques within the silicon itself. Bel's "filterless magnetics" are optimized for this specific application and create a very cost efficient design solution. Each Bel part type contains transmit and receive transformers to provide for wave shaping, high voltage isolation and EMI noise suppression.
- Bel has designed these parts as a family of parts with common footprint and pinouts to enable the designer to customize the use of common mode choke for optimum system performance.
- In multi-port system applications, good PCB layout and proper grounding techniques are very critical to achieve FCC class A and B equipment approvals. Bel recommendations are available and can be provided by contacting our engineering department or your local sales representative.
- Bel's low profile, surface mount packaging is ideal for high speed pick and place machinery. Parts can be shipped on tape and reel for high speed placement. Construction processes have been implemented for thermal compatibility with high temperature IR reflow assembly processing. Post dipping of leads assist with PC board solderability. Each part is optically inspected to meet rigid coplanarity requirements.

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