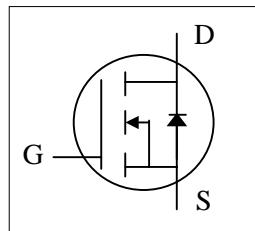
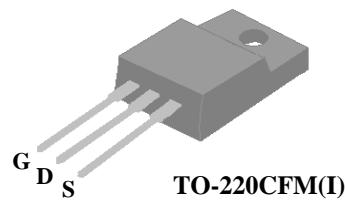




- ▼ Simple Drive Requirement
- ▼ Lower On-resistance
- ▼ Fast Switching Characteristic
- ▼ RoHS Compliant & Halogen-Free



| | |
|--------------|------|
| BV_{DSS} | 150V |
| $R_{DS(ON)}$ | 28mΩ |
| I_D | 26A |



Description

AP70T15 series are from Advanced Power innovative design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

The TO-220CFM package is widely preferred for all commercial-industrial through hole applications. The mold compound provides a high isolation voltage capability and low thermal resistance between the tab and the external heat-sink.

Absolute Maximum Ratings

| Symbol | Parameter | Rating | Units |
|---------------------------|--|------------|-------|
| V_{DS} | Drain-Source Voltage | 150 | V |
| V_{GS} | Gate-Source Voltage | +20 | V |
| $I_D @ T_C = 25^\circ C$ | Continuous Drain Current, $V_{GS} @ 10V$ | 26 | A |
| $I_D @ T_C = 100^\circ C$ | Continuous Drain Current, $V_{GS} @ 10V$ | 16.4 | A |
| I_{DM} | Pulsed Drain Current ¹ | 100 | A |
| $P_D @ T_C = 25^\circ C$ | Total Power Dissipation | 44.6 | W |
| $P_D @ T_A = 25^\circ C$ | Total Power Dissipation | 1.92 | W |
| T_{STG} | Storage Temperature Range | -55 to 150 | °C |
| T_J | Operating Junction Temperature Range | -55 to 150 | °C |

Thermal Data

| Symbol | Parameter | Value | Units |
|-------------|--|-------|-------|
| R_{thj-c} | Maximum Thermal Resistance, Junction-case | 2.8 | °C/W |
| R_{thj-a} | Maximum Thermal Resistance, Junction-ambient | 65 | °C/W |



Electrical Characteristics@ $T_j=25^\circ C$ (unless otherwise specified)

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Units |
|--------------|--|-------------------------------|------|------|-----------|-----------|
| BV_{DSS} | Drain-Source Breakdown Voltage | $V_{GS}=0V, I_D=250\mu A$ | 150 | - | - | V |
| $R_{DS(ON)}$ | Static Drain-Source On-Resistance ² | $V_{GS}=10V, I_D=17A$ | - | - | 28 | $m\Omega$ |
| $V_{GS(th)}$ | Gate Threshold Voltage | $V_{DS}=V_{GS}, I_D=250\mu A$ | 2 | - | 4 | V |
| g_f | Forward Transconductance | $V_{DS}=10V, I_D=20A$ | - | 60 | - | S |
| I_{DSS} | Drain-Source Leakage Current | $V_{DS}=120V, V_{GS}=0V$ | - | - | 25 | μA |
| I_{GSS} | Gate-Source Leakage | $V_{GS}=\pm 20V, V_{DS}=0V$ | - | - | ± 100 | nA |
| Q_g | Total Gate Charge | $I_D=20A$ | - | 125 | 200 | nC |
| Q_{gs} | Gate-Source Charge | $V_{DS}=120V$ | - | 25 | - | nC |
| Q_{gd} | Gate-Drain ("Miller") Charge | $V_{GS}=10V$ | - | 45 | - | nC |
| $t_{d(on)}$ | Turn-on Delay Time | $V_{DS}=75V$ | - | 25 | - | ns |
| t_r | Rise Time | $I_D=20A$ | - | 50 | - | ns |
| $t_{d(off)}$ | Turn-off Delay Time | $R_G=3.3\Omega$ | - | 60 | - | ns |
| t_f | Fall Time | $V_{GS}=10V$ | - | 50 | - | ns |
| C_{iss} | Input Capacitance | $V_{GS}=0V$ | - | 7000 | 11200 | pF |
| C_{oss} | Output Capacitance | $V_{DS}=25V$ | - | 390 | - | pF |
| C_{rss} | Reverse Transfer Capacitance | f=1.0MHz | - | 200 | - | pF |
| R_g | Gate Resistance | f=1.0MHz | - | 1.2 | 2.4 | Ω |

Source-Drain Diode

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Units |
|----------|---------------------------------|----------------------|------|------|------|-------|
| V_{SD} | Forward On Voltage ² | $I_S=17A, V_{GS}=0V$ | - | - | 1.3 | V |
| t_{rr} | Reverse Recovery Time | $I_S=10A, V_{GS}=0V$ | - | 90 | - | ns |
| Q_{rr} | Reverse Recovery Charge | $dI/dt=100A/\mu s$ | - | 380 | - | nC |

Notes:

1.Pulse width limited by Max. junction temperature.

2.Pulse test

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

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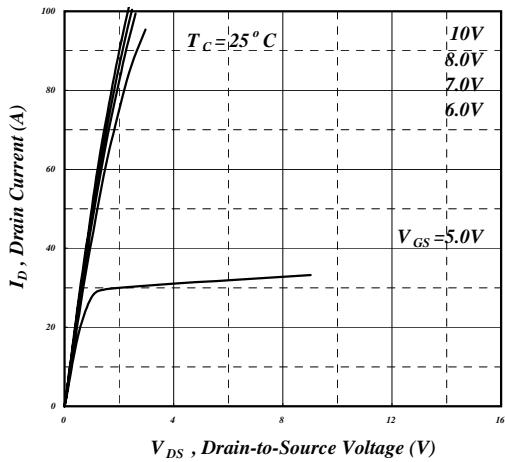


Fig 1. Typical Output Characteristics

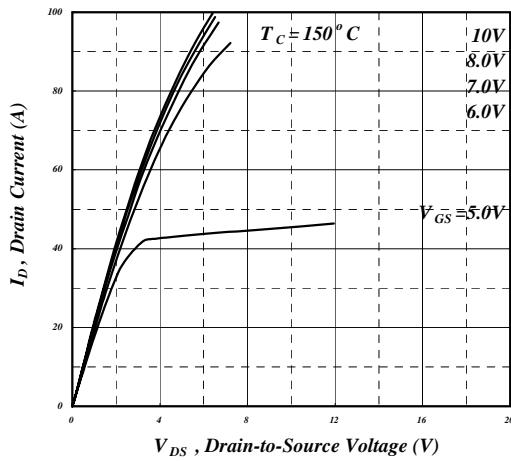


Fig 2. Typical Output Characteristics

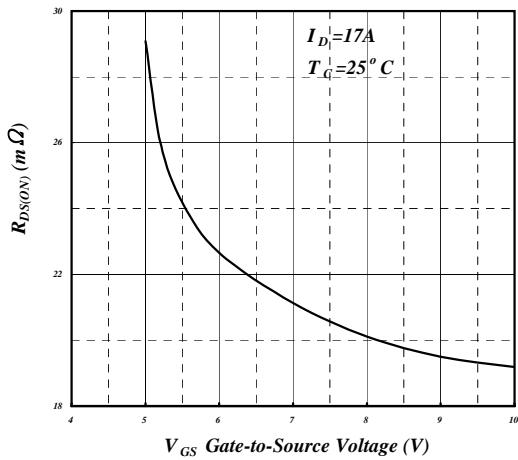


Fig 3. On-Resistance v.s. Gate Voltage

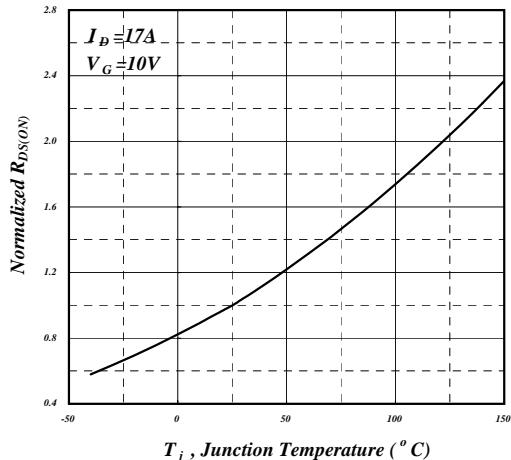


Fig 4. Normalized On-Resistance v.s. Junction Temperature

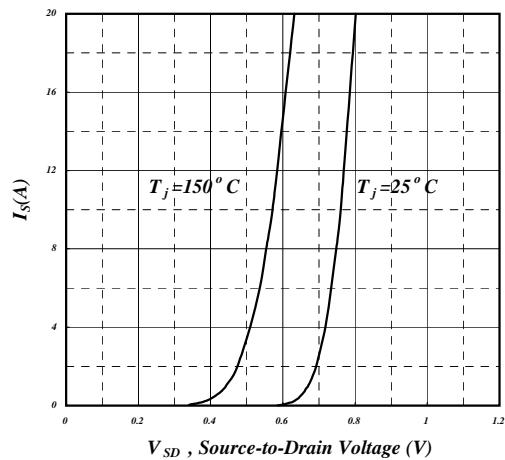


Fig 5. Forward Characteristic of Reverse Diode

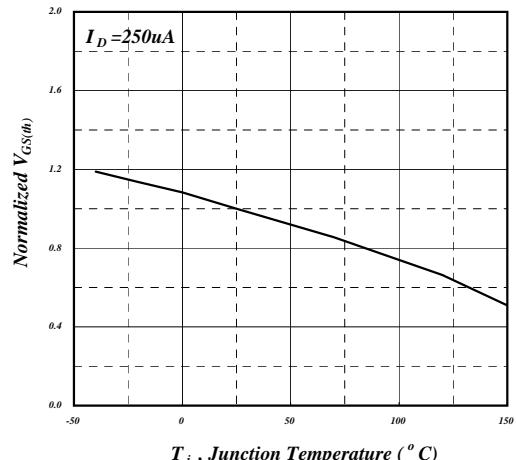


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

AP70T15GI-HF

