



Status: **Active**

9LPR363

Low Power Programmable Timing Control Hub™ for P4™ Processor

Product Quick Links

- [Specifications](#)
- [Product Options](#)
- [Documentation & Tools](#)
- [Technical Support](#)
- [Contact Sales](#)

Features

- 2 - 0.8V push-pull differential CPU pairs
- 7 - 0.8V push-pull differential PCIEX pairs
- 1 - 0.8V push-pull differential SATA pair
- 1 - 0.8V push-pull differential CPU/PCIEX selectable pair
- 1 - 0.8V push-pull differential 27MHz/LCDCLK/PCIEX selectable pair4 - PCI (33MHz)
- 2 - PCICLK_F, (33MHz) free-running
- 1 - USB, 48MHz
- 1 - DOT96/PCIEX selectable pair
- 2 - REF, 14.318MHz

Product Specification

CHIPSET MANUFACTURER	CHIPSET NAME	CLOCK SPEC.	CORE VOLTAGE (V)	INPUT FREQ (MHZ)	OUTPUTS (#)	PLATFORM NAME	POWER TYP (MW)	SPREAD SPECTRUM	XTAL FREQ (KHZ)
Intel	Calistoga, 940GML, 943GML, 945PM, 945GME, 945GSE, 945GMS	CK410M	3.3	14.320000	22			Yes	14.320000

Product Options

ORDERABLE PART ID	PART STATUS	PKG. CODE	PKG. TYPE	LEAD COUNT (#)	TEMP. GRADE	PB (LEAD) FREE	ORDER
9LPR363DGLF	Active	PAG64	TSSOP	64	C	Y	Check Inventory
9LPR363DGLFT	Active	PAG64	TSSOP	64	C	Y	Check Inventory
9LPR363EGLF	Active	PAG64	TSSOP	64	C	Y	Check Inventory
9LPR363EGLFT	Active	PAG64	TSSOP	64	C	Y	Check Inventory

Documents

TITLE	TYPE	FORMAT	FILE SIZE	DATE
PC Clocks Contact Info	Misc	PDF	62 KB	05/29/2007

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