

## Schottky Rectifier, 175 A



PowerTab®

PRODUCT SUMMARY				
Package	PowerTab <sup>®</sup>			
I <sub>F(AV)</sub>	175 A			
$V_{R}$	30 V			
V <sub>F</sub> at I <sub>F</sub>	0.52 V			
I <sub>RM</sub>	650 mA at 125 °C			
T <sub>J</sub> max.	125 °C			
Diode variation	Single die			
E <sub>AS</sub>	80 mJ			

#### **FEATURES**

- 150 °C max. operating junction temperature
- High frequency operation
- Ultralow forward voltage drop
- Continuous high current operation
- Guard ring for enhanced ruggedness and long term reliability



COMPLIANT

- Screw mounting only
- Designed and qualified according to JEDEC-JESD47
- PowerTab<sup>®</sup> package
- Compliant to RoHS Directive 2002/95/EC

#### **DESCRIPTION**

The VS-175BGQ030 Schottky rectifier has been optimized for ultralow forward voltage drop specifically for low voltage output in high current AC/DC power supplies.

The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
. Rectangular waveform		175	Α		
I <sub>F(AV)</sub>	T <sub>C</sub>	112	°C		
V <sub>RRM</sub>		30	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	7400	А		
V	175 A <sub>pk</sub> (typical)	0.47	V		
V <sub>F</sub>	TJ	150	°C		
T <sub>J</sub>	Range	- 55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-175BGQ030	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	30	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	30	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 112 °C, rectangular waveform		175	Α
Maximum peak one cycle	_	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	7400	Α
non-repetitive surge current	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	1400	ζ
Non-repetitive avalanche energy	E <sub>AS</sub>	$T_J = 25 ^{\circ}\text{C},  I_{AS} = 12 \text{A},  L = 1.12 \text{mH}$		mJ	
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s  Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		А	



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
	V <sub>FM</sub> <sup>(1)</sup>	100 A	T <sub>J</sub> = 25 °C	0.47	0.49	V
Forward voltage drop		175 A		0.55	0.59	
Forward voltage drop		100 A	T <sub>J</sub> = 150 °C	0.36	0.39	
		175 A		0.47	0.52	
			T <sub>J</sub> = 125 °C, V <sub>R</sub> = 15 V		220	
Reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	$T_J = 150 ^{\circ}\text{C},  V_R = 30 ^{\circ}\text{V}$		1400	2000	mA
		T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	1.3	4.5	IIIA
		T <sub>J</sub> = 125 °C		450	650	
Maximum junction capacitance	C <sub>T</sub>	$V_R = 5 V_{DC}$ , (test signal range 100 kHz to 1 MHz), 25 °C		85	00	pF
Typical series inductance	L <sub>S</sub>	Measured from tab to mounting plane 3.5		.5	nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000		V/µs		

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL TEST CONDITIONS		VALUES	UNITS		
Maximum junction and temperature range	storage	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 150	°C	
Maximum thermal resis junction to case	tance,	R <sub>thJC</sub>	DC operation	0.25	°C/W	
Typical thermal resistar case to heatsink	nce,	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.20	C/VV	
Approximate weight				5	g	
				0.18	OZ.	
Mounting torque —	minimum			1.2 (10)	N · m	
	maximum			2.4 (20)	(lbf $\cdot$ in)	
Marking device	Marking device Case style PowerTab® 175B		Q045			

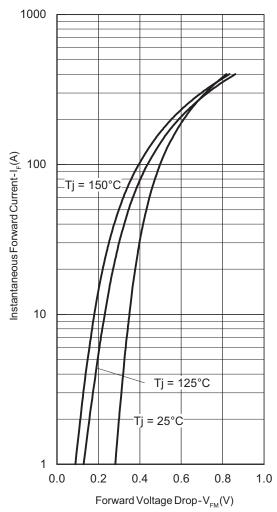


Fig. 1 - Maximum Forward Voltage Drop Characteristics

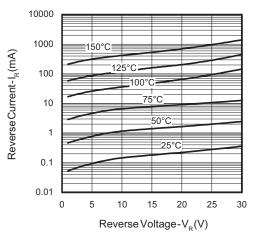


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

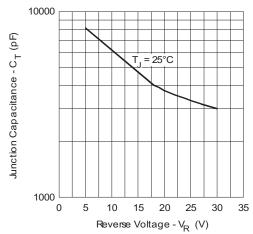


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

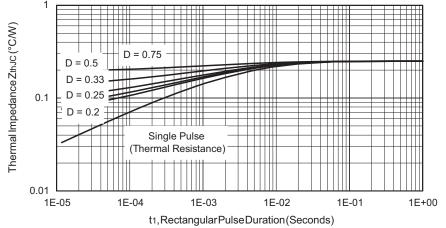


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

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### Vishay Semiconductors

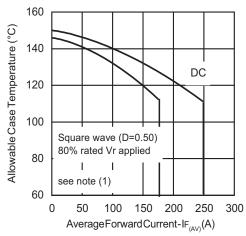


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

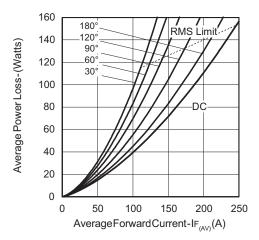


Fig. 6 - Forward Power Loss Characteristics

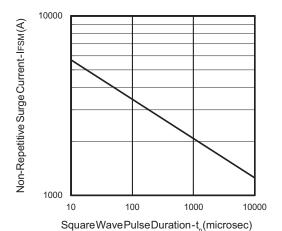


Fig. 7 - Maximum Non-Repetitive Surge Current

D.U.T. High-speed switch  $R_g = 25 \Omega$  Freewheel diode  $\frac{1}{40}$  High-speed switch  $\frac{1}{40}$  High-spe

Fig. 8 - Unclamped Inductive Test Circuit

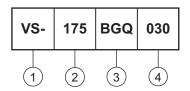
#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$ ;  $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = 80 \%$  rated  $V_R$ 



### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

2 - Current rating

Essential part number

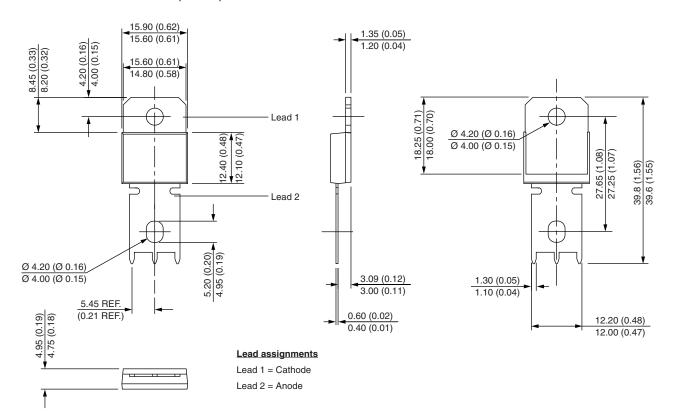
Voltage code = V<sub>RRM</sub>

LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95240				
Part marking information	www.vishay.com/doc?95370			
SPICE model <u>www.vishay.com/doc?95427</u>				
Application note	www.vishay.com/doc?95179			



### PowerTab®

#### **DIMENSIONS** in millimeters (inches)







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