



# PI5C32861

## 10-Bit, 2-Port Bus Switch with 25Ω Series Resistor

### Product Features

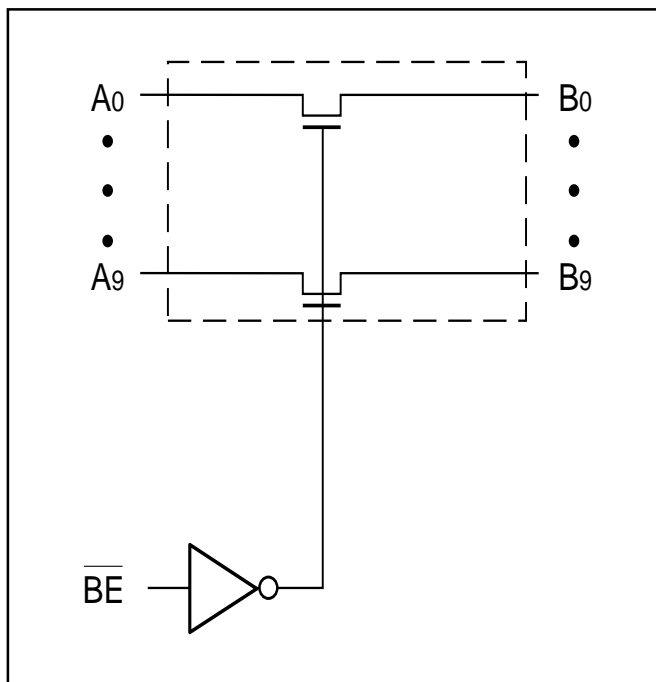
- Near zero propagation delay
- 5Ω switches with 25Ω series resistor connect inputs to outputs
- Direct bus connection when switches are ON
- Ultra Low Quiescent Power (0.2μA Typical)
  - Ideally suited for notebook applications
- Packages available:
  - 24-pin 150 mil wide plastic QSOP (Q)
  - 24-pin 300 mil wide plastic SOIC (S)

### Product Description

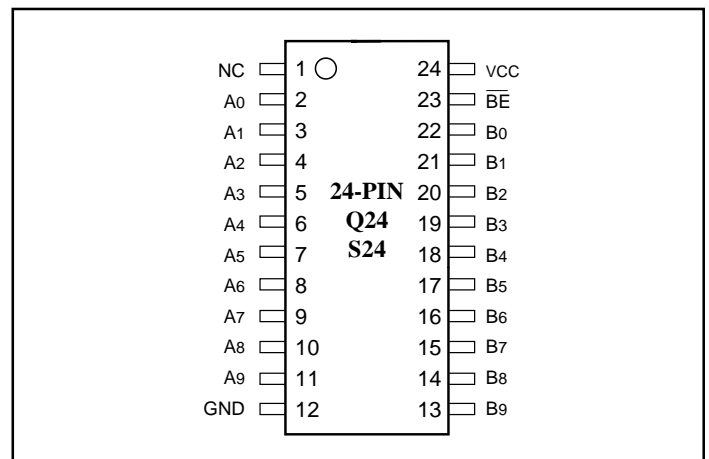
Pericom Semiconductor's PI5C series of logic circuits are produced using the Company's advanced 0.8 micron CMOS technology, achieving industry leading performance.

The PI5C32861 is a 10-bit, 2-port bus switch designed with a low ON resistance that permits inputs to be connected directly to outputs. The bus switch creates no additional propagational delay or additional ground bounce noise. The switches are turned ON by the Bus Enable (BE) input signal. The PI5C32861 is designed with an internal 25Ω resistor that reduces noise reflection in high-speed applications

### Logic Block Diagram



### Product Pin Configuration



### Product Pin Description

Pin Name	Description
$\overline{BE}$	Bus Enable Input (Active LOW)
A0-9	Bus A
B0-9	Bus B
GND	Ground
Vcc	Power

### Truth Table<sup>(1)</sup>

Function	$\overline{BE}$	A0-9
Disconnect	H	Hi-Z
Connect	L	B0-9

**Note:** 1. H = High Voltage Level  
 L = Low Voltage Level  
 Hi-Z = High Impedance



### Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature .....	-65°C to +150°C
Ambient Temperature with Power Applied .....	-40°C to +85°C
Supply Voltage to Ground Potential (Inputs & Vcc Only) ..	-0.5V to +7.0V
Supply Voltage to Ground Potential (Outputs & D/O Only)	-0.5V to +7.0V
DC Input Voltage .....	-0.5V to +7.0V
DC Output Current .....	120mA
Power Dissipation .....	0.5W

**Note:**

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

### DC Electrical Characteristics (Over the Operating Range, TA = -40°C to +85°C, VCC = 5V ±5%)

Parameters	Description	Test Conditions <sup>(1)</sup>	Min.	Typ <sup>(2)</sup>	Max.	Units
V <sub>IH</sub>	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V
V <sub>IL</sub>	Input LOW Voltage	Guaranteed Logic LOW Level	-0.5		0.8	V
I <sub>IH</sub>	Input HIGH Current	V <sub>CC</sub> = Max., V <sub>IN</sub> = V <sub>CC</sub>			±1	μA
I <sub>IL</sub>	Input LOW Current	V <sub>CC</sub> = Max., V <sub>IN</sub> = GND			±1	μA
I <sub>OZH</sub>	High Impedance Output Current	0 - A, B - V <sub>CC</sub>			±1	μA
V <sub>IK</sub>	Clamp Diode Voltage	V <sub>CC</sub> = Min., I <sub>IN</sub> = -18mA		-0.7	-1.2	V
I <sub>OS</sub>	Short Circuit Current <sup>(3)</sup>	A (B) = 0V, B (A) = V <sub>CC</sub> 100				mA
V <sub>H</sub>	Input Hysteresis at Control Pins			150		mV
R <sub>ON</sub>	Switch On Resistance <sup>(4)</sup>	V <sub>CC</sub> = Min., V <sub>IN</sub> = 0.0V, I <sub>ON</sub> = 48mA V <sub>CC</sub> = Min., V <sub>IN</sub> = 2.4V, I <sub>ON</sub> = 15mA	18 18	28 35	40 48	Ω

### Capacitance (TA = 25°C, f = 1 MHz)

Parameters <sup>(5)</sup>	Description	Test Conditions	Typ	Max.	Units
C <sub>IN</sub>	Input Capacitance	V <sub>IN</sub> = 0V		6	pF
C <sub>OFF</sub>	A/B Capacitance, Switch Off	V <sub>IN</sub> = 0V		6	pF
C <sub>ON</sub>	A/B Capacitance, Switch On	V <sub>IN</sub> = 0V		8	pF

**Notes:**

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at V<sub>CC</sub> = 5.0V, TA = 25°C ambient and maximum loading.
3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
4. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.
5. This parameter is determined by device characterization but is not production tested.



### Power Supply Characteristics

Parameters	Description	Test Conditions <sup>(1)</sup>		Min.	Typ <sup>(2)</sup>	Max.	Units
I <sub>CC</sub>	Quiescent Power Supply Current	V <sub>CC</sub> = Max.	V <sub>IN</sub> = GND or V <sub>CC</sub>		0.1	10	μA
ΔI <sub>CC</sub>	Supply Current per Input @ TTL HIGH	V <sub>CC</sub> = Max.	V <sub>IN</sub> = 3.4V <sup>(3)</sup>			2.5	mA
I <sub>CCD</sub>	Supply Current per Input per MHz <sup>(4)</sup>	V <sub>CC</sub> = Max., A and B Pins Open $\overline{BE}$ = GND Control Input Toggling 50% Duty Cycle				0.25	mA/ MHz

**Notes:**

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at V<sub>CC</sub> = 5.0V, +25°C ambient.
3. Per TTL driven input (V<sub>IN</sub> = 3.4V, control inputs only); A and B pins do not contribute to I<sub>CC</sub>.
4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

### Switching Characteristics over Operating Range

Parameters	Description	Conditions <sup>(1)</sup>	PI5C32861		Units
			Com.		
			Min	Max	
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay <sup>(2,3)</sup> A <sub>X</sub> to B <sub>X</sub> , B <sub>X</sub> to A <sub>X</sub>	C <sub>L</sub> = 50pF R <sub>L</sub> = 500Ω		1.25	ns
t <sub>PZH</sub> t <sub>PZL</sub>	Bus Enable Time $\overline{BE}$ to A <sub>X</sub> or B <sub>X</sub>		1.5	6.5	ns
t <sub>PHZ</sub> t <sub>PLZ</sub>	Bus Disable Time $\overline{BE}$ to A <sub>X</sub> or B <sub>X</sub>		1.5	5.5	ns

**Notes:**

1. See test circuit and waveforms.
2. This parameter is guaranteed but not tested on Propagation Delays.
3. The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 1.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.