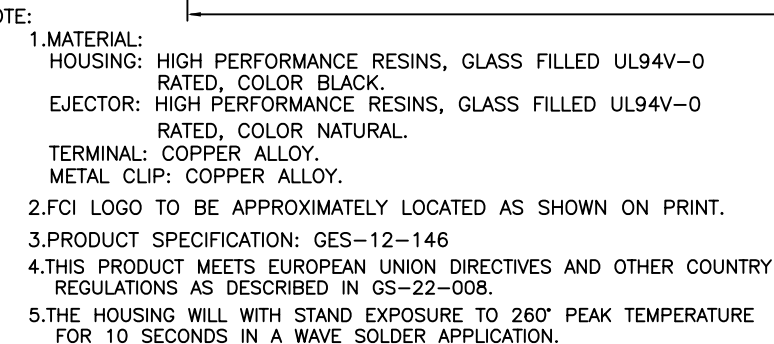


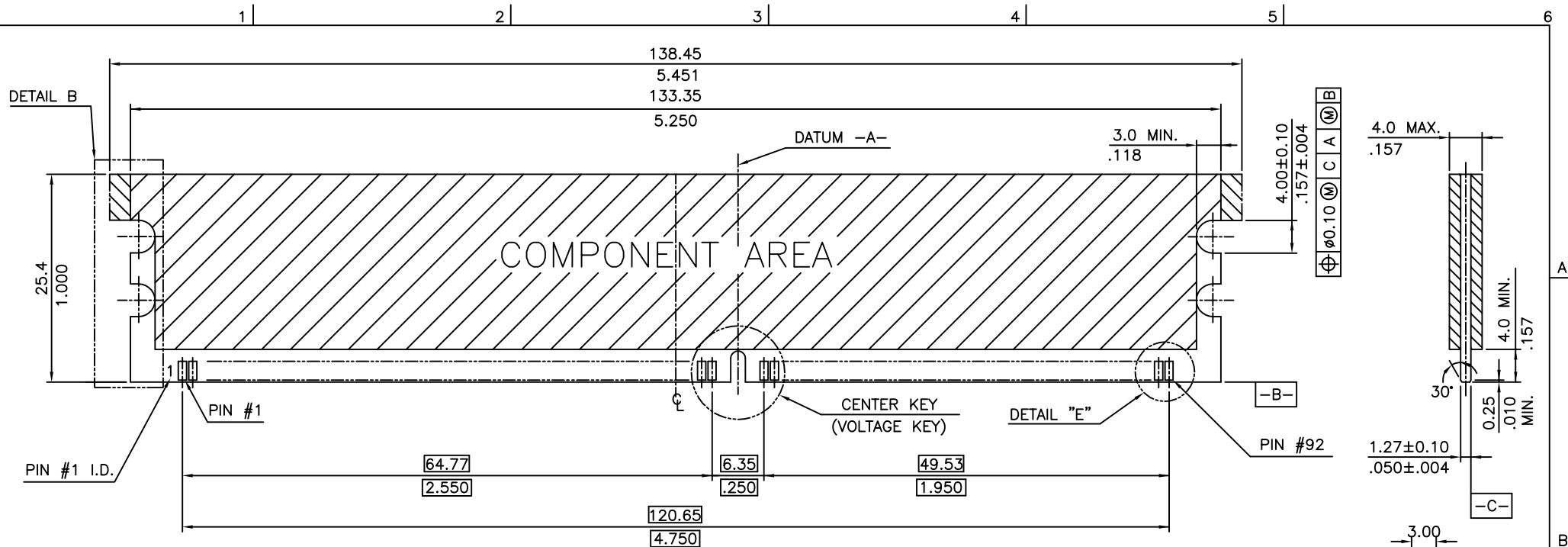
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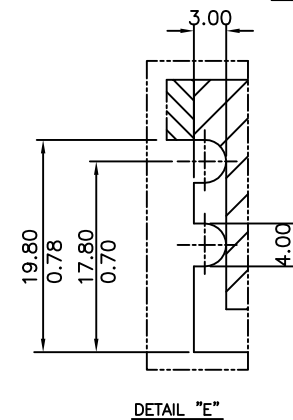
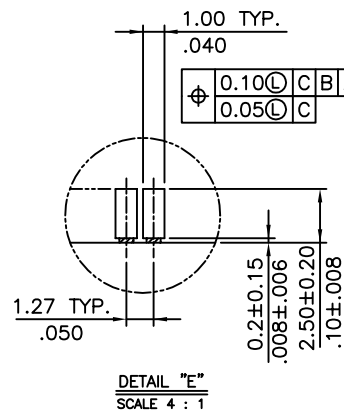
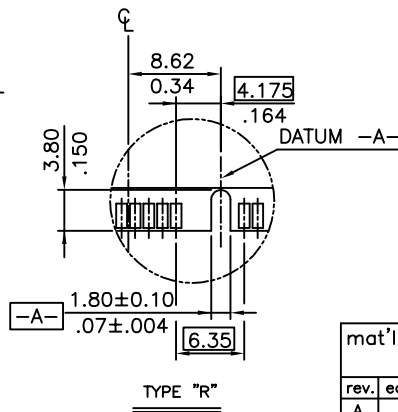
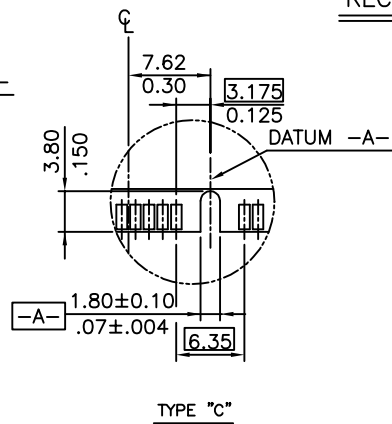
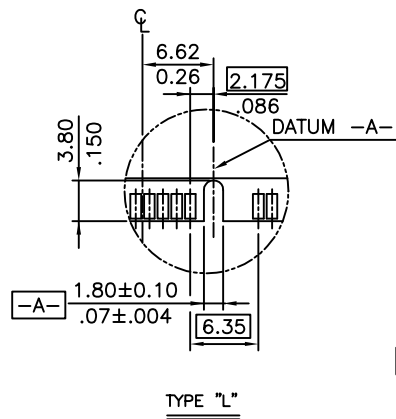
[illegible]

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RECOMMENDED MODULE LAYOUT



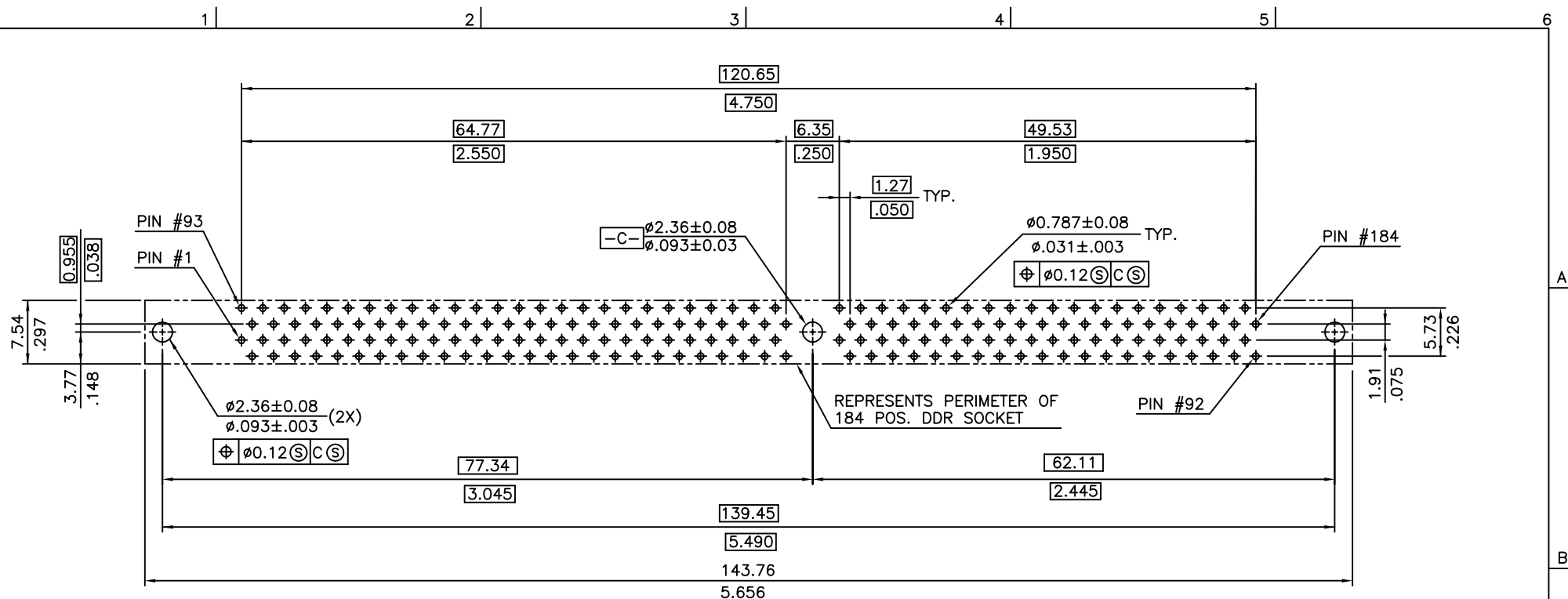
TYPE "R"	3.3 VOLT.
TYPE "L"	2.5 VOLT.
TYPE "C"	1.8 VOLT.
CENTER KEY	DESCRIPTION

mat'l code	tolerance unless otherwise specified	CUSTOMER COPY	FCI Electronics www.fciconnect.com
rev. ecn no. dr date	.0 ±0.30/.012 linear .00 ±0.20/.008 .000±0.10/.004 angles ± 2'	projection	title DDR 184P .050" 1-ROW, V/T ASS'Y METAL CLIP (GIA)
dr MAGGIE WUP8/07/01	unit mm/inch	product family DDR	code TWN
enr BEN TSAO 08/07/01	scale 2 : 1	size dwg no. 57927	sheet 2 OF
chr BEN TSAO 08/07/01			
appd JENN TSAO 08/07/01			
sheet revision			
index sheet			

PDM: Rev:C

STATUS: Released

Printed: Jul 28, 2010



RECOMMENDED CIRCUIT BOARD HOLE LAYOUT

DDR ASSEMBLY PART NUMBER

5 7 9 2 7 - □ □ □ □ □

STYLE

- 1 : FOR 3.3 VOLT.
2 : FOR 2.5 VOLT.
3 : FOR 1.8 VOLT.

METAL CLIP

1 : WITH METAL CLIP + DIM."D"=3.50/.138

PLATING

CODE	CONTACT	SOLDERTAIL	UNDERPLATE	CODE	CONTACT	SOLDERTAIL	UNDERPLATE
2	15μ"(0.38μm) MIN. GOLD	100μ"(2.54μm) MIN. TIN/LEAD	50μ" (1.27μm) MIN. NICKEL OVERALL	7LF	15μ"(0.38μm) MIN. GOLD	100μ"(2.54μm) MIN. 100% TIN	50μ" (1.27μm) MIN. NICKEL OVERALL
3	30μ"(0.76μm) MIN. GOLD			8LF	30μ"(0.76μm) MIN. GOLD		
4	GOLD FLASH			9LF	GOLD FLASH		

COLOR OF HOUSING AND EJECTOR

0 : BLACK HOUSING + IVORY EJECTOR

TAIL LENGTH		
CODE	DIM. "B"	DIM. "C"
0	2.59 .102	2.44 .096
1	3.35 .132	3.20 .126

mat'l code					tolerance unless otherwise specified		CUSTOMER COPY		 Electronics www.fciconnect.com		
rev.	ecn	no.	dr	date	linear	.0 ±0.30/.012	projection 	title DDR 184P .050" 1-ROW, V/T ASS'Y METAL CLIP (GIAI)			
B						.00 ±0.20/.008					
						.000±0.10/.004					
					angles	± 2°					
			dr	MAGGIE WUB	08/07/01	unit		product family		DDR	code
			engr	BEN TSAO	08/07/01	mm/inch		size		dwg no.	TWN
			chr	BEN TSAO	08/07/01	scale		A3		57927	sheet
			appd	JENN TSAO	08/07/01	2 : 1					3 OF
sheet		revision									
index		sheet									