ISL9V5036S3S / ISL9V5036P3



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EcoSPARKTM 500mJ, 360V, N-Channel Ignition IGBT

General Description

The ISL9V5036S3S and ISL9V5036P3 are the next generation IGBTs that offer outstanding SCIS capability in the D²-Pak (TO-263) and TO-220 plastic package. These devices are intended for use in automotive ignition circuits, specifically as coil drivers. Internal diodes provide voltage clamping without the need for external components.

EcoSPARK™ devices can be custom made to specific clamp voltages. Contact your nearest Fairchild sales office for more information.

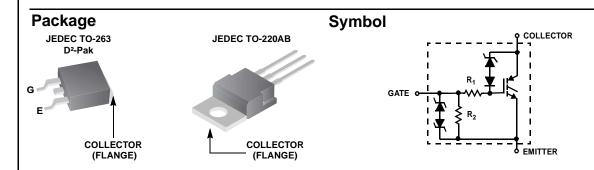
Formerly Developmental Type 49443

Applications

- · Automotive Ignition Coil Driver Circuits
- Coil- On Plug Applications

Features

- Industry Standard D-Pak package
- SCIS Energy = 500mJ at T_J = 25°C
- · Logic Level Gate Drive



Device Maximum Ratings T_A = 25°C unless otherwise noted

Symbol	Parameter	Ratings	Units
BV _{CER}	Collector to Emitter Breakdown Voltage (I _C = 1 mA)	390	V
BV _{ECS}	Emitter to Collector Voltage - Reverse Battery Condition (I _C = 10 mA)	24	V
E _{SCIS25}	At Starting $T_J = 25$ °C, $I_{SCIS} = 38.5A$, $L = 670 \mu Hy$	500	mJ
E _{SCIS150}	At Starting $T_J = 150$ °C, $I_{SCIS} = 30$ A, $L = 670 \mu Hy$	300	mJ
I _{C25}	Collector Current Continuous, At T _C = 25°C, See Fig 9	46	Α
I _{C110}	Collector Current Continuous, At T _C = 110°C, See Fig 9	31	А
V _{GEM}	Gate to Emitter Voltage Continuous	±10	V
P _D	Power Dissipation Total T _C = 25°C	200	W
	Power Dissipation Derating T _C > 25°C	1.33	W/°C
TJ	Operating Junction Temperature Range	-40 to 175	°C
T _{STG}	Storage Junction Temperature Range	-40 to 175	°C
T _L	T _L Max Lead Temp for Soldering (Leads at 1.6mm from Case for 10s)		°C
T _{pkg}	Max Lead Temp for Soldering (Package Body for 10s)		°C
ESD	Electrostatic Discharge Voltage at 100pF, 1500Ω	4	kV

Package Marking and Ordering Information	Package	Marking	and	Orderina	Information
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	Device Marking	Device	Package	Reel Size	Tape Width	Quantity
_	V5036S	ISL9V5036S3ST	TO-263AB	330mm	24mm	800 units
_	V5036S	ISL9V5036S3S	TO-263AB	Tube	N/A	50 units
	V5036P	ISL9V5036P3	TO-220AB	Tube	N/A	50 units

Electrical Characteristics $T_A = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions		Min	Тур	Max	Units
Off State	Characteristics						
BV _{CER}	Collector to Emitter Breakdown Voltage	$I_C = 2mA$, $V_{GE} = 0$, $R_G = 1K\Omega$, See Fig. 15 $T_J = -40$ to 150°C		330	360	390	V
BV _{CES}	Collector to Emitter Breakdown Voltage	$I_C = 10 \text{mA}, V_{GE} = 0,$ $R_G = 0, \text{See Fig. 15}$ $T_J = -40 \text{ to } 150^{\circ}\text{C}$		360	390	420	V
BV _{ECS}	Emitter to Collector Breakdown Voltage	$I_C = -75 \text{mA}, V_{GE} = 0 \text{V},$ $T_C = 25 ^{\circ} \text{C}$		30	-	-	V
BV _{GES}	Gate to Emitter Breakdown Voltage	$I_{GES} = \pm 2mA$		±12	±14	-	V
I _{CER}	Collector to Emitter Leakage Current	$V_{CER} = 250V$,	$T_C = 25^{\circ}C$	-	-	25	μΑ
		$R_G = 1K\Omega$, See Fig. 11	T _C = 150°C	-	-	1	mA
I _{ECS}	Emitter to Collector Leakage Current	$V_{EC} = 24V$,	$T_C = 25$ °C	-	-	1	mA
		See Fig. 11	$T_C = 150$ °C	-	-	40	mA
R ₁	Series Gate Resistance			-	75	-	Ω
R_2	Gate to Emitter Resistance			10K	-	30K	Ω
V _{CE(SAT)}	Characteristics Collector to Emitter Saturation Voltage	I _C = 10A,	T _C = 25°C,	-	1.17	1.60	V
V _{CE(SAT)}	Collector to Emitter Saturation Voltage	$V_{GE} = 4.0V$ $I_{C} = 15A,$ $V_{GE} = 4.5V$	See Fig. 4 T _C = 150°C	-	1.50	1.80	V
Oynamic Q _{G(ON)}	Characteristics Gate Charge	$I_{C} = 10A, V_{CE} = 12V,$ $V_{GE} = 5V, \text{ See Fig. } 14$ $I_{C} = 1.0\text{mA}, T_{C} = 25^{\circ}\text{C}$ $V_{CE} = V_{GE}, T_{C} = 150^{\circ}\text{C}$		-	32	-	nC
				4.0	 	0.0	17
V _{GE(TH)}	Gate to Emitter Threshold Voltage			1.3 0.75	-	1.8	V
V_{GEP}	Gate to Emitter Plateau Voltage	See Fig. 10 I _C = 10A, V _{CE} = 12V		-	3.0	-	V
Switching	Characteristics						
t _{d(ON)R}	Current Turn-On Delay Time-Resistive	$V_{CE} = 14V, R_L = 1\Omega,$		-	0.7	4	μs
t _{rR}	Current Rise Time-Resistive	$V_{GE} = 5V$, $R_G = 1K\Omega$ $T_J = 25^{\circ}C$, See Fig. 12		-	2.1	7	μs
t _{d(OFF)L}	Current Turn-Off Delay Time-Inductive	$V_{CE} = 300V, R_L = 46\Omega,$		-	4.8	15	μs
t _{fL}	Current Fall Time-Inductive	$V_{GE} = 5V, R_G = 1K\Omega$ T _J = 25°C, See Fig. 12		-	2.8	15	μs
SCIS	Self Clamped Inductive Switching	$\begin{split} T_J &= 25^{\circ}\text{C}, \text{ L} = 670 \mu\text{H}, \\ R_G &= 1\text{K}\Omega, \text{ V}_{GE} = 5\text{V}, \text{ See} \\ \text{Fig. 1 \& 2} \end{split}$		-	-	500	mJ
Thermal (Characteristics						
$R_{\theta JC}$	Thermal Resistance Junction-Case	TO-263, TO-220		-	-	0.75	°C/V
	<u> </u>	. 5 255, 15 225					

Typical Characteristics

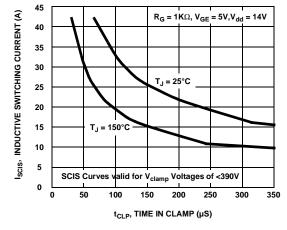


Figure 1. Self Clamped Inductive Switching Current vs Time in Clamp

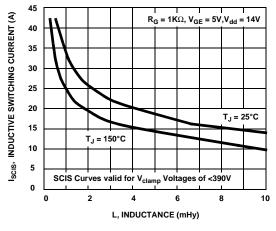


Figure 2. Self Clamped Inductive Switching Current vs Inductance

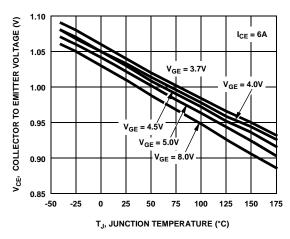


Figure 3. Collector to Emitter On-State Voltage vs Junction Temperature

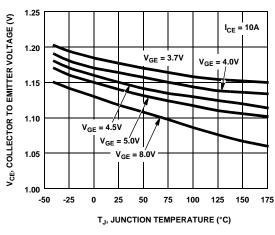


Figure 4. Collector to Emitter On-State Voltage vs Junction Temperature

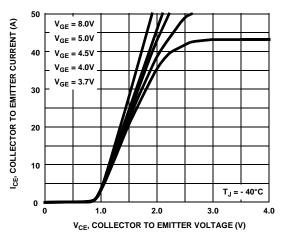


Figure 5. Collector Current vs Collector Emitter On-State Voltage

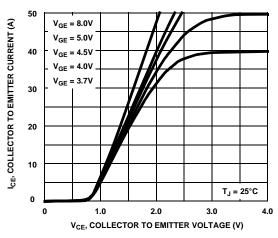
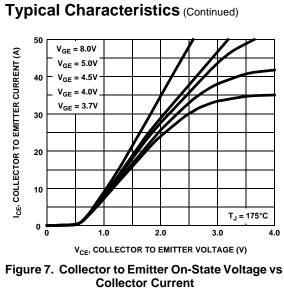
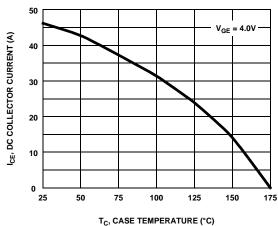


Figure 6. Collector Current vs Collector Emitter On-State Voltage



DUTY CYCLE < 0.5%, $V_{CE} = 5V$ I_{CE}, COLLECTOR TO EMITTER CURRENT (A) PULSE DURATION = 250µs 40 30 T_J = 175°C 20 T_J = 25°C 10 $T_J = -40^{\circ}C$ 0 1.0 1.5 3.5 4.0 V_{GE}, GATE TO EMITTER VOLTAGE (V)

Figure 8. Transfer Characteristics



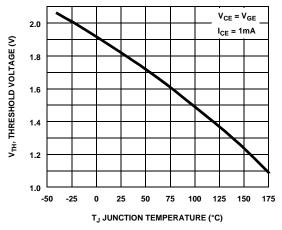
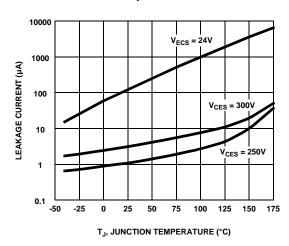


Figure 9. DC Collector Current vs Case **Temperature**

Figure 10. Threshold Voltage vs Junction **Temperature**



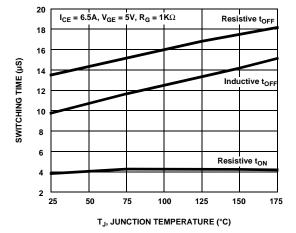
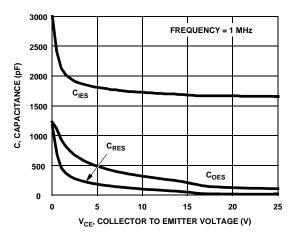


Figure 11. Leakage Current vs Junction **Temperature**

Figure 12. Switching Time vs Junction **Temperature**

Typical Characteristics (Continued)



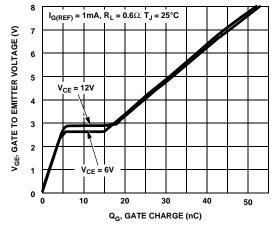


Figure 13. Capacitance vs Collector to Emitter Voltage

Figure 14. Gate Charge

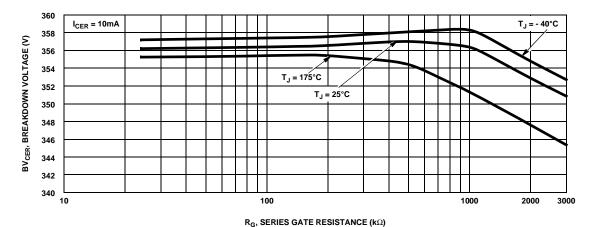


Figure 15. Breakdown Voltage vs Series Gate Resistance

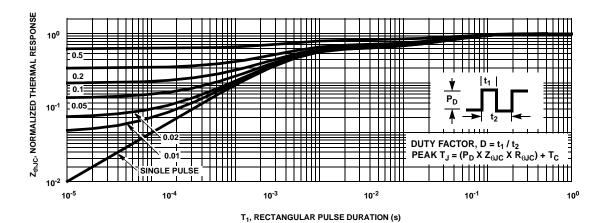
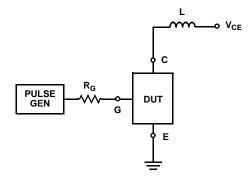


Figure 16. IGBT Normalized Transient Thermal Impedance, Junction to Case

Test Circuits and Waveforms



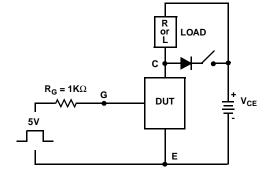


Figure 17. Inductive Switching Test Circuit

Figure 18. t_{ON} and t_{OFF} Switching Test Circuit

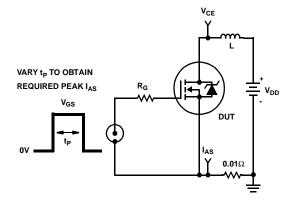


Figure 19. Unclamped Energy Test Circuit

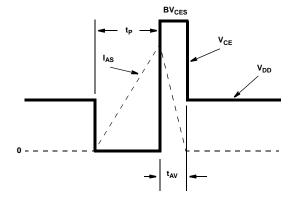


Figure 20. Unclamped Energy Waveforms

SPICE Thermal Model JUNCTION REV 31 May 2001 ISL9V5036S3S / ISL9V3036P3 CTHERM1 th 6 4.0e-2 CTHERM2 6 5 4.8e-4 CTHERM3 5 4 4.7e-4 RTHERM1 CTHERM1 CTHERM4 4 3 6.4e-2 CTHERM5 3 2 4.9e-2 CTHERM6 2 tl 4.9e-2 6 RTHERM1 th 6 6.7e-2 RTHERM2 6 5 1.7e-2 RTHERM3 5 4 2.5e-1 RTHERM2 CTHERM2 RTHERM4 4 3 6.5e-2 RTHERM5 3 2 6.4e-2 RTHERM6 2 tl 1.0e-1 5 SABER Thermal Model SABER thermal model ISL9V5036S3S / ISL9V5036P3 RTHERM3 CTHERM3 template thermal_model th tl thermal_c th, tl ctherm.ctherm1 th 6 = 4.0e-2 ctherm.ctherm2 6.5 = 4.8e-4ctherm.ctherm3 5 4 = 4.7e-4 ctherm.ctherm4 4 3 = 6.4e-2 RTHERM4 CTHERM4 ctherm.ctherm5 32 = 4.9e-2ctherm.ctherm6 2 tl = 4.9e-2 rtherm.rtherm1 th 6 = 6.7e-23 rtherm.rtherm2 6 5 = 1.7e-2 rtherm.rtherm354 = 2.5e-1rtherm.rtherm4 43 = 6.5e-2RTHERM5 CTHERM5 rtherm.rtherm5 32 = 6.4e-2rtherm.rtherm6 2 tl = 1.0e-1 2 RTHERM6 CTHERM6

CASE

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