

SUR535H

Epitaxial planar PNP silicon transistor

Description

• Dual chip digital transistor

Features

- Two SRA2202 chips in SOT-353 package
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process

Package: SOT-353

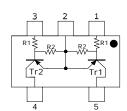
Ordering Information

Type NO.	Marking	Package Code
SUR535H	<u>35H</u> □ ① ②	SOT-353

①Device Code ②Year&Week Code

Equivalent circuit & PIN Connections

• Equivalent Circuit



	\mathbf{R}_{1}	\mathbf{R}_2
Tr1	10ΚΩ	10ΚΩ
Tr2	10ΚΩ	10ΚΩ

PIN Connections

- 1. IN 1
- 2. COMMON 1,2
- 3. IN 2
- 4. OUT 2
- 5. OUT 1

Absolute Maximum Ratings [Tr1,Tr2]

 $(Ta=25^{\circ}C)$

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	-50	V
Input voltage	Vı	-30,10	V
Output current	I _O	-100	mA
Power dissipation	P _D **	200	mW
Junction temperature	TJ	150	°C
Storage temperature range	T _{stg}	-55 ~ 150	°C

*: Total rating

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Electrical Characteristics [Tr1,Tr2]

(Ta=25°C)

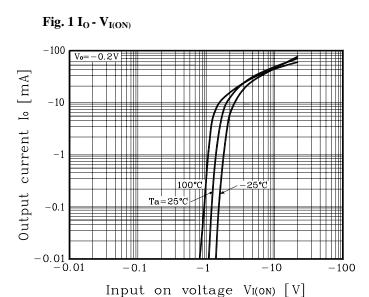
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I _{O(OFF)}	V ₀ =-50V, V ₁ =0	-	-	-500	nA
DC current gain	G _I	V _O =-5V, I _O =-10mA	50	80	-	-
Output voltage	V _{O(ON)}	I _O =-10mA, I _I =-0.5mA	-	-0.1	-0.3	V
Input voltage (ON)	V _{I(ON)}	V _O =-0.2V, I _O =-5mA	-	-1.8	-2.4	V
Input voltage (OFF)	V _{I(OFF)}	V _O =-5V, I _O =-0.1mA	-1.0	-1.2	-	٧
Transition frequency	f _T *	$V_0 = -10V$, $I_0 = -5$ mA, $f = 1$ MHz	-	200	-	MHz
Input current	I ₁	V _I =-5V, I _O =0	-	-	-0.88	mA
Input resistor (Input to base)	R ₁	-	7	10	13	ΚΩ
Input resistor (Base to common)	R ₂	-	7	10	13	ΚΩ

^{* :} Characteristic of transistor only

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Electrical Characteristic Curves

[Tr1,Tr2]



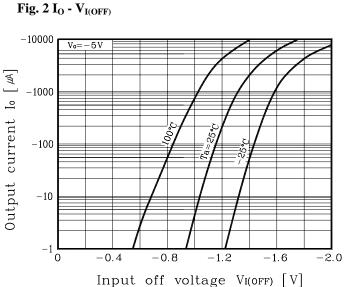
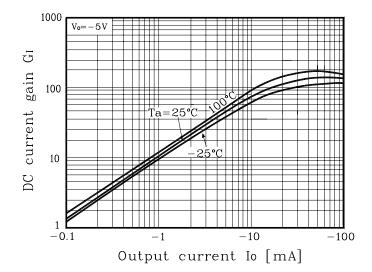
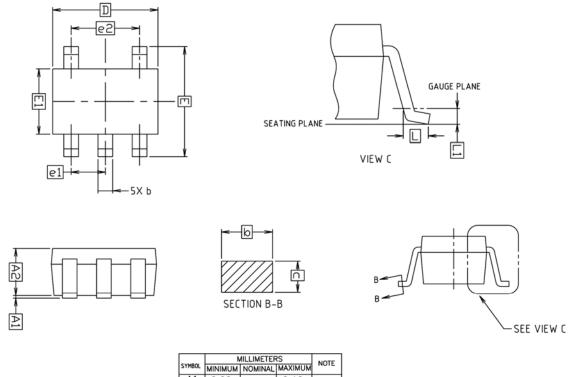


Fig. 3 G_I - I_O

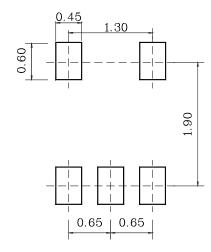


Outline Dimension



	MILLIMETERS			NOTE
SYMBOL	MINIMUM	NOMINAL	MAXIMUM	NOIL
A1	0.00	_	0.10	
A2	0.90	0.95	1.00	
Ь	0.25	-	0.40	
С	0.10	_	0.25	
D	1.90	2.00	2.10	
E	1.95	2.10	2.25	
E1	1.15	1.25	1.35	
e1	0.65 BSC			
e2	1.30 BSC			
L	0.25	_	_	
L1	0.15 BSC			

* Recommend PCB solder land [Unit: mm]



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